

FIG. 1

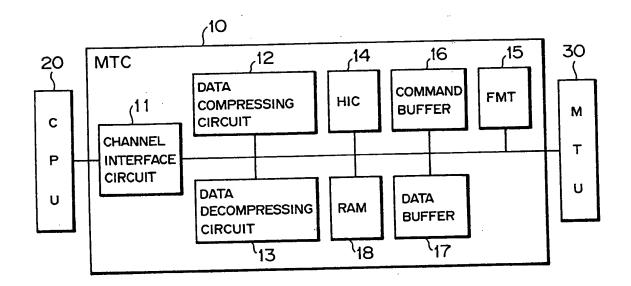


FIG. 2(A)

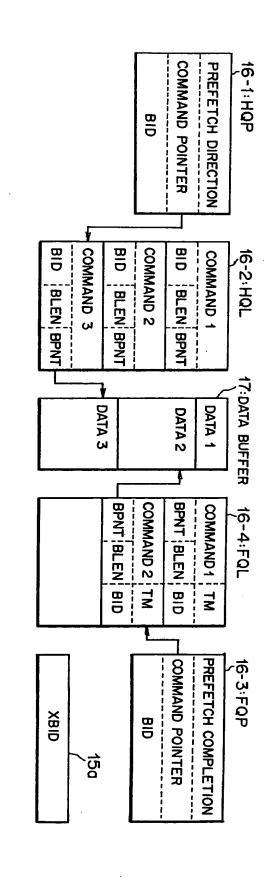


FIG. 2(B)

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SDT 1

SLN 1

SDT 2

SLN2

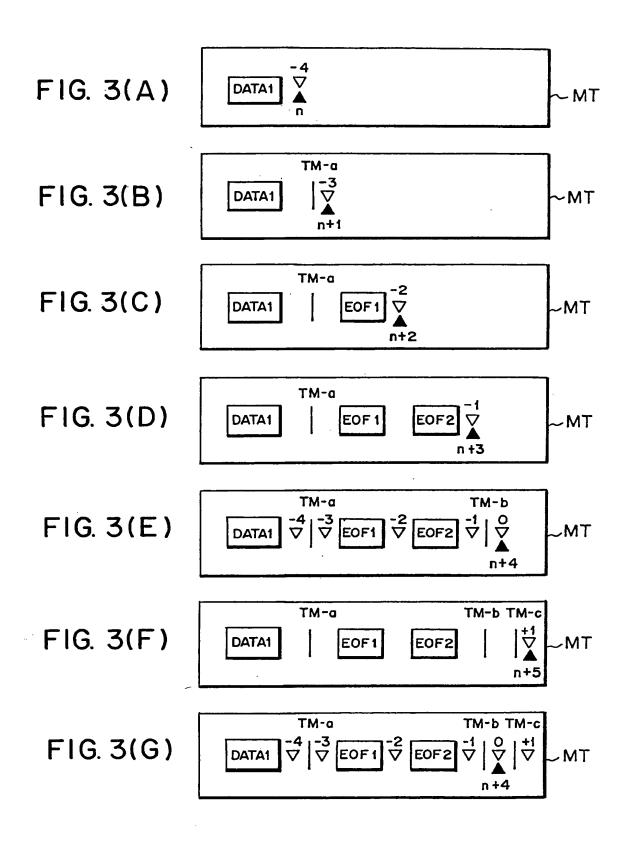
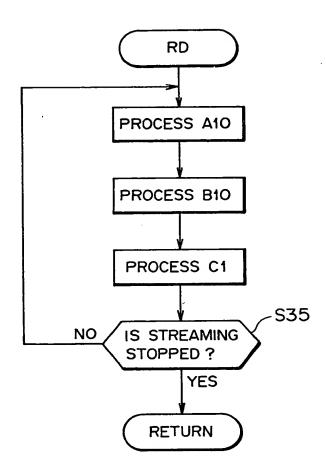


FIG. 4



F1G. 5

			OFCC	A10		$\neg \top$					PR	OCE	SS	B10				PRO	FSS
רוסמכ	TIONS	R	E-	OPERATION		ATE	CONDITION	K I	ATE	UPDATI EP		M AG	SAVE	E AREA	PROCESS	BLEN	BID		D
ES	EP	POS	ITION		<u> </u> "	-		+-	1	-3	十	+				0	XBIE		000
,	-4			REAL	хв	10+1	TN	┅╁┅┅	0	-4		0				LEN) ABIL	1	001
0	-4	_		READ	╁-		OTHERS EOF		2	-2	┰		SDT	}←BUF	SLNI LEN	LEN		1	100
				REAL			******		<u></u> 1	-3		1				0	XBI	1	101
1	-3	1		READ	XB	ID+1	TM		. <u></u> 0	-4		0			-	LEN		1	102
		1		ļ	-		OTHERS	<u>`</u>	3	-1	-	0	SDT	2+-BUF	:SLN2+-LEN	LEN			200
			•	REAL	_v_	31D+1	EOF TM		<u></u>	-3		1				0	XBI	D 1	201
2	-2		••	READ	^-	ויטונ	OTHER	=	0	-4		0			-	LEN		—	202
	<u> </u>	\bot			+		TM	+	4	1		1				0	хв		1300
3	-1	1		REAL	x	BID+1		<u>-</u>	0		4	0	 			LEN	1		1301
		4			_			-	4	 -	3	1	T		••	0		.	1400
		4		EMULAT				 -	<u>.</u>	-	2	0		BUF-	-SDT1:	SLN		+ }	1410
	-	3	 	EMULA		 			<u>.</u> 4		1	0	1	BUF-	-SDT2;	SLN	2 E		1420
	-	2	 	EWILL.					<u>:</u> 4		0	1	-	***********		0			1430
4	-	1		EMULA	!E -		EOF	: 	<u>:</u> 2	-	-2	0	SI	DT1+BU	:SLN1←L	EN			1440
				REAL	.	XB I D+		t	<u></u> 5		+1	1	<u> </u>				X	3ID	1441
		0		REAL		יטופא	OTHE		0		 -4	0		***********			· _		1442
_	_						EO		2	+	-2	0	s	DT1 ←BL	F;SLN1+-L	EN -	-		150
				REA	.	XB I D-			1		-3	1				-	- X	BID	150
!	5	+1		REA		יפוש	отн		0	····	4	0				-			150
_	_			EMUL	ATE		-		-	;	-3	1			••		0		160
		-4					-		1 6	5	-2	(0	BU	F-SDT1:	SL	N1	ŒID	161
	-	-3							1	6	-1		0	BU	F←SDT2;		.N2	+ EP	162
	\ .	-2			•••••				+	6	0		1				0	_	163
	6	-1			••••••					6	+1		1				0		16
		0						г м		1	-3		1				0	XBID	16
	-	+1	XB +E		AL AD	XBII	0+1	HERS		0	-4	-	0			1	EN		16

FIG. 6

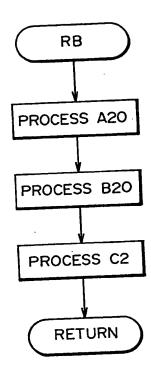


FIG. 7

		PRO	CESS	5 A20						PF	OC	ESS	B20			PRO	CESS
ONDI	TIONS		RE-	OPERATION	UPDATE XBID	CONDI	TIONS	UPDATE ES	UPDA EP		NI LAG	SAVE	AREA PROCESS	BLEN	ВП	<u> </u>	ID
ES	EP	Pas	SITION			-		0	-4	+	ቨ	-	••	0	хв		2000
_	-4			REAL	XBID-1	TI	*******	0			0			0	1		2001
0	-4	_		RB		 	ERS	0	+-	-	1	\vdash	••	0	хв		2100
1	-3			REAL RB	XBID-1		riii Hers	0	-		0	·		0			2101
<u>'</u>	L	_		NO NO	 		TM	0	┼	4	1	1		0	x		2200
2	-2			REAL RB	XBID-	ı	HERS	0		4	0			0			2201
				, nb	 		TN	10	+-	4	1	1		0	XI	310	2300
3	-1			REAL RB	XBID-	1	HERS	-		-4	0	-		0			2301
		4		1.0			TM	10	+	4	1	1		0	x	BID	2400
	_,	4	XBID + EP	REAL	XBID-	.1	THERS	0		 -4	0			0			2401
		-	+ EF	EMULATI	E			-		-4	1			0		BID EP	2410
4		3		EMULAT	_			-	-	3	0)	~~	C	1	XBID	2420
		-2								 -2		0				(BID + EP	2430
		-1		EMULA	E											XBID	2440
	-	0		EMULA	ΤΕ -				-	-1	1	1			-	VOID	2500
+	_			REAL	IADI	D-1			6	0		1			0	XBID	
L	5	+1	ļ	RB		\dashv	TM		0	-4	1	1			0	XBID	2600
		-4	XBI + E		1 1 1 1	D-1	OTHER	is	0	-4		0			0		2601
		-3	-							-4		1			0	XBII	
		-2		- EMUL	ATE					-:	- 1	0			0	ХВІ	262
	6			EMUL						-	2	0	***************************************		0	+ E	P 263
1		-1								·}	-	<u> </u>			0	ХВ	ID 264
		0			LATE							1			0	+ 1	
		+	i T	EMU	LATE						0	<u> </u>					

FIG. 8

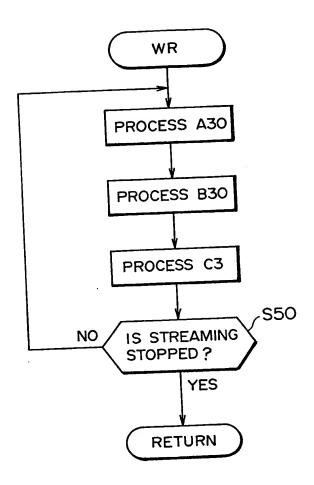


FIG. 9

		PROCES	S A30					PROC	ESS B30			
	TIONS	RE- POSITION	OPERATION	UPDATE XBID	CONDITIONS	UPDATE Es	update ep	TM FLAG	SAVE AREA PROCESS	BLEN	BID	PROCESS ID
ES	EP	rosinia		AD10							_	
0	-4		REAL WR	XBID+1		0	-4		••	LEN	XBID	3000
	•		REAL	XBID+1	EOF	2	-2		SDT1-BUF;SLN1-LEN	LEN	XBID	3100
1	-3		WR	ודעופג	OTHERS	0	-4			LEN	7.5.5	3101
	•		REAL	VD I D. 1	E0F	3	-1		SDT2-BUF;SLN2-LEN	LEN	XBID	3200
2	-2		WR	XBID+1	OTHERS	0	-4			LEN	2010	3201
3	-1		REAL WR	XBID+1	••	0	-4			LEN	XBID	3300
	-4		REAL WR	XBID+1		0	-4			LEN	XBID	3400
	_		REAL		EOF	2	-2		SDT1←BUF ;SLN1←LEN	LEN	XBID	3410
	-3	XBID	WR	XBID+1	OTHERS	0	-4			LEN	7810	3411
	_	+ EP	REAL		EOF	3	-1		SDT2+BUF;SLN2+LEN	LEN	ABIU	3420
4	-2		WR	XBID+1	OTHERS	0	-4			LEN	XBID	3421
	-1		REAL WR	XBID+1		0	-4	- -		LEN	XBID	3430
		•••••	REAL.	VD 15 . 4	EOF	2	-2		SDT1←BUF;SLN1←LEN	LEN	XBID	3140
	0		WR	XBID+1	OTHERS	0	-4			LEN	7510	3141
			REAL		EOF	2	-2		SOT1 ←BUF ;SLN1 ←LEN	LEN	XBID	3500
5	+1		WR	XBID+1	OTHERS	0	-4		·	LEN	2510	3501
	-4		REAL WR	XB1D+1		0	-4	-	••	LEN	XBID	3600
	_		REAL		EOF	2	-2		SDT1←BUF;SLN1←LEN	LEN	XBID	3610
	-3	XBID	WR	XBID+1	OTHERS	0	-4			LEN	7810	3611
		+ EP	REAL		EOF	3	-1		SDT2-BUF :SLN2-LEN	LEN	XBID	3620
	-2		WR	XBID+1	OTHERS	0	-4		••	LEN		3621
6	-1		REAL WR	XBID+1		0	-4			LEN	XBID	3630
			REAL		EOF	2	-2		SDT1-BUF ;SLN1-LEN	LEN	VDID	3640
	0		WR	XBID+1	OTHERS	0	-4			LEN	XBID	3641
		XBID	REAL	VD I D	EOF	2	-2		SDT1-BUF;SLN1-LEN	LEN		3650
	+1	+ EP	WR	XBID+1	OTHERS	0	-4			LEN	עומא	3651

FIG. 10

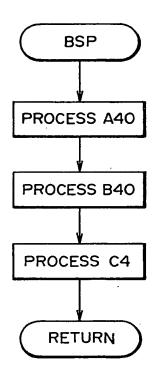


FIG. 11

	-	PROCES	S A40	•		-		PROC	ESS B40			
CONDI	TIONS	RE- POSITION	OPERATION	UPDATE XBID	CONDITIONS	UPDATE ES	UPDATE EP	TN FLAG	SAVE AREA PROCESS	BLEN	BID	PROCESS ID
ES	EP	POSTTION		ABID		E3		r LAG				
0	4		REAL BSP	XBID-1		0	-4	0		0	XBID	4000
1	-3		REAL BSP	XBID-1		0	-4	0	••	0	XBID	4100
2	-2		REAL BSP	XBID-1		0	-4	0	••	0	XBID	4200
3	-1		REAL BSP	XBID-1		0	-4	0		0	XBID	4300
	-4	XBID + EP	REAL BSP	XBID-1	••	0	-4	0		0	XBID	4400
١. ا	-3	~-	EMULATE			4	-4	1		0		4410
4	-2		EMULATE			4	-3	0		0	XBID	4420
	-1		EMULATE			4	-2	0		0	+ EP	4430
	0		EMULATE	į		4	-1	1	••	0		4440
5	+1		REAL BSP	XBID-1	••	6	0	1	••	0	XBID	4500
	-4	XBID + EP	REAL BSP	XBID-1		0	-4	0		0	XBID	4600
	-3		EMULATE			4	-4	1		0		4610
6	-2		EMULATE			4	-3	0		0		4620
.	-1		EMULATE			4	-2	0		0	XBID + EP	4630
	0		EMULATE			4	-1	1		0		4640
	+1		EMULATE			4	0	1		.0		4650

FIG.12

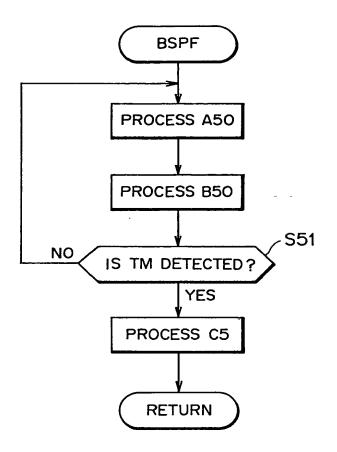
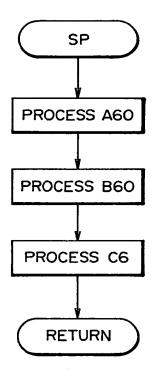


FIG. 13

		PROCES	S A50			·		PROC	ESS B50			
CONDI		RE- POSITION	OPERATION	UPDATE XBID	CONDITIONS	update es	UPDATE EP	TM FLAG	SAVE AREA PROCESS	BLEN	BID	PROCESS ID
ES	EP		PC41									
0	-4		REAL BACK SPACE	XBID-1		0	-4		••		XBID	5000
1	-3		REAL BACK SPACE	XBID-1		0	-4				XBID	5100
2	-2	. <u></u>	REAL BACK SPACE	XBID-1		0	-4				XBID	5200
3	-1		REAL BACK SPACE	XBID-1		0	-4		, 		XBID	5300
	-4	XBID + EP	REAL BACK SPACE	XBID-1		0	-4				XBID	5400
	-3		EMULATE			4	-4					5410
4	-2		EMULATE			4	-3				XBID	5420
	-1		EMULATE			4	-2				+ EP	5430
	0		EMULATE			4	-1			ļ		5440
5	+1		REAL BACK SPACE	XBID-1		6	0				XBID	5500
	-4	XBID + EP	REAL BACK SPACE	XBID-1		0	-4				XBID	5600
	-3		EMULATE			6	-4					5610
6	-2		EMULATE			6	-3					5620
	-1		EMULATE			6	-2				XBID + EP	5630
	0		EMULATE			6	-1					5640
	+1		EMULATE			6	0					5650

FIG. 14



F I G. 15

<u> </u>	1	PROCES	S A60				ı	PROC	ESS B60			
CONDI	TIONS EP	RE- POSITION	OPERATION	UPDATE XBID	CONDITIONS	UPDATE Es	update ep	TN FLAG	SAVE AREA PROCESS	BLEN	BID	PROCESS ID
F					TIM	1	-3	1				6000
0	-4		REAL READ	XBID+1	OTHERS	0	-4	0			XBID	6001
			. — —		EOF	2	-2	0	SDT1-BUF :SLN1-LEN			6100
1	-3		REAL READ	XBID+1	TH	1	-3	1	**************************************		XBID	6101
'			READ		OTHERS	0.	-4	0				6102
					EOF	3	-1	0	SDT2-BUF ;SLN2-LEN			6200
2	-2		REAL READ	XB1D+1	TM	1	-3	1			XBID	6201
			MEAD		OTHERS	0	-4	0				6202
			REAL		TM	4	0	1			VOLD	6300
3	-1		READ	XBID+1	OTHERS	0	-4	0			XBID	6301
	-4		EMULATE			4	-3	1				6400
	-3		EMULATE			4	-2	0			XBID +	6410
1	-2		EMULATE			4	-1	0			EP	6420
4	-1		EMULATE			4	0	1				6430
					EOF	2	-2	0	SDT1+BUF;SLN1+LEN			6440
	0		REAL READ	XBID+1	TM	5	+1	1			XBID	6441
			1.2.0		OTHERS	0	-4	0				6442
 					EOF	2	-2	0	SOT1 BUF ; SLN1 LEN			6500
5	+1		REAL READ	XB1D+1	TM	1	-3	1			XBID	6501
İ			, new		OTHERS	0	-4	0				6502
	-4		EMULATE			6	-3	1				6600
	-3		EMULATE			6	-2	0			XBID	6610
	-2		EMULATE			6	-1	0			+ EP	6620
6	-1		EMULATE			6	0	1				6630
	0		EMULATE			6	+1	1			<u> </u>	6640
1		XBID	REAL	V010.4	TM	1	-3	1			XBID	6650
	+1	+EP	READ	XBID+1	OTHERS	0	-4	0			را م	6651

FIG. 16

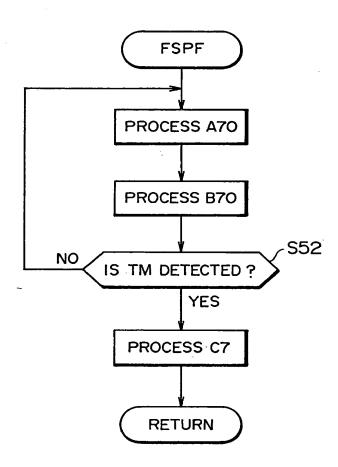
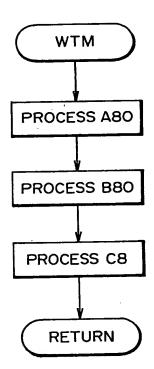


FIG. 17

		PROCES	S A70				<u>-</u>	PROC	ESS B70			
COND!		RE- POSITION	OPERATION	UPDATE XBID	CONDITIONS	UPDATE ES	UPDATE EP	TN FLAG	SAVE AREA PROCESS	BLEN	BID	PROCESS ID
			25.11		TM	1	-3	1	••		XBID	7000
0	-4		REAL READ	XBID+1	OTHERS	0	-4	0	**************************************		עופא	7001
					EOF	2	-2	0	SDT1 ←BUF ;SLN1 ←LEN			7100
1	-3	<u></u>	REAL	XB1D+1	TN	1	-3	1	==		XBID	7101
'	ľ		READ		OTHERS	0	-4	0				7102
	-				EOF	3	-1	0	SOT2-BUF; SLN2-LEN			7200
2	-2		REAL.	XBID+1	TN	1	-3	1			XBID	7201
-	_		READ		OTHERS	O	-4	0				7202
-	-		554		TN	4	0	1			XBID	7300
3	-1		REAL READ	XBID+1	OTHERS	0	-4	0			7510	7301
	-4	 	EMULATE			4	-3	1				7400
	-3	<u> </u>	EMULATE			4	-2	0			XBID	7410
	-3 -2		EMULATE			4	-1	0			EP	7420
١,	- <u>-</u> 2		EMULATE			4	0	1				7430
4					EOF	2	-2	0	SDT1-BUF :SLN1-LE			7440
			REAL	XBID+1	TM	5	+1	1	**		XBID	7441
	"		READ		OTHERS	0	-4	0			·	7442
-	-	_	-	+	EOF	2	-2	0	SDT1+BUF ;SLN1+LE	N		7500
5	+1		REAL	XB1D+	1 TM	1	-3	1			XBID	7501
	''		READ		OTHERS	0	-4	0				7502
-	-4	+	EMULATE		••	6	-3	1				7600
	-3		EMULATE			6	-2	0			XBID	7610
	-2		EMULATE			6	-1	0			+	7620
6	-1		EMULATE			6	0	1			EP	7630
0	0		EMULATE			6	+1	1				7640
					TM	1	-3	1			Voir	7650
	+1	XBID +EP	REAL READ	XB1D+		+	-4	0			XBIC	7651

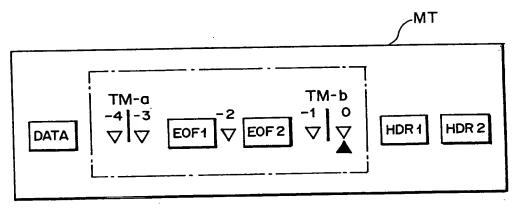
FIG. 18



F I G. 19

		PROCESS	S A80					PRO	CESS B80			
COND1	TIONS	RE-	OPERATION	UPDATE XBID	CONDITIONS	UPDATE ES	UPDATE EP	TM FLAG	SAVE AREA PROCESS	BLEN	BID	PROCESS 1D
ES	EP	POSITION		ABIU		- C3						
0	-4		REAL WTM	XBID+1		1	-3	1		0	XBID	8000
1	-3		REAL WTM	XB1D+1		1	-3	1	•-	0	XBID	8100
2	-2		REAL WITM	XBID+1		1	-3	1		0	XBID	8200
3	-1		REAL WTM	XB1D+1		4	0	1		0	XBID	8300
	-4	XBID + EP	REAL WTM	XBID+1		1	-3	1		0	XBID	8400
	-3	XBID	REAL WTM	XBID+1		1	-3	1		0	XBID	8410
4	-2	+ EP	REAL WTM	XBID+1		1	-3	1		0	XBID	8420
	-1		EMULATE			4	0	1		0	XBID + EP	8430
	0		REAL. WTM	XB1D+1		5	+1	1		0	XBID	8440
5	+1		REAL WTM	XBID+1		1	-3	1		0	XBID	8500
	-4	XBID + EP	REAL WTM	XB1D+1		1	-3	1		0	XBID	8600
	-3	XBID	REAL WTM	XBID+1		1	-3	1		0	XBID	8610
	-2	+ EP	REAL. WTM	XBID+1		1	-3	1		0	XBID	8620
6	-1	XBID + EP	REAL WTM	XBID+1		1	-3	1		0	XBID	8630
	0		EMULATE			6	+1	1		0	XBID + EP	8640
	+1	XBID + EP	REAL WTM	XBID+1		1	-3	1		0	XBID	8650

F I G. 20

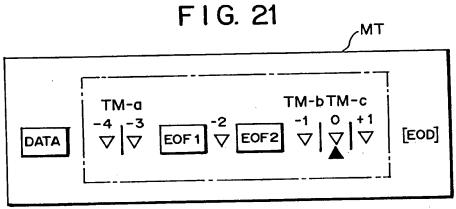


EMULATE IF RB, BPS OR BSPF IS EXECUTED WHEN ES=4 AND EP=0 EMULATE IF RD, RB, BSP, BSPF, SP, FSPF OR WTM IS EXECUTED WHEN ES=4 AND EP=-1

EMULATE IF RD, RB, BSP, BSPF, SP OR FSPF IS EXECUTED WHEN ES = 4 AND EP = -2

EMULATE IF RD, RB, BSP, BSPF, SP OR FSPF IS EXECUTED WHEN ES = 4 AND EP = -3

EMULATE IF RD, SP OR FSPF IS EXECUTED WHEN ES=4 AND EP=-4



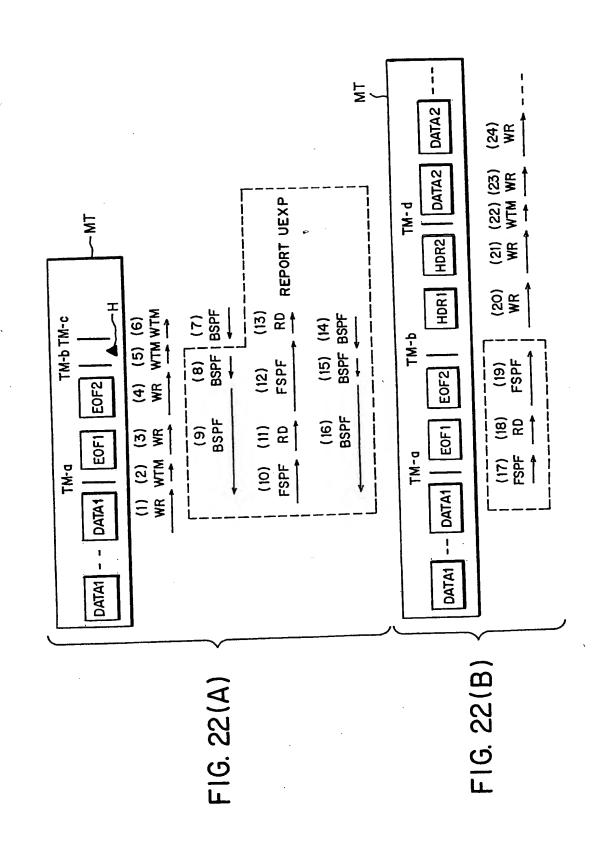
EMULATE IF RB, BSP OR BSPF IS EXECUTED WHEN ES=6 AND EP=+1 EMULATE IF RD, RB, BSP, BSPF, SP FSPF OR WTM IS EXECUTED WHEN ES=6 AND EP=0

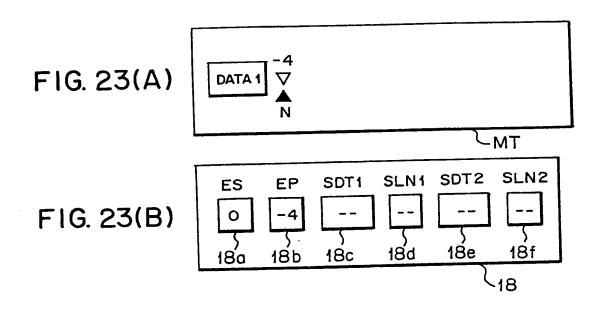
EMULATE IF RD, RB, BSP, BSPF, SP OR FSPF IS EXECUTED WHEN ES = 6 AND EP = -1

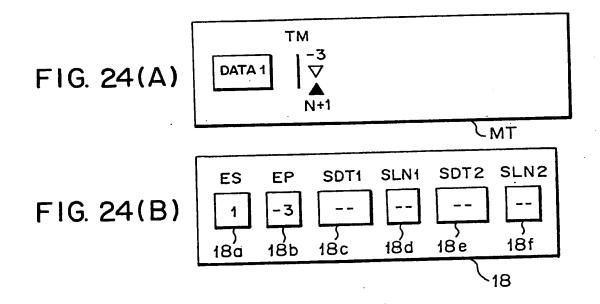
EMULATE IF RD, RB, BSP, BSPF, SP OR FSPF IS EXECUTED WHEN ES=6 AND EP=-2

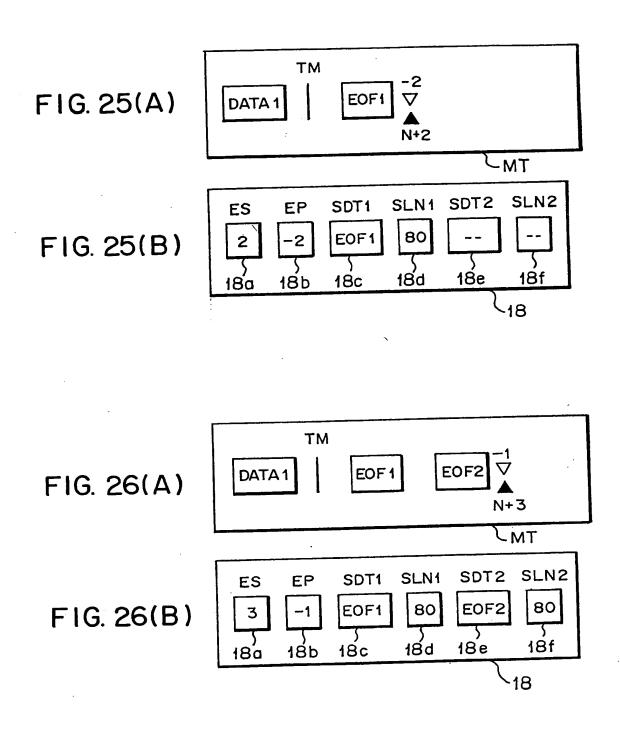
EMULATE IF RD, RB, BSP, BSPF, SP OR FSPF IS EXECUTED WHEN ES=6 AND EP=-3

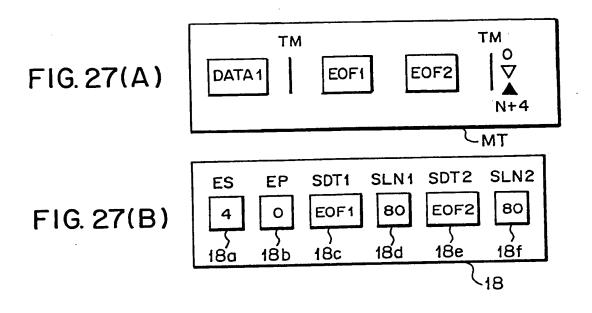
EMULATE IF RD, SP OR FSPF IS EXECUTED WHEN ES=6 AND EP=-4

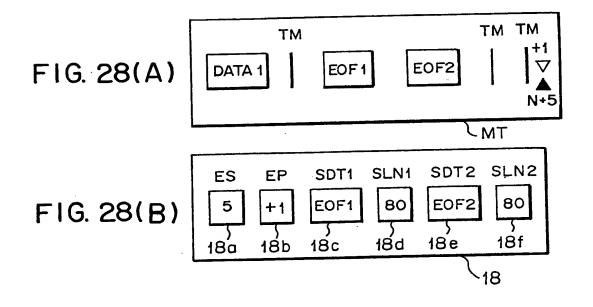


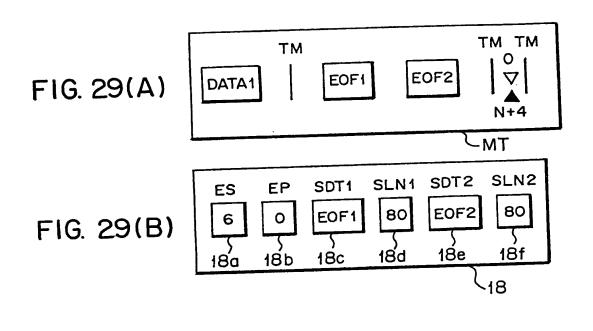


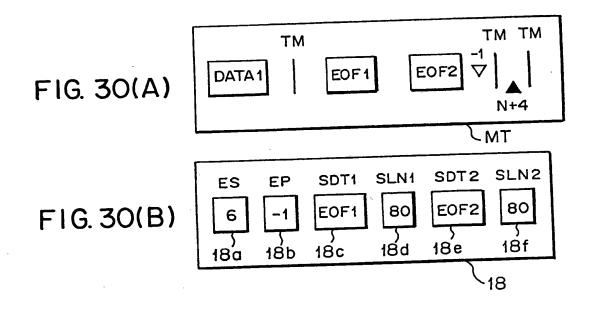


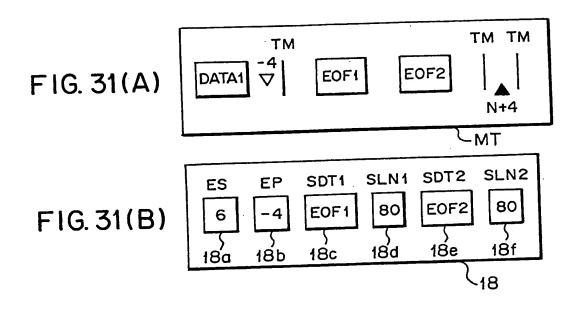


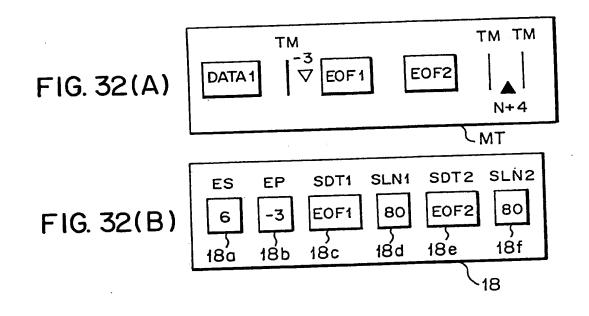


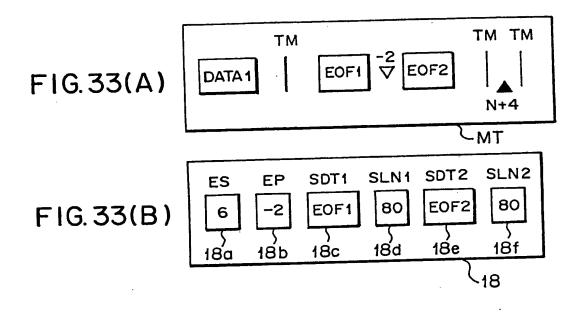


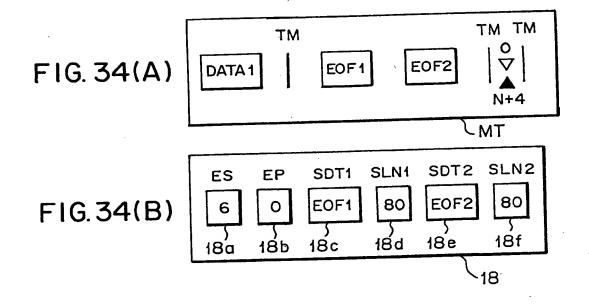


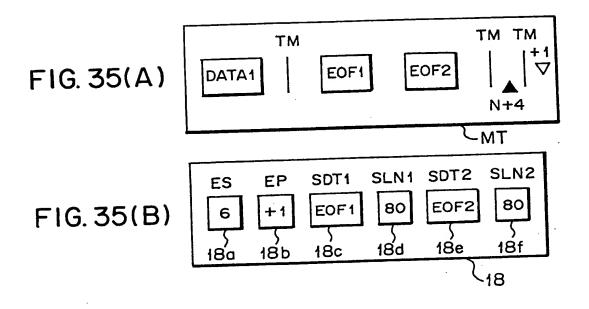


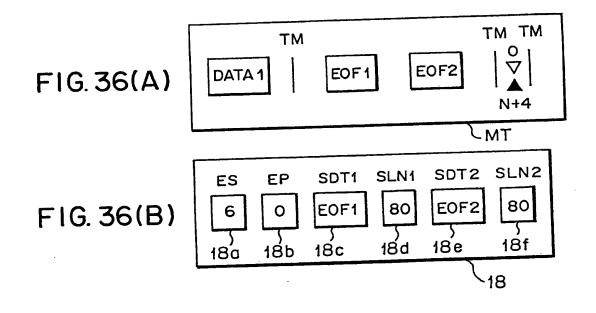


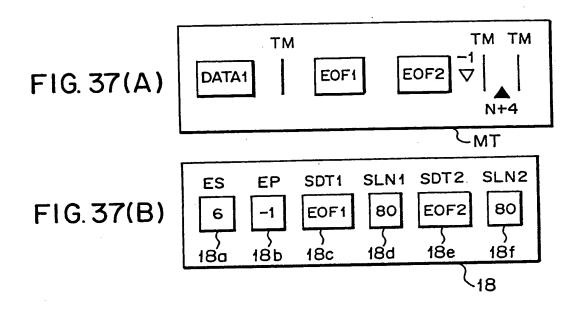


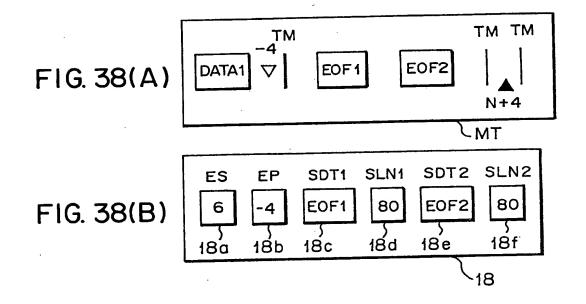


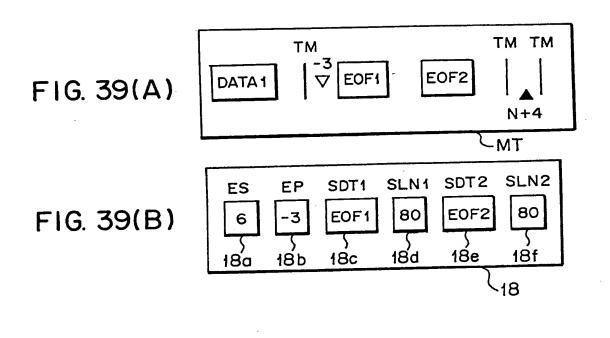


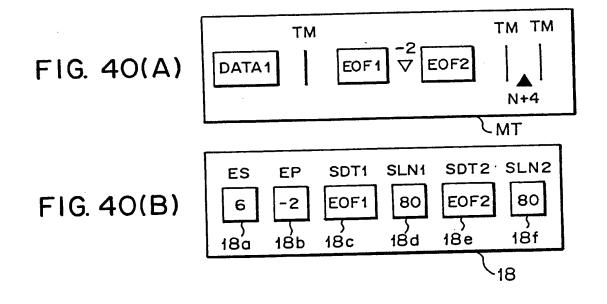












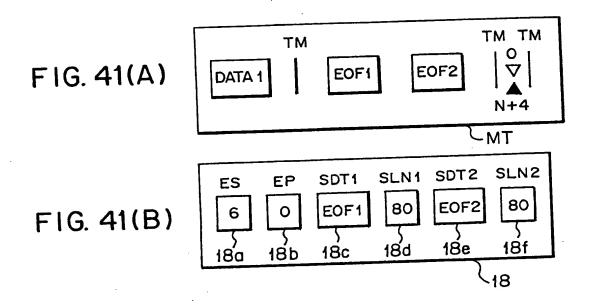


FIG. 42(A)

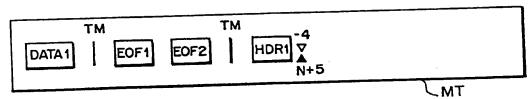


FIG. 42(B)

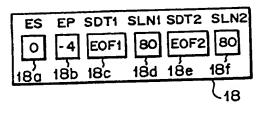


FIG. 43(A)

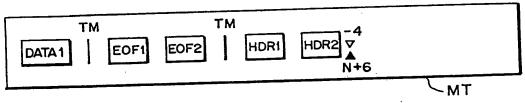


FIG. 43(B)

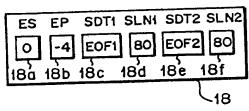


FIG. 44(A)

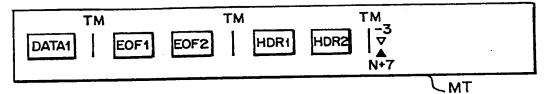


FIG. 44(B)

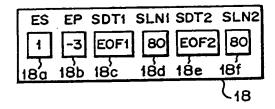


FIG. 45(A)

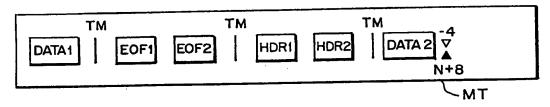


FIG. 45(B)

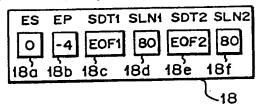


FIG. 46(A)

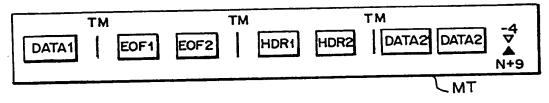
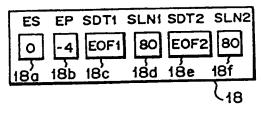


FIG. 46(B)



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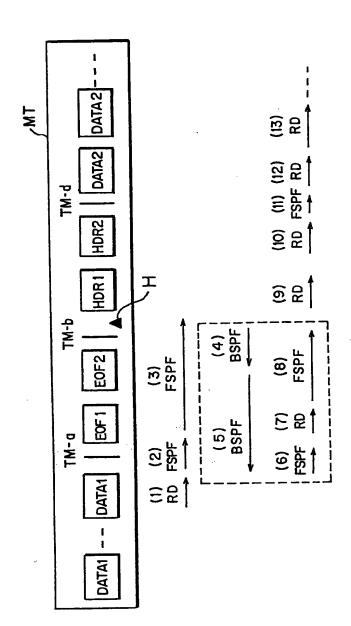


FIG. 48(A)

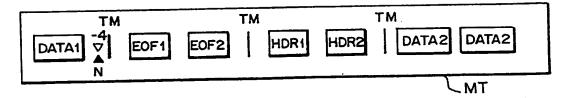


FIG. 48(B)

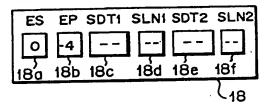


FIG. 49(A)

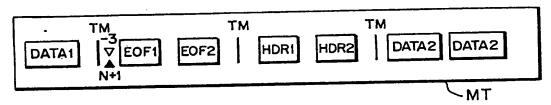


FIG. 49(B)

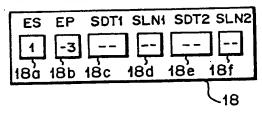


FIG. 50(A)

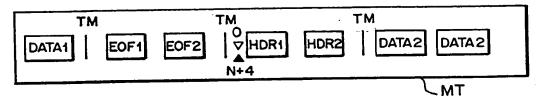


FIG. 50(B)

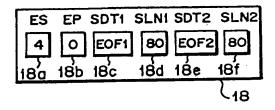


FIG. 51(A)

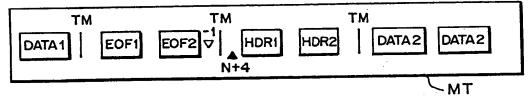


FIG. 51(B)

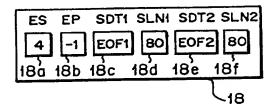


FIG. 52(A)

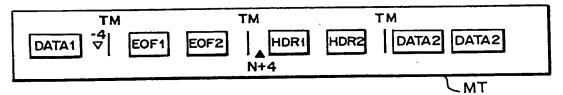


FIG. 52(B)

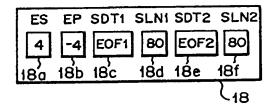


FIG. 53(A)

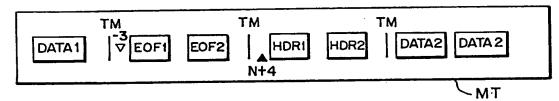


FIG. 53(B)

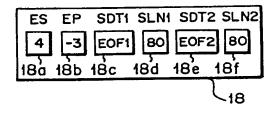


FIG. 54(A)

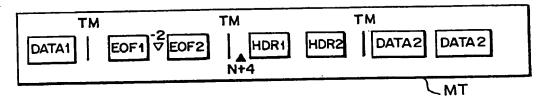


FIG. 54(B)

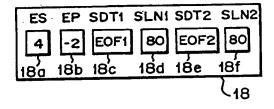


FIG. 55(A)

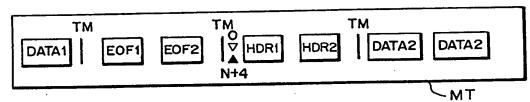


FIG. 55(B)

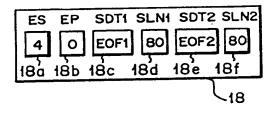


FIG. 56(A)

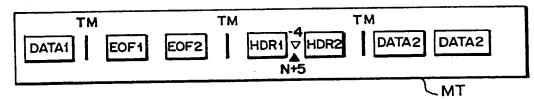


FIG. 56(B)

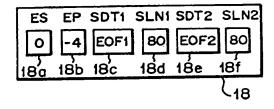


FIG. 57(A)

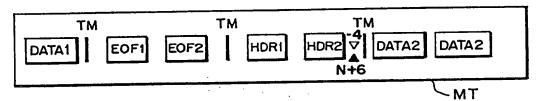


FIG. 57(B)

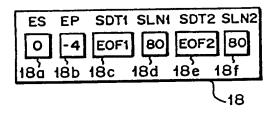


FIG. 58(A)

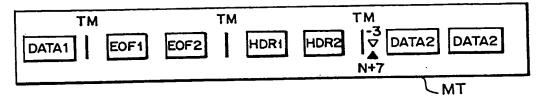


FIG. 58(B)

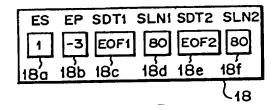


FIG. 59(A)

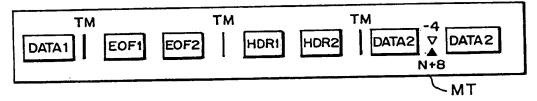


FIG. 59(B)

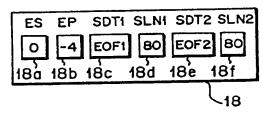


FIG. 60(A)

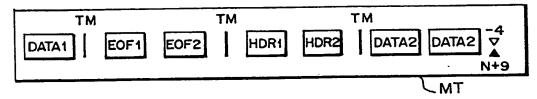
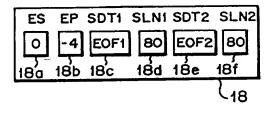
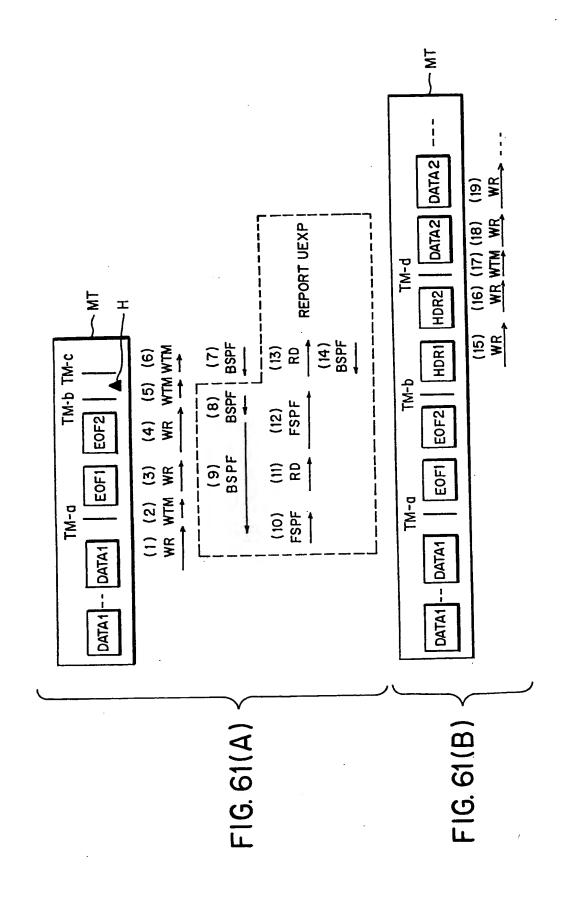


FIG. 60(B)





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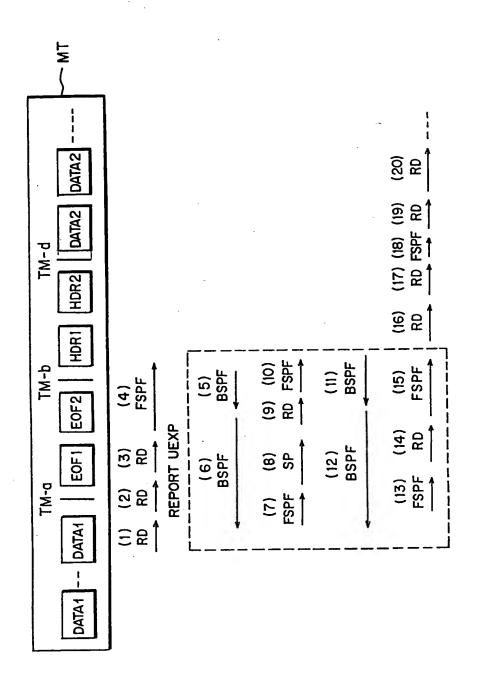


FIG.63

COMMAND	WITHOUT EMULATION	WITH EMULATION	REMARKS
WR	0. 000731	0. 000731	(1)
WTM	0. 713581	0. 713581	(2)
WR	0. 003502	0. 003502	(3)
WR	0. 000641	0. 000641	(4)
WTM	0. 596851	0. 596851	(5)
WTM	0. 298012	0. 298012	(6)
BSPF	0. 000548	0. 000548	(7)
NOP	0. 460880	0. 460880	
LDSP	0. 000521	0. 000521	
LDSP	0. 000469	0. 000469	
LDSP	0. 000493	0. 000493	
NOP	0. 000524	0. 000524	1
SNIO	0. 002945	0. 002945	
RDVC	0. 000580	0. 000580	
NOP	0. 000529	0. 000529	
SNS	0. 002731	0. 002731	
LDSP	0. 000420	0. 000420	ļ
BSPF	0. 000583	0. 000500	(8)
BSPF	0. 291417	0. 000500	(9)
FSPF	0. 291342	0.000500	(10)
RD	0. 531535	0. 000500	(11)
FSPF	1. 410659	0. 000500	(12)
RD	0. 581191	0. 000500	(13)
BSPF	0.000606	0. 000500	(14)
BSPF	0. 455999	0. 000500	(15)
BSPF	0. 293099	0. 000500	(16)
FSPF	0. 290969	0. 000500	(17)
RD	0. 531411	0. 000500	(18)
FSPF	1. 414089	0. 000500	(19)
NOP	0. 290595	0. 000500	
WR	0. 000615	0. 000615	(20)
WR	0.000615	0. 000615	(21)
WTM	0. 639278	0. 639278	(22)
WR	0. 003324	0. 003324	(23)
WR	0. 000641	0. 000641	(24)
TOTAL	9. 111926	2. 734931	SECOND

FIG.64

COMMAND	WITHOUT EMULATION	WITH EMULATION	REMARKS
RD	0. 001561	0. 001561	(1)
FSPF	0. 453726	0. 453726	(2)
FSPF	0. 453545	0. 453545	(3)
NOP	0. 293952	0. 293952	
LDSP	0. 000525	0. 000525	
LDSP	0. 000456	0. 000456	
- LDSP	0. 000492	0. 000492	
NOP	0. 000498	0. 000498	
SNIO	0.002933	0. 002933	
RDVC	0. 000591	0. 000591	
NOP	0. 000495	0. 000495	
SNS	0. 002706	0. 002706	
LDSP	0. 000425	0. 000425	
BSPF	0. 000581	0. 000500	(4)
BSPF	0. 457145	0. 000500	(5)
FSPF	0. 290540	0. 000500	(6)
RD	0. 531764	0. 000500	(7)
FSPF	1. 409729	0. 000500	(8)
RD	0. 369065	0. 369065	(9)
RD	0. 003179	0. 003179	(10)
FSPF	0. 657888	0. 657888	(11)
NOP	0. 454188	0. 454188	
RD	0. 127053	0. 127053	(12)
RD	0. 001793	0. 001793	(13)
TOTAL	5. 514830	2. 827571	SECOND

FIG.65

COMMAND	WITHOUT EMULATION	WITH EMULATION	REMARKS
WR	0. 000700	0. 000700	(1)
WTM	0. 713500	0. 713500	(2)
WR	0. 003500	0. 003500	(3)
WR	0. 000600	0. 000600	(4)
WTM	0. 596800	0. 596800	(5)
WTM	0. 298000	0. 298000	(6)
BSPF	0. 000500	0. 000500	(7)
NOP	0. 460800	0. 460800	
LDSP	0. 000500	0. 000500	
LDSP	0. 000500	0. 000500	
NOP	0. 000500	0. 000500	
NOP	0. 000500	0. 000500	
SNS	0. 002900	0. 002900	
LDSP	0. 000500	0. 000500	
BSPF	0. 000500	0. 000500	(8)
BSPF	0. 291400	0. 000500	(9)
FSPF	0. 291300	0. 000500	(10)
RD	0. 531500	0. 000500	(11)
FSPF	1. 410600	0. 000500	(12)
RD	0. 581100	0. 000500	(13)
BSPF	0. 000600	0. 000500	(14)
LDSP	0. 000500	0. 000500	
WR	0. 000600	0. 000600	(15)
WR	0. 000600	0. 000600	(16)
WTM	0. 639200	0. 639200	(17)
WR	0. 003300	0. 003300	(18)
WR	0. 000600	0. 000600	(19)
TOTAL	5. 831600	2. 728100	SECOND

FIG.66

COMMAND	WITHOUT EMULATION	WITH EMULATION	REMARKS
RD	0. 001500	0. 001500	(1)
RD	0. 581100	0. 581100	(2)
RD	0. 531700	0. 531700	(3)
FSPF	1. 409700	1. 409700	(4)
BSPF	0. 000500	0. 000500	(5)
BSPF	0. 457100	0. 000500	(6)
FSPF	0. 290900	0. 000500	(7)
SP	0. 531400	0. 000500	(8)
RD	0. 531400	0. 000500	(9)
FSPF	0. 141400	0. 000500	(10)
LDSP	0. 000500	0. 000500	
LDSP	0. 000500	0. 000500	
NOP	0. 000500	0. 000500	
LDSP	0. 000500	0. 000500	
BSPF	0. 000500	0. 000500	(11)
BSPF	0. 457100	0. 000500	(12)
FSPF	0. 290500	0. 000500	(13)
RD	0. 531700	0. 000500	(14)
FSPF	1. 409700	0. 000500	(15)
RD	0. 369000	0. 369000	(16)
RD	0. 003100	0. 003100	(17)
LDSP	0. 000500	0. 000500	
FSPF	0. 657800	0. 657800	(18)
NOP	0. 454100	0. 454100	
LDSP	0. 000500	0. 000500	
RD	0. 127000	0. 127000	(19)
RD	0. 001700	0. 001700	(20)
TOTAL	8. 781900	4. 145200	SECOND

FIG. 67

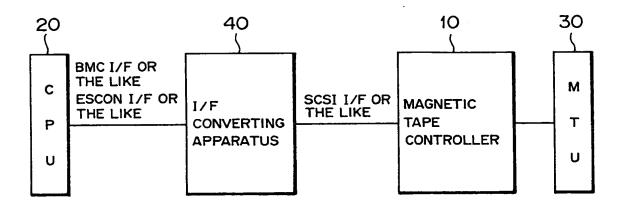
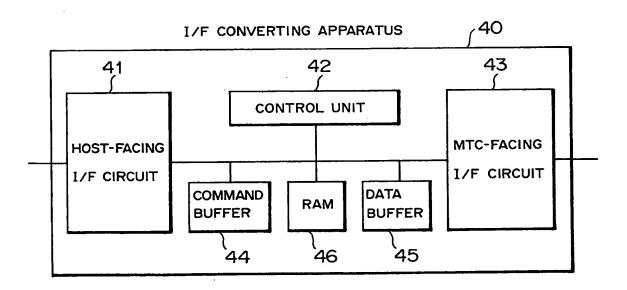
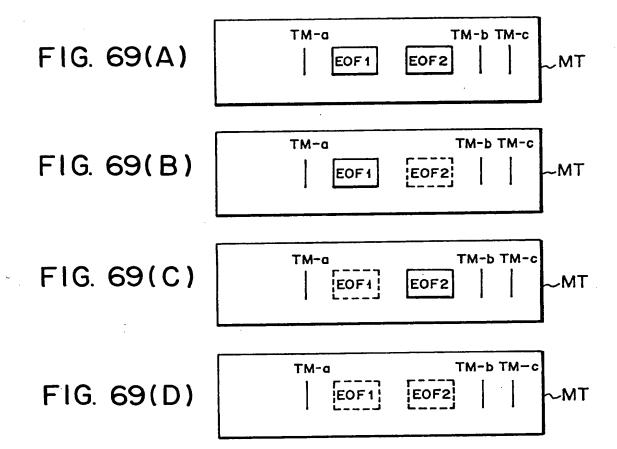
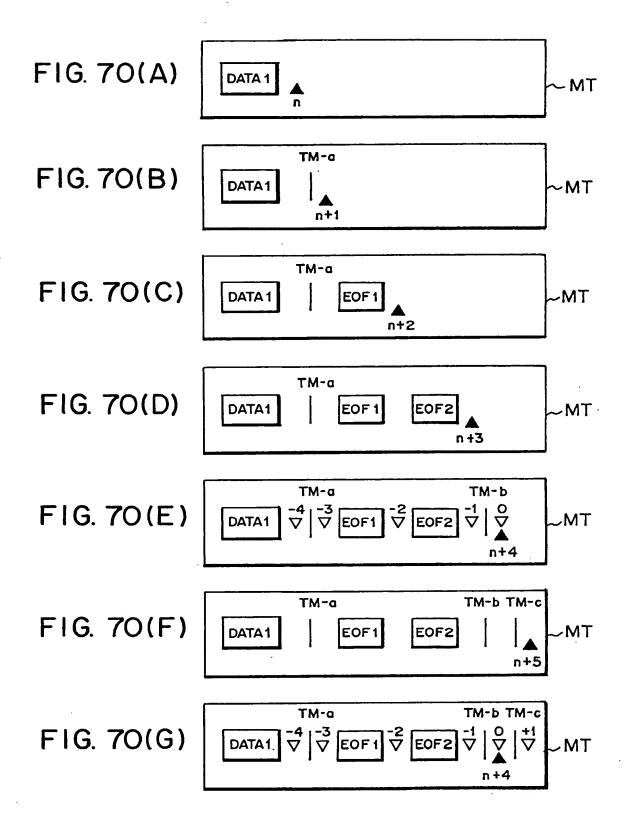
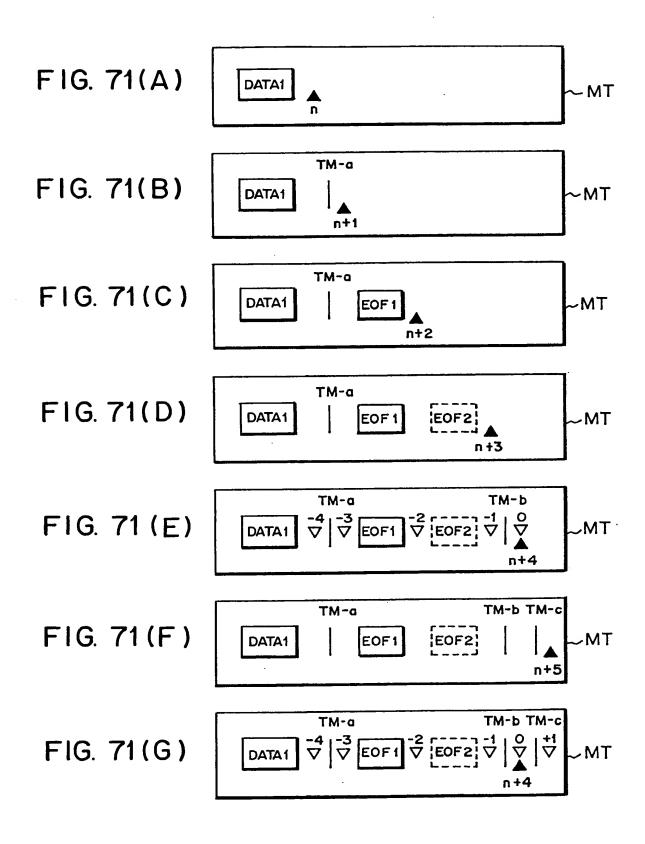


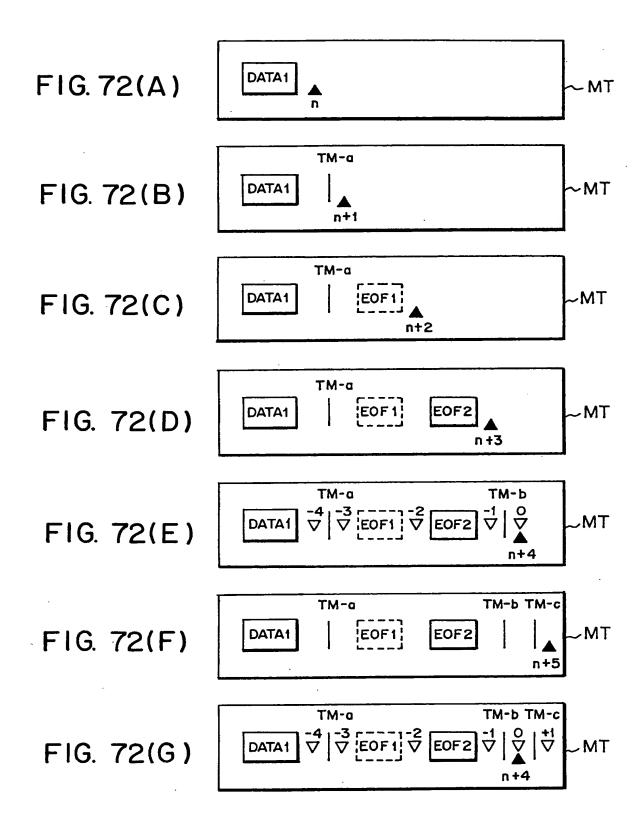
FIG. 68











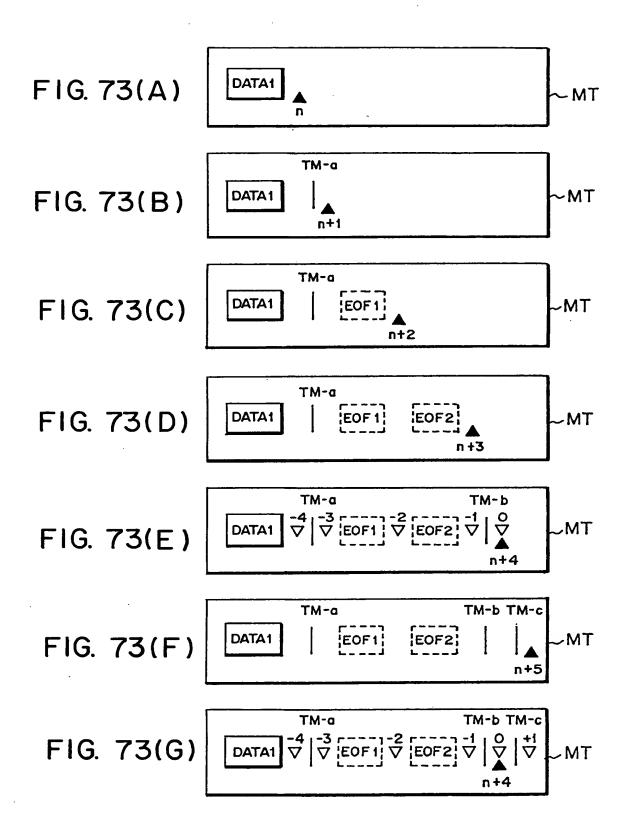


FIG. 74(A)

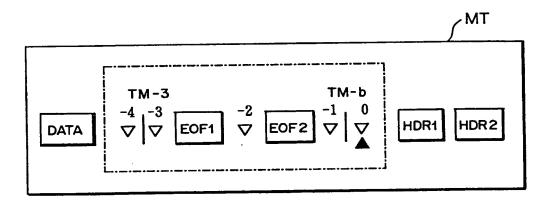


FIG. 74(B)

	RD	RB	BSP	BSPF	SP	FSPF	WTM
0	×	0	0	0	×	×	×
-1	0	0	0	0	0	0	0
-2	0	0	0	0	0	0	×
-3	0	0	0	0	0	0	×
-4	0	×	×	×	0	0	×

(:EMULATION POSSIBLE (INVOLVING DATA TRANSFER)

O:EMULATION POSSIBLE

FIG. 75(A)

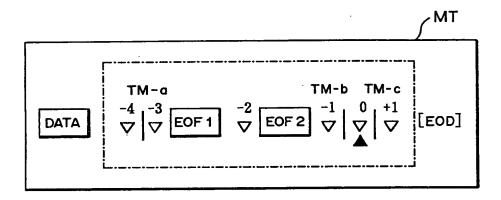


FIG. 75(B)

	RD	RB	BSP	BSPF	SP	FSPF	WTM
+1	×	0	0	0	×	×	×
0	0	0	0	0	0	0	0
-1	0	0	0	0	0	0	×
-2	0	0	0	0	0	0	×
-3	0	0	0	0	0	0	×
-4	0	×	×	×	0	0	×

(: EMULATION POSSIBLE (INVOLVING DATA TRANSFER)

O: EMULATION POSSIBLE

FIG. 76(A)

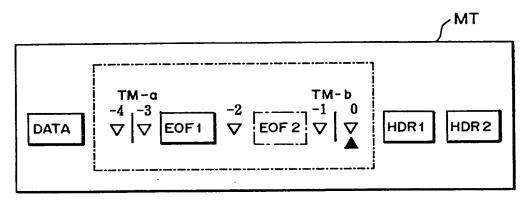


FIG. 76(B)

	RD	RB	BSP	BSPF	SP	FSPF	WTM
0	×	0	0	0	×	×	×
-1	0	Ó	0	0	0	0	0
-2	×	0	0	0	0	0	×
-3	0	0	0	0	0	0	×
-4	0	×	×	×	0	0	×

(INVOLVING DATA TRANSFER)

O: EMULATION POSSIBLE

FIG. 77(A)

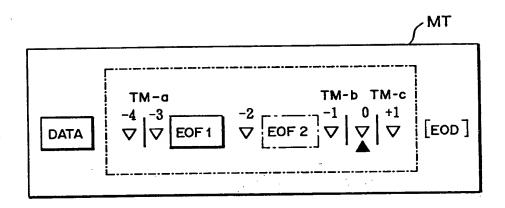


FIG. 77(B)

	RD	RB	BSP	BSPF	SP	FSPF	WTM
+1	×	0	0	0	×	×	×
0	0	0	0	0	0	0	0
-1	0	0	0	0	0	0	×
-2	×	0	0	0	0	0	×
-3	0	0	0	0	0	0	×
-4	0	×	×	×	0	0	×

(: EMULATION POSSIBLE (INVOLVING DATA TRANSFER)

: EMULATION POSSIBLE

FIG. 78(A)

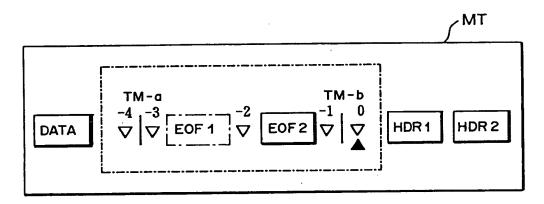


FIG. 78(B)

	RD	RB	BSP	BSPF	SP	FSPF	WTM
0	×	0	0	0	×	×	×
-1	0	0	0	0	0	0	0
-2	0	0	0	0	0	0	×
-3	×	0	0	0	0	0	×
-4	0	×	×	×	0	0	×

**O: EMULATION POSSIBLE (INVOLVING DATA TRANSFER)

O: EMULATION POSSIBLE

FIG. 79(A)

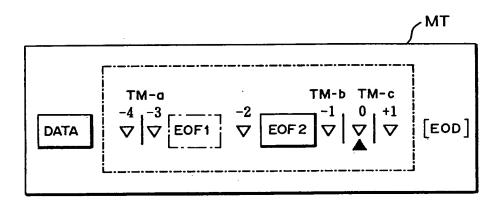


FIG. 79(B)

	RD	RB	BSP	BSPF	SP	FSPF	WTM
+1	×	0	0	0	×	×	×
0	0	0	0	0	0	0	0
-1	0	0	0	0	0	0	×
-2	0	0	0	0	0	0	×
-3	×	0	0	0	0	0	×
-4	0	×	×	×	0	0	×

(INVOLVING DATA TRANSFER)

O: EMULATION POSSIBLE

FIG. 80(A)

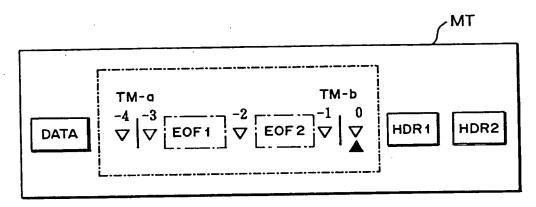


FIG. 80(B)

	RD	RB	BSP	BSPF	SP	FSPF	WTM
0	×	0	0	0	×	×	×
-1	0	0	0	0	0	0	0
-2	×	0	0	0	0	0	×
-3	×	0	0	0	0	0	×
-4	0	×	×	×	0	0.	×

O: EMULATION POSSIBLE

FIG. 81(A)

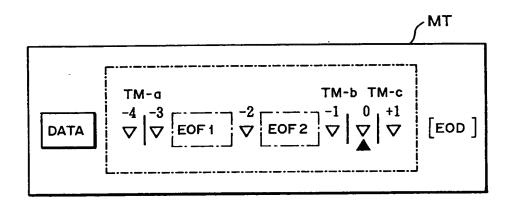


FIG. 81(B)

	RD	RB	BSP	BSPF	SP	FSPF	WTM
+1	×	0	0	0	×	×	×
0	0	0	0	0	0	0	0
-1	0	0	0	0	0	0	×
-2	. x	0	0	0	0	0	×
-3	×	0	0	0	0	0	×
-4	0	×	×	×	0	0	×

O: EMULATION POSSIBLE

FIG. 82

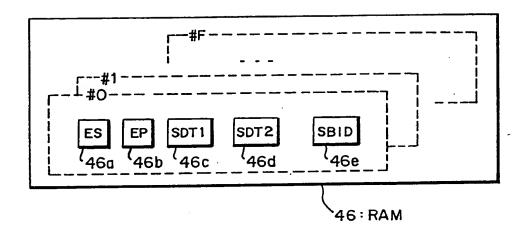


FIG. 83

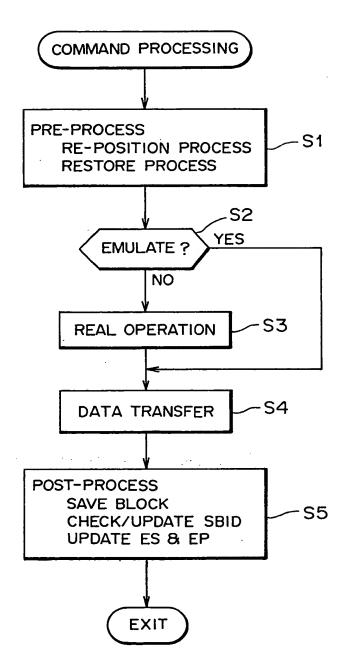


FIG. 84

			DOCE	22	Γ	$\neg \tau$				POST - PROCESS				OCESS
ES	PRE	F	ROCE	RESTOR	OPERA	TION	BLOCK	SAY	VE	BID PROCESS	ES	EP		ID
:3		POS	ITION		-	_	TM	-	. 🕇	SBID+REAL BID	10		1.	00.00
00		1			RE		OTHERS	-	-	# #	00	<u></u>	1.	00. 01
	_	-			+		EOF	SD	T1		20		1.	10.00
					RE		TH	<u> </u>	-	SBID-REAL BID	10			10.01
10					RE	AD	OTHERS				00	<u> </u>	4-	. 10. 02
	-	-					EOF	SI)T2		30		1.	. 20. 00
						EAL	TN	†		SBID-REAL BID	10	<u> </u>		. 20. 01
20	-			,	R	EAD	OTHER	s			00	<u> -</u>	1	. 20. 02
	╁	+		+-	R	 EAL	TM	†		SBID+REAL BID WHEN (REAL BID)-(SBID)=3	40			30.00
30	-	-				EAD	OTHER	s			00	<u> -</u>	-1-	1. 30. 01
-	+	+		ENGLATE			40) -		1. 40. 00				
	}	4		SDT	1 EW	ULATE		-		##	4) -	<u> </u>	1. 40. 10
		3				ULATE		·-†''			4	0 -	.	1. 40. 20
	}	2			EMULATE			4	0		1. 40. 30			
4	0 .	-1		ENULATE EOF SDT1		###	2	0		1. 40. 40				
						REAL READ	TIM			SBID←REAL BID WHEN (REAL BID)-(SBID)=1	. 5	0		1. 40. 41
		1		1		INCAD	OTHE	RS			19	00	_	1. 40. 42
-	-	\dashv					EO	F	SDT1			20		1. 50. 00
1.	_ \			١.		REAL	ח			SBID-REAL BID	}-	10		1. 50. 0
1	50	-		ļ.		READ	отн	RS			_	00		1. 50. 0
-		-4				MULAT	E -	-		##	∤·	60	-3	1. 60. 0
		-3		SI	OT1 E	EMULAT	TE					60	-2	1. 60. 1
		-2				EMULAT	E -	-		***************************************		60	-1 	1. 60. 2
	-1 ENLATE		E -	-						1. 60. 3				
١	60 -1 EBULATE		TE .				}	60	+1	1. 60. 4				
		} <u>.</u>				*****	E	0F	SDT	SBID+1		20		1. 60.
		+1	SBID	+EP		REAL REAL		LM		SBID+REAL BID		10		1. 60.
١		''		_	1	LIEAL		HERS				.00		1. 60.

FIG. 85

	PRF	- PROCE	ss			· · ·	POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
		rwinat		REAL	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)=3	41	0	1. 31. 00
31				READ	OTHERS		0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	00		1. 31. 01
\vdash	-4			EMULATE			## ###################################	41	-3	1. 41. 00
ļ	-3		SDT1	EMULATE			##	41	-2	1. 41. 10
				REAL	EOF	SDT2	SBID-3	30		1. 41. 20
	-2	SBID+EP		READ	OTHERS		***	00		1.41.21
41	-1			EMULATE				41	0	1. 41. 30
					EOF	SDT1	***************************************	20		1.41.40
	0			REAL READ	TM		SBID⊷REAL BID WHEN (REAL BID)-(SBID)=1	51		1. 41. 41
				1.2.2	OTHERS			00		1.41.42
-	╁		+	-	EOF	SDT1	••	20		1. 51. 00
51				REAL	TM	<u> </u>	SBID-REAL BID	10		1. 51. 01
1 31	-			READ	OTHERS			00	-	1. 51. 02
-	-A		 	EMULATE				61	-3	1.61.00
	-3		SDT1	EMULATE				61	-2	1.61.10
	-3			REAL	EOF	SDT2	\$B1D-3	30		1. 61. 20
	-2	SB1D+EF	·	READ	OTHERS			00		1. 61. 21
	 1			EMULATE			P	61	(1. 61. 30
61	1 -1			EMULATE				61	+	1. 61. 40
	ļ				EOF	SDT1	S81D+1	2) -	1. 61. 50
	+1	SBID+E		REAL	TM		SBID-REAL BID	11) -	- 1. 61. 51
	*	SOLUTE		READ	OTHER	s		Ó	0 -	- 1. 61. 52

	PRF	- PROCE	SS					POST - PROCESS			PR	OCESS
s	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE		BID PROCESS	ES	EP	_	ID
+	_	PUSTITUR			EOF	SDT2			32		1.	22.00
_				REAL	TM			SBID-REAL BID	10		1.	22. 01
22				READ	OTHERS		†		00		1.	. 22. 02
\dashv				REAL	TM		T	SBID-REAL BID WHEN (REAL BID)-(SBID)=3	42	0	1	. 32. 00
32				READ	OTHERS		+	***************************************	00	<u> </u>	1	. 32. 01
	-4	\ \-\-\-\-\-\-\-\-\-\-\-\-\-\-\-\-\-\-\	 	EMULATE					42	-3		. 42. 00
	ļ			REAL	EOF	SDT1		SBID-3	20			. 42. 10
	-3	SBIDHEP		READ	OTHERS			######################################	00			1. 42.11
	-2	·	SDT2	EMULATE		1		### ### ##############################	42	-1	 	1. 42. 20
42	-1			EMULATE					42			1. 42. 30
					EOF	SDT1		###	20	-	- -	1. 42. 40
				REAL	TH			SBID←REAL BID WHEN (REAL BID)-(SBID)=1	52	2 -	-	1. 42. 41
	ľ			nexu	OTHER	s	-	***	0	<u> </u>	1	1. 42. 42
	+-				EOF	SDT	1	g-i-	2	0 -	-	1. 52. 00
52				REAL READ	TM			SBID-REAL BID	1	0		1. 52. 01
32			Ì	READ	OTHER	s			0	0		1. 52. 02
├	╁	4		EMULAT	E						-3	1. 62. 00
				REAL	EOF	SDT	п	SBID-3	2	0		1. 62. 1
	-	3 SBIDH	₽	READ	OTHE	s -	-					1. 62. 1
	-	2	SDT	2 EMULAT	E	-	-	##		52	-1	1. 62. 2
1	}	-1		EMULAT	E	-	-	##		52	0	1. 62. 3
"	-	<u> </u>		EMULA	ΓΕ	-	-	##		62	+1	1.62.4
	-				EOF	SD	Т1	SBID+1		20		1. 62. 5
		+1 SBID+	EP -	REAL REAL	1 15	-	-	SBID-REAL BID		10		1. 62.
				nc/L	OTHE	RS -				00		1. 62.

· · ·	PRE	- PROCE	SS				POST PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	巴	ID
				REAL	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)=3	43	0	1. 33. 00
33				READ	OTHERS	**	***	00		1. 33. 01
	-4			EMULATE			. ••	43	-3	1. 43. 00
	_			REAL	EOF	SDT1	SBID-3	20		1. 43. 10
	-3	SBID+EP		READ	OTHERS			00		1. 43. 11
				REAL.	EOF	SDT2	\$81D-3	32		1. 43. 20
43	-2	SBIDHEP		READ	OTHERS	2-	##	00		1. 43. 21
	-1			EMULATE			**	43	0	1. 43. 30
				***********	EOF	SDT1	~~	20		1. 43. 40
	0			REAL READ	TN		SBID←REAL BID WHEN (REAL BID)-(SBID)=1	53		1. 43. 41
					OTHERS			00		1. 43. 42
					EOF	SDT1		20		1. 53. 00
53				REAL READ	TM		SBID←REAL BID	10		1. 53. 01
					OTHERS		~~~	00		1. 53. 02
	-4			EMULATE				63	-3	1. 63. 00
Ì				REAL	EOF	SDT1	SB1D-3	20		1. 63. 10
	-3	SBIDHEP		READ	OTHERS			00		1. 63. 11
				REAL	EOF	SDT2	SBID-3	32		1. 63. 20
	-2	SBID+EP	 .	READ	OTHERS			00		1. 63. 21
63	-1			EMULATE				63	0	1. 63. 30
	0			EMULATE			••	63	+1	1- 63- 40
					EOF	SDT1	SBID+1	20	<u> </u>	1. 63. 50
	+1	SB1D+EP		REAL READ	TM		SBID-REAL BID	10		1. 63. 51
					OTHERS			00		1. 63. 52

PROCESS	Ω	2. 00. 00	2. 10. 00	2. 20. 00	2. 30. 00	2. 40. 00	2.40.10	2. 40. 20	2. 40. 30	2. 40. 40	2. 50. 00	2. 60. 00	2. 60. 10	2. 60. 20	2. 60. 30	2. 60. 40	2. 60. 50
	GJ	1	:	;	;	:	4-	က	-5	Ţ	0	1	7	က	-5	T	0
	ES	00	00	00	8	8	40	40	49	40	09	8	90	9	9	99	99
POST - PROCESS	BID PROCESS	-	-	•	:	:	-	-	-	-	SBID-FREAL BID WHEN (SBID)-(REAL BID)=1	:	1	-	1	1	
	SAVE	:	-	-	:	-	ŀ	1	ŀ	1	:	:	-	1	-		1
	BLOCK	:	•	:	:	:	1	1	-		Æ	1			-	1	:
	OPERATION	REAL RB	REAL RB	PEAL 788	REAL RB	REAL RB	EMULATE	EMULATE	EMULATE	EMULATE	REAL RB	REAL PB	EMULATE	EMULATE	EMULATE	EMULATE	EMULATE
SS	RESTORE	:	i	ŀ	1	:	-				;	!	;	1		1	-
- PROCESS	RE- POSITION	-	i	1	i	SBID+EP				-	;	SBID+EP		1			
PRE -	E	:	1	;	:	7	ကု	-2	T	0	:	4-	-3	-2	T	0	+1
	ES	00	10	20	က္က			\$			20			9	3		

FIG.89

PROCESS	5 EP 10) 2.31.00	0 2.41.00	1 -4 2.41.10	1 -3 2.41.20	1 -2 2.41.30	1 -1 2.41.40	61 0 2.51.00	00 2.61.00	61 -4 2.61.10	61 -3 2.61.20	61 -2 2.61.30	61 -1 2.61.40	61 0 2.61.50
	ES	8	8	41	41	4	4	9	0	9	9	_		
POST - PROCESS	BID PROCESS	1		•	•	•	-	SBID-FEAL BID WHEN (SBID)-(REAL BID)=1	1	-	1	1	1	•
	SAVE	:	1	1	1	1	1	:	;		;	!	!	;
	BLOCK		;	1	1	1	1	Ĕ	:			1	1	1
	OPERATION	REAL R8	78 R	EMULATE	EMULATE	EMULATE	EMULATE	REAL 88	REAL 38	EMULATE	EMULATE	EMULATE	EMULATE	EMULATE
SS	RESTORE		1		;	1	1	•	;		:	1	<u> </u>	!
PRE - PROCESS	RE-	1	SBIDHEP		1	1	1		SBIDEP	!	1	!		;
常	8	:	-4	ئ	-2	7	0	<u>:</u>	7	ကု	-5	T		<u> </u>
	ES	स			4			51				<u> </u>		

PROCESS	Ol	2. 32. 00	2. 42. 00	2. 42. 10	2. 42. 20	2. 42. 30	2. 42. 40	2. 52. 00	2. 62. 00	2. 62. 10	2. 62. 20	2. 62. 30	2. 62. 40	2. 62. 50
E				i		i		0				-2 2		0 2
	굡	:	-	7	ဌ	-5	<u> </u>		-	4	က		<u></u>	
	ES	8	8	42	42	42	42	62	00	62	62	62	62	62
POST - PROCESS	BID PROCESS		:	1	,		-	SBID-FEAL BID WHEN (SBID)-(REAL BID)=1	:	•	1	:	•	•
	SAVE		;	1	!	1	1	;	:			<u> </u>	1	1
	BLOCK	:		1	1	-	1	Æ	•		\$ 5 5 7 7 8		,	1
	OPERATION	REAL R8	PEAL PB	EMULATE	EMULATE	EMULATE	EMULATE	PEAL 88	₹ 88	EMULATE	EMULATE	EMULATE	EMULATE	EMULATE
SS	RESTORE	:-	1		•	1	1	:	:	1	1	:	1	
- PROCESS	RE- POSITION	1	381D+EP	+		. !	1	:	SBIDHEP	1		1	1	1
PRE	6	;	4-	က	-2	_	0	:	-4	က	7-	<u> </u>	0	Ŧ
	ES	32			42			52			ម	3		

FIG.91

	PRE -	- PROCESS	SS				POST - PROCESS			PROCESS
ES	8	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	a	QI
33	:		:	REAL RB	:	1	1	8	:	2. 33. 00
	4	SBIDHEP	ŀ	PEAL PB		:	1	8		2. 43. 00
	က		1	EMULATE	l	l	1	43	4	2. 43. 10
43	-2	1	ļ	EMULATE	1	-	1	43	က	2. 43. 20
	-		-	EMULATE		1	-	43	-2	2. 43. 30
	0		1	EMULATE	ŀ	-	•	43	T	2. 43. 40
53	;	1	-1	PEAL 38	Æ	1	SBID4-FEAL BID WHEN (SBID)-(REAL BID)=1	63	0	2. 53. 00
	4-	SBID+EP	ł	PEAL BB	ł	:	;	8	:	2. 63. 00
	က			EMULATE	1	1	-	63	7	2. 63. 10
3	-5		1	EMULATE	1	1	1	63	က	2. 63. 20
3	丁			EMULATE	1	ŀ	-	ည	7-	2. 63. 30
	0		l	EMULATE	1	1	•	63	T	2. 63. 40
	+	-	-	EMULATE	•	-	-	63	0	2. 63. 50

FIG. 92

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
00				REAL WR			••	00		3. 00. 00
				REAL	EOF	SDT1		20		3. 10. 00
10				WR	OTHERS		######################################	00		3. 10. 01
		·		REAL	EOF	SDT2		30		3. 20. 00
20				WR	OTHERS			00		3. 20. 01
30		-		REAL WR				00		3. 30. 00
	-4			REAL WR				00		3. 40. 00
				REAL	EOF	SDT1	SB1D-3	20		3. 40. 10
	-3			- WR	OTHERS		###	00		3. 40. 11
	•••••	SB1D+EP		REAL	EOF	SDT2	SB1D-3	30		3. 40. 20
40	-2			WR	OTHERS		===	00		3. 40. 21
	-1	1		REAL WR				00		3. 40. 30
				REAL.	EOF	SDT1		20		3. 40. 40
	0			WR	OTHERS			00		3. 40. 41
				REAL.	EOF	SDT1		20		3. 50. 00
50				WR	OTHERS		••	00		3. 50. 01
	-4			REAL WR				00		3. 60. 00
	-			REAL.	EOF	SDT1	SBID-3	20		3. 60. 10
	-3	CD LD LED		WR	OTHERS			00	<u> </u>	3. 60. 11
		SB1D+EP		REAL	EOF	SDT2	SBID-3	30		3. 60. 20
60	-2			WR	OTHERS			00		3. 60. 21
60	-1			REAL WR				00		3. 60. 30
		**************		REAL	EOF	SDT1		20		3. 60. 40
	0			WR	OTHERS			00		3. 60. 41
		CDIN.FR		REAL	EOF	SDT1	SBID+1	20		3. 60. 50
	+1	SB1D+EP		WR	OTHERS		••	00		3. 60. 51

F I G. 93

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
31				REAL WR				00		3. 31. 00
	-4			REAL.			••	00		3. 41. 00
			ļ	REAL	EOF	SDT1	SBID-3	20		3. 41. 10
	-3			WR	OTHERS		~~~	00		3. 41. 11
		SBID+EP		REAL	EOF	SDT2	SBID-3	30		3. 41. 20
41	-2			WR	OTHERS			00		3. 41. 21
	-1			REAL WR				00		3. 41. 30
				REAL	EOF	SDT1		20		3. 41. 40
	0			WR	OTHERS		**************************************	00		3. 41. 41
\vdash	┼─		 	REAL	EOF	SDT1		20		3. 51. 00
51				WR	OTHERS			00		3. 51. 01
	-4			REAL WR				00		3. 61. 00
.		1		REAL	EOF	SDT1	SBID-3	20		3. 61. 10
	-3			WR	OTHERS			00		3. 61. 11
1	ļ	SBID+EP		REAL	EOF	SDT2	S81D-3	30		3. 61. 20
	-2			WR	OTHERS			00		3. 61. 21
61	-1	·		REAL WR				00		3. 61. 30
		· ······			EOF	SDT1		20		3. 61. 40
	0			REAL WR	OTHERS			00		3. 61. 41
	}	·		REAL	EOF	SDT1	SB1D+1	20		3. 61. 50
	+1	SB I D+ES	·	WR	OTHERS			00		3. 61. 51

FIG. 94

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
H		10011101		REAL	EOF	SDT2	••	32		3. 22. 00
22				WR	OTHERS		=+	00		3. 22. 01
32				REAL WR				00	<i>-</i> -	3. 32. 00
	-4			REAL WR				00		3. 42. 00
				REAL	EOF	SDT1	SBID-3	20		3. 42. 10
	-3			WR	OTHERS		••	00		3. 42. 11
		SB1D+EP		REAL	EOF	SDT2	SBID-3	32		3. 42. 20
42	-2			WR	OTHERS		**************************************	00		3. 42. 21
	-1			REAL WR			~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	00		3. 42. 30
1	ļ			REAL	EOF	SDT1		20		3. 42. 40
	0			WR	OTHERS		o-	00	<u> </u>	3. 42. 41
 	 			REAL	EOF	SDT1		20		3. 52. 00
52				WR	OTHERS			00		3. 52. 01
一	-4			REAL WR				00		3. 62. 00
	}	1		REAL	EOF	SDT1	SBID-3	20		3. 62. 10
	-3			WR	OTHERS			00		3. 62. 11
		SBID+EP		REAL	EOF	SDT2	SBID-3	32		3. 62. 20
	-2			, WR	OTHERS		4-0	00		3. 62. 21
62	-1	1		REAL WR				00		3. 62. 30
	ļ	†		REAL	EOF	SDT1		20		3. 62. 40
	0			WR	OTHERS		***	00		3. 62. 41
	-	†		REAL	EOF	SDT1	SBID+1	20		3. 62. 50
	+1	SBID+EP		WR	OTHERS			00		3. 62. 51

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	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID .
33			-	REAL WR				00		3. 33. 00
	4	,		REAL. WR			••	00		3. 43. 00
				REAL	EOF	SDT1	SBID-3	20		3. 43. 10
	-3			WR WR	OTHERS			00		3. 43. 11
		SBID+EP		REAL	EOF	SDT2	SBID-3	32		3. 43. 20
43	-2	-		WR	OTHERS		**************************************	00		3. 43. 21
	-1			REAL WR				00-		3. 43. 30
ł			ļ	REAL	EOF	SDT1		20		3. 43. 40
	0			WR	OTHERS			00		3. 43. 41
-	-		 	REAL	EOF	SDT1	••	20		3. 53. 00
53				WR	OTHERS		~~~···································	00		3. 53. 01
	-4			REAL WR				00		3. 63. 00
-	}	1		REAL	EOF	SDT1	SBID-3	20		3. 63. 10
	-3		-	WR	OTHERS			00		3. 63. 11
		SBIDHEP		REAL	EOF	SDT2	SBID-3	32		3. 63. 20
	-2			WR	OTHERS			00		3. 63. 21
63	-1			REAL WR				00		3. 63. 30
	ļ			REAL	EOF	SDT1		20		3. 63. 40
	0			WR	OTHERS			00		3. 63. 41
				REAL	EOF	SDT1	SBID+1	20		3. 63. 50
	+1	SBIDHEP		WR	OTHERS			00		3. 63. 51

FIG. 96

	PRE	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP		RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
00			1	REAL BSP	••	-		00		4. 00. 00
10				REAL BSP				00		4. 10. 00
20				REAL BSP				10	•	4. 20. 00
30				REAL BSP				20		4. 30. 00
	-4	SB1D+EP		REAL BSP			•	00		4. 40. 00
	-3			EMULATE				40	-4	4. 40. 10
40	-2			EMULATE				40	-3	4. 40. 20
	-1			EMULATE				40	-2	4. 40. 30
	0			EMULATE				40	-1	4. 40. 40
50				REAL BSP	TM		SBID←REAL BID WHEN (SBID)-(REAL BID)=1	60	0	4. 50. 00
<u> </u>	-4	SB1D+EP		REAL BSP				00		4. 60. 00
	-3			EMULATE				60	-4	4. 60. 10
60	-2			EMULATE				60	-3	4. 60. 20
00	-1			EMULATE				60	-2	4. 60. 30
	0			EMULATE				60	-1	4. 60. 40
	+1			EMULATE			••	60	0	4. 60. 50

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	æ	ID
31			-	REAL BSP	1	•		20		4. 31. 00
	-4	SB1D+EP		REAL BSP				00		4. 41. 00
	-3			ENULATE				41	-4	4. 41. 10
41	-2	~~		ENULATE				41	-3	4. 41. 20
į	-1			EMULATE				41	-2	4. 41. 30
	0			EMULATE				41	-1	4. 41. 40
51				REAL BSP	TM		SBID←REAL BID WHEN (SBID)-(REAL BID)=1	61	0	4. 51. 00
	-4	SBID+EP		REAL BSP			 .	00		4. 61. 00
	-3			EMULATE				61	-4	4. 61. 10
61	-2			EMULATE				61	-3	4. 61. 20
"	-1			EMULATE				61	-2	4. 61. 30
	0			EMULATE				61	-1	4. 61. 40
	+1			EMULATE				61	0	4. 61. 50

FIG. 98

	PRF	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
22				REAL BSP				10		4. 22. 00
32				REAL BSP				22	1	4. 32. 00
	-4	SB I D+EP		REAL. BSP				00		4. 42. 00
	-3			EMULATE			~	42	-4	4. 42. 10
42	-2			EMULATE				42	-3	4. 42. 20
	-1			EMULATE				42	-2	4. 42. 30
	0			EMULATE			**************************************	42	-1	4. 42. 40
52				REAL BSP	MT		SBIDREAL BID WHEN (SBID)-(REAL BID)=1	62	0	4. 52. 00
-	-4	SB1D+EP		REAL BSP				00		4. 62. 00
	-3			EMULATE			### ##################################	62	-4	4. 62. 10
	-2			EMULATE			#=	62	-3	4. 62. 20
62	-1	·		EMULATE				62	-2	4. 62. 30
	0			EMULATE				62	-1	4. 62. 40
	+1			EMULATE		<u> </u>		62	0	4. 62. 50

FIG. 99

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP.	ID
33			 · ·	REAL BSP	i			22		4. 33. 00
	-4	SB1D+EP		REAL. BSP				00		4. 43. 00
	-3			EMULATE			##	43	-4	4. 43. 10
43	-2			EMULATE			***************************************	43	-3	4. 43. 20
	-1			ENULATE			==	43	-2	4. 43. 30
	0			EMULATE				43	-1	4. 43. 40
53				REAL BSP	TM		SBID←REAL BID WHEN (SBID)-(REAL BID)=1	63	0	4. 53. 00
-	-4	SBID+EP		REAL BSP				00		4. 63. 00
	-3			EMULATE				63	-4	4. 63. 10
63	-2			EMULATE				63	-3	4. 63. 20
63	-1			EMULATE		 		63	-2	4. 63. 30
	0			EMULATE				63	-1	4. 63. 40
	+1			EMULATE			••	63	0	4. 63. 50

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	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
00		••		REAL BSPF	•			00		5. 00. 00
10				REAL BSPF				00		5. 10. 00
20				REAL BSPF				00		5. 20. 00
30				REAL BSPF			••	00		5. 30. 00
	-4	SB1D+EP		REAL BSPF			••	00		5- 40- 00
	-3			EMULATE			***************************************	40	-4	5. 40. 10
40	-2		` - -	EMULATE				40	-4	5. 40. 20
	-1			EMULATE				40	-4	5. 40. 30
	0			EMULATE				40	-1	5. 40. 40
50				REAL BSPF	TM		SBID-REAL BID WHEN (SBID)-(REAL BID)=1	60	0	5. 50. 00
	-4	SB1D+EP		REAL BSPF				00		5. 60. 00
	-3			EMULATE		† "	***	60	-4	5. 60. 10
60	-2			EMULATE		 		60	-4	5. 60. 20
"	-1			EMULATE		1		60	-4	5. 60. 30
	0			EMULATE				60	-1	5. 60. 40
	+1			EMULATE				60	0	5. 60. 50

	PRE	- PROCE	SS		,		POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
31				REAL BSPF				00		5. 31. 00
	-4	SB1D+EP		REAL BSPF				00		5. 41. 00
	-3			EMULATE				41	-4	5. 41. 10
41	-2			EMULATE			••	41	-4	5. 41. 20
	-1			EMULATE				41	-4	5. 41. 30
	0			EMULATE				41	-1	5. 41. 40
51				REAL BSPF	TM		SBID→REAL BID WHEN (SBID)-(REAL BID)=1	61	0	5. 51. 00
	-4	SB1D+EP		REAL BSPF				00		5. 61. 00
	-3			EMULATE				61	-4	5. 61. 10
61	-2			EMULATE				61	-4	5. 61. 20
"	-1			EMULATE				61	-4	5. 61. 30
	0			EMULATE			•-	61	-1	5. 61. 40
	+1	,		EMULATE			••	61	0	5. 61. 50

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<u> </u>	PRE	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	1D
22				REAL BSPF			· <u></u>	00		5. 22. 00
32				REAL BSPF				00		5. 32. 00
	-4	SB1D+EP		REAL BSPF				00		5. 42. 00
	-3			EMULATE				42	-4	5. 42. 10
42	-2			EMULATE				42	-4	5. 42. 20
	-1			EMULATE			=======================================	42	-4	5. 42. 30
	0			EMULATE				42	-1	5. 42. 40
52				REAL BSPF	TM		SBID←REAL BID WHEN (SBID)-(REAL BID)=1	62	0	5. 52. 00
	-4	SB1D+EP		REAL BSPF				00		5. 62. 00
	-3			EMULATE				62	-4	5. 62. 10
62	-2			EMULATE				62	-4	5. 62. 20
02	-1			EMULATE				62	-4	5. 62. 30
	0			EMULATE				62	-1	5. 62. 40
	+1			EMULATE			••	62	0	5. 62. 50

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID .
33				REAL BSPF				00		5. 33. 00
	-4	SB1D+EP		REAL BSPF				00		5. 43. 00
	-3			EXULATE			==	43	-4	5. 43. 10
43	-2			ENULATE				43	-4	5. 43. 20
	-1			EMULATE			=======================================	43	-4	5. 43. 30
	0			EMULATE				43	-1	5. 43. 40
53				REAL BSPF	TM		SBID+-REAL BID WHEN (SBID)-(REAL BID)=1	63	0	5. 53. 00
	-4	SB1D+EP		REAL BSPF				00		5. 63. 00
	-3			EMULATE		<u> </u>		63	-4	5. 63. 10
	-2			EMULATE			######################################	63	-4	5. 63. 20
63				EMULATE				63	-4	5. 63. 30
	0			EMULATE			ψ=	63	-1	5. 63. 40
	+1			ENULATE			***	63	0	5. 63. 50

	PRE	- PROCE	SS		•		POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID .
	\vdash	10011101		REAL	TM		SBID-REAL BID	10		6. 00. 00
00				SP	OTHERS		##	00		6. 00. 01
			 		EOF			22		6. 10. 00
10				REAL	TM		SBID←REAL BID	10		6. 10. 01
'				SP	OTHERS			00		6. 10. 02
	-				EOF			31		6. 20. 00
20				REAL	TM		SBID←REAL BID	10		6. 20. 01
20	İ			SP ·	OTHERS		**************************************	00		6. 20. 02
				REAL	TM		SBID+-REAL BID WHEN (REAL BID)-(SBID)=3	40	0	6. 30. 00
30				SP	OTHERS			00		6. 30. 01
-	-4	 	 	EMULATE				40	-3	6. 40. 00
	-3			EMULATE				40	-2	6. 40. 10
	-2			EMULATE				40	-1	6. 40. 20
				EMULATE			± =	40	0	6. 40. 30
40	ļ				EOF		**************************************	22		6. 40. 40
	0			REAL SP	TM		SBID-FEAL BID WHEN (REAL BID)-(SBID)=1	50		6. 40. 41
1					OTHERS			00		6. 40. 42
\vdash	+-	-	 		EOF	 		22		6. 50. 00
50	,			" REAL SP	TM		SBID←REAL BID	10		6. 50. 01
					OTHER	s	#=	00)	6. 50. 02
-	+-	 	 	EMULATE				60) -3	6. 60. 00
	-3			EMULATE				60) -2	6. 60. 10
	-2			EMULATE				60) -1	6. 60. 20
	-1			EMULATE				60		6. 60. 30
6				EMULATE				60) +	6. 60. 40
	}				EOF		SBID+1	2	2 -	- 6. 60. 50
	+	1 SBID+E	р	REAL	TM		SBID←REAL BID	1	0 -	6. 60. 51
				SP	OTHER	s		0	0 -	- 6. 60. 52

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	PRF	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
		10011101		REAL	TM		SBID+-REAL BID WHEN (REAL BID)-(SBID)=3	41	0	6. 31. 00
31				SP	OTHERS		~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	00		6. 31. 01
-	-4			ENULATE				41	-3	6. 41. 00
	-3			ENULATE				41	-2	6. 41. 10
	-2			EMULATE				41	-1	6. 41. 20
١.,	-1			EMULATE	···			41	0	6. 41. 30
41					EOF			22		6. 41. 40
	0			REAL SP	TM		SBID+-REAL BID WHEN (REAL BID)-(SBID)=1	51		6. 41. 41
		,			OTHERS			00		6. 41. 42
-	-				EOF			22		6. 51. 00
51				REAL SP	TH		SBID-REAL BID	10		6. 51. 01
"			ł) SF	OTHERS		~-	00		6. 51. 02
-	-4		 	EMULATE				61	-3	6. 61. 00
	-3			EMULATE				61	-2	6. 61. 10
	-2			EMULATE				61	-1	6. 61. 20
	-1			EMULATE				61	0	6. 61. 30
61	0			EMULATE				61	+1	6. 61. 40
	-	·····			EOF	T	SBID+1	22		6. 61. 50
	+1	SBID+EF	.	REAL SP	TM		SBID←REAL BID	10		6. 61. 51
				"	OTHERS			00		6. 61. 52

	DDE	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE-	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
		POSITION			EOF		••	33		6. 22. 00
				REAL	TM		SBID-REAL BID	10		6. 22. 01
22				SP	OTHERS			00		6. 22. 02
_				REAL	MT	••	SBID+-REAL BID WHEN (REAL BID)-(SBID)=3	42	0	6. 32. 00
32				SP	OTHERS			00		6. 32. 01
\vdash	-4			EMULATE				42	-3	6. 42. 00
	-3			EMULATE				42	-2	6. 42. 10
	-2			EMULATE				42	-1	6. 42. 20
	\ <u>-</u>			EMULATE				42	0	6. 42. 30
42		·			EOF			22		6. 42. 40
	0			REAL SP	TM		SBID⊶REAL BID WHEN (REAL BID)-(SBID)=1	52		6. 42. 41
		}			OTHERS			00		6. 42. 42
\vdash	+-		+	-	EOF			22		6. 52. 00
52	,			REAL	TM		SBID-REAL BID	10		6. 52. 01
3		1	1	SP	OTHERS	si		00	<u> </u>	10.02.02
\vdash	 _	 		EMULATE				62	2 -:	6. 62. 00
-	-3			EMULATE				62	2 -2	
	-			EMULATE	·			6	2. -	6. 62. 20
	-			EMULATE				6	2	0 6. 62. 30
6	2 }	· 0		EMULATE				6	2 +	1 6.62.40
1	-				EOF		SBID+1	2	2 -	- 6. 62. 50
1		1 SBIDHE	p	REAL	TM		SBID-REAL BID	1	0 -	- 6. 62. 51
		, , , , , , ,		SP	OTHER	s		0	0 -	- 6. 62. 52

FIG. 107

PRF	- PROCE	SS				POST - PROCESS			PROCESS
	RE-		OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
	rostrion		REAL	TH		SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	6. 33. 00
			SP	OTHERS		##	00		6. 33. 01
-4			EMULATE				43	-3	6. 43. 00
			EMULATE			***	43	-2	6. 43. 10
			EMULATE			##	43	-1	6. 43. 20
			EMULATE				43	0	6. 43. 30
}				EOF			22		6. 43. 40
0			REAL SP	TM		SBID⊷REAL BID WHEN (REAL BID)-(SBID)=1	53		6. 43. 41
				OTHERS			00		6. 43. 42
┼		-	 	EOF	-		22		6. 53. 00
			REAL	TM	†	SBID-REAL BID	10		6. 53. 01
			SP	OTHERS			00		6. 53. 02
-4	 	+	EMULATE	 			63	-3	6. 63. 00
}	.+		EMULATE		·		63	-2	6. 63. 10
}	· 				†** <u></u>		63	-1	6. 63. 20
							63	3 (6. 63. 30
3			EMULATE			»«	63	3 +1	6. 63. 40
				EOF		SBID+1	2	2 -	6. 63. 50
١,,	CBIDIE		REAL	TM		SBID-REAL BID	1	0 -	6. 63. 51
*'	SOLUTE	'	SP	OTHER	s		0	0 -	6. 63. 52
	-4 -3 -2 -1 0	EP RE-POSITION		EP RE-POSITION RESTORE OPERATION REAL SP -4 EMULATE -3 EMULATE -1 EMULATE 0 REAL SP EMULATE -3 EMULATE -2 EMULATE -1 EMULATE 0 EMULATE REAL EMULATE	EP RE-POSITION RESTORE OPERATION BLOCK REAL SP OTHERS TM OTHERS -4 EMULATE -3 EMULATE -1 EMULATE 0 REAL SP TM OTHERS EMULATE -4 EMULATE -3 EMULATE -1 EMULATE -1 EMULATE -1 EMULATE -1 EMULATE -1 EMULATE +1 SBIDHEP REAL SP TM	EP RE-POSITION RESTORE OPERATION BLOCK SAVE	PRE	PRICE	PRICE

	PRF	- PROCE	SS				POST PROCESS			PR	OCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP		ID
=		POSITION		DEAL	TN		SBID-REAL BID	10		7.	00.00
00				REAL FSPF	OTHERS		**************************************	00		7.	00. 01
							SBID←REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7.	10.00
10				REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10			. 10. 01
					OTHERS		••	00	=	17	. 10. 02
	-	<u> </u>					SBID-REAL BID WHEN (REAL BID)-(SBID)=3	41	0	7	. 20. 00
20				REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10			. 20. 01
		1			OTHERS		••	00	<u> </u> -	17	. 20. 02
	-						SBID+-REAL BID WHEN (REAL BID)-(SBID)=3	40	() 7	7. 30. 00
30				REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10			7. 30. 01
	1				OTHERS		***	00	-	1	7. 30. 02
	-4			EMULATE			••	40		3	7. 40. 00
i	-3			EMULATE			***************************************	40		0	7. 40. 10
	-2			EMULATE				40	<u> </u>	0	7. 40. 20
	-1			EMULATE				40		0	7. 40. 30
40	ļ				1		SBID←REAL BID WHEN (REAL BID)-(SBID)=3	43		0	7. 40. 40
				REAL	TN		SBID - REAL BID WHEN (REAL BID)-(SBID)=1	50) .	-	7. 40. 41
		0		FSPF			SBID-REAL BID WHEN (REAL BID)-(SBID)≠1AND	3 10)		7. 40. 42
					OTHER	s		0	2		7. 40. 43
\vdash	+	_	_				SBID+REAL BID WHEN (REAL BID)-(SBID)=3	4	3	0	7. 50. 00
5	- ا ہ	_		REAL FSPF	TM.		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	1	0		7. 50. 01
				Ì	OTHER	s		0	0		7. 50. 0
\vdash	+	4		EMULAT	E			6	0	-3	7. 60. 0
	}	-3		EMULAT	E		## ### ###############################	6	0	0	7. 60. 1
	ļ	-2		EMULAT	E			6	0	0	7. 60. 2
	- }	-1		EMULAT	E		A	16	0	0	7. 60. 3
1	50	0					***	10	0	+1	7. 60. 4
		· · · · · · · · · · · · · · · · · · ·					SBID→REAL BID WHEN (REAL BID)-(SBID)=3		13	0	7. 60. 5
		+1 SBID+	EP	REAL FSPF			SBID←REAL BID WHEN (REAL BID)-(SBID)≠3		10		7. 60. !
					OTHE	RS			00		7. 60. !

F	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	1D
-							SBID-REAL BID WHEN (REAL BID)-(SBID)=3	41	0	7. 31. 00
31				REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 31. 01
					OTHERS			00		7. 31. 02
	-4			EMULATE			##	41	-3	7. 41. 00
	-3			EMULATE			***************************************	41	0	7. 41. 10
	-2			EMULATE				41	0	7. 41. 20
	-1			ENULATE				41	0	7. 41. 30
41							SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 41. 40
				REAL	TM		SBID-REAL BID WHEN (REAL BID)-(SBID)=1	51		7. 41. 41
	0			FSPF			SBID←REAL BID WHEN (REAL BID)-(SBID)≠1AND3	10		7. 41. 42
					OTHERS			00		7. 41. 43
-	+-		1				SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 51. 00
51				REAL FSPF	MT		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 51. 01
		•			OTHERS			00		7. 51. 02
-	-4		+	EMULATE				61	-3	7. 61. 00
	-3	 		EMULATE				61	0	7. 61. 10
	-2			EMULATE				61	0	7. 61. 20
	-1			EMULATE		T		61	0	7. 61. 30
61	l			EMULATE		†		61	+1	7. 61. 40
	-	1					SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 61. 50
	+1	SB1D+EF		REAL FSPF	TM		SBID→REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 61. 51
					OTHER	s		00		7. 61. 52

	PRF	- PROCE	ss				POST - PROCESS		_	PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
		POSITION					SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 22. 00
22				REAL FSPF	TM			10		7. 22. 01
				'	OTHERS			00		7. 22. 02
	-						SBID←REAL BID WHEN (REAL BID)-(SBID)=3	42	0	7. 32. 00
32				REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 32. 01
				_	OTHERS			00		7. 32. 02
 	-4			EMULATE	T			42	-3	
	-3			ENULATE		<u> </u>		42	0	7. 42. 10
	-2			EMULATE			**	42	0	
1	-1	ļ		EMULATE		T		42	0	7. 42. 30
42							SBID←REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 42. 40
				REAL	TM		SBID-REAL BID WHEN (REAL BID)-(SBID)=1	52		7. 42. 41
	0		-	FSPF			SBID-REAL BID WHEN (REAL BID)-(SBID)≠1AND3	10		7. 42. 42
			İ		OTHER	s		00		7. 42. 43
	+	+	_				SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43		7. 52. 00
5	2 -			REAL FSPF	TM		SBID-REAL BID WHEN (REAL BID)-(SBID)≠3	10) -	
					OTHE	S		00	<u> </u>	1.02.0
+	+-	1		EMULAT	E	- -		62	2 -	3 7. 62. 00
	}			EMILAT				6	2	0 7. 62. 10
	}			EMULAT			***	6	2	0 7. 62. 20
		2		EMULAT				6	2	0 7.62.30
	52	-1		EMULAT				6	2	7. 62. 40
1	-	0					SBID⊷REAL BID WHEN (REAL BID)-(SBID)=3	4	3	0 7. 62. 50
		+1 SBID+	EP	REAL		-	SBID←REAL BID	1	0	7. 62. 51
				Fari	OTH	RS -		0	00	7. 62. 52

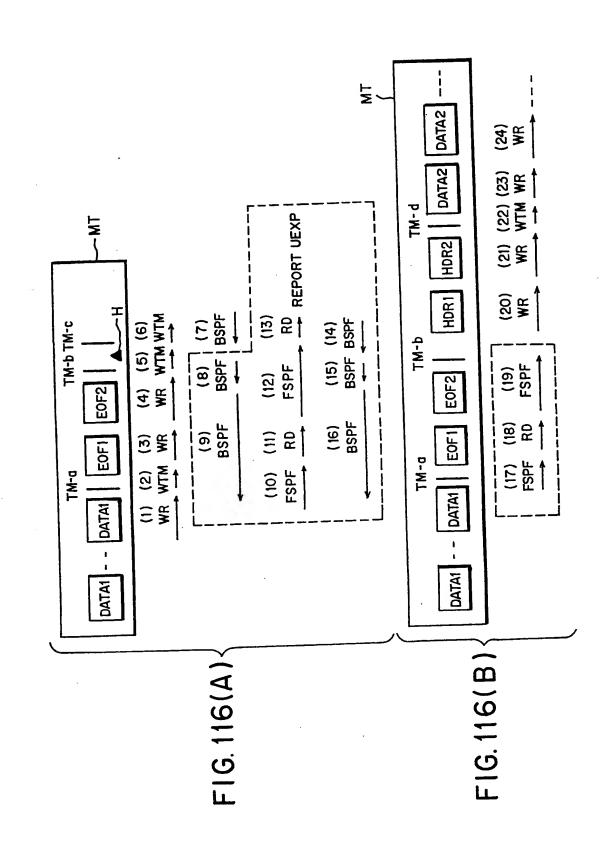
	PRF	– PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
		rostrian					SBID←REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 33. 00
33				REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 33. 01
					OTHERS			00	••	7. 33. 02
	-4			EMULATE				43	-3	7. 43. 00
	-3			EMULATE				43	0	7. 43. 10
·	-2			ENULATE			***************************************	43	0	7. 43. 20
ļ	-1			EMULATE			••••••••••	43	0	7. 43. 30
43	<u>-</u> -						SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 43. 40
				REAL	TM		SBID-REAL BID WHEN (REAL BID)-(SBID)=1	53		7. 43. 41
	0			FSPF			SBID+-REAL BID WHEN (REAL BID)-(SBID)≠1AND3	10		7. 43. 42
					OTHERS			00	<u> </u>	7. 43. 43
-	+	 	-				SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 53. 00
53				REAL FSPF	TM		SBID-REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 53. 01
					OTHERS	 S		00		7. 53. 02
-	-4	 	+	EMULATE				63	-3	7. 63. 00
	-3			EMULATE				63	3 (7. 63. 10
ł	-2			ENULATE				63	3 (7. 63. 20
	-1		 	EMULATE				6	3	7. 63. 30
6	l			EMULATI				6	3 +	7. 63. 40
	-	,					SBID+-REAL BID WHEN (REAL BID)-(SBID)=3	4	3	0 7. 63. 50
	+	SBID+E	Р	REAL	TM		SBID-REAL BID WHEN (REAL BID)-(SBID)≠3	1	0 -	- 7. 63. 51
				13,	OTHER	s		0	0 -	- 7. 63. 52

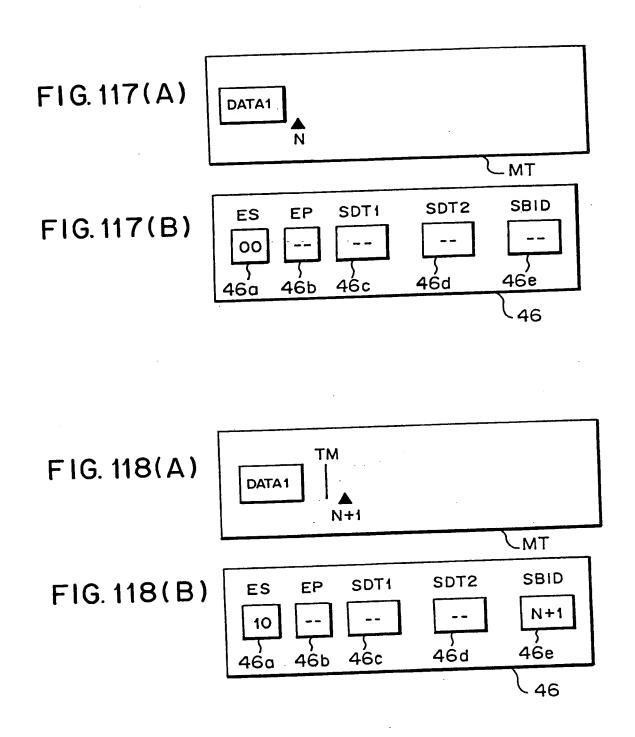
							POST - PROCESS			PI	ROCESS
-	PRE	- PROCE	SS	OPERATION			BID PROCESS	ES	EP	١	ID
ES	EP	RE- POSITION	RESTORE		BLOCK	SAVE	BID PROCESS		==	十	00.00
00				REAL WTM	·		SBID-REAL BID	10		\ <u>*</u>	. 00. 00
			 	REAL			SBID-REAL BID	10		8	3. 10. 00
10				REAL			SBID-REAL BID	10		1	3. 20. 00
20			<u> </u>	WTM REAL			CRID-REAL BID	40	1	, ,	8. 30. 00
30				WTM	<u> </u>	ļ <u></u>	WHEN (REAL BID)-(SBID)=3	10	+-	1	8, 40, 00
	-4	SBID+EP		REAL							
	-3	SB1D+EF		REAL WTM			SBID-REAL BID	10	-	- -	8. 40. 10
				REAL			SBID+-REAL BID	10	-	-	8. 40. 20
40	-2	SB ID+EF		WTM				40		0	8. 40. 30
	-1			EMULATE	•		SBID-REAL BID	5	0		8. 40. 40
	0			WTM	<u> </u>		WHEN (REAL BID)-(SBID)=1	+,	$^{+}$		8, 50, 00
51	0			REAL			SBID-REAL BID		+	-	0.00.00
\vdash	 	4 SBID+E	P	REAL			SBID-REAL BID	1	0		8. 60. 00
	-			REAL			SBID-REAL BID	1	0		8. 60. 10
		3 SBIDH		WTM			SBID-REAL BID	1	0		8. 60. 20
	- ₀	2 SBID+	P	WTM REAL			COLD DEAL RID		10		8. 60. 3
1		-1 SBID+	EP	WTM			20 In-IEUT DIE		60	 +1	8. 60. 4
		0		EMULA.	TE	-					
	-	+1 SBID+	EP	REAL WITM			- SBID-REAL BID		10		8. 60. 5

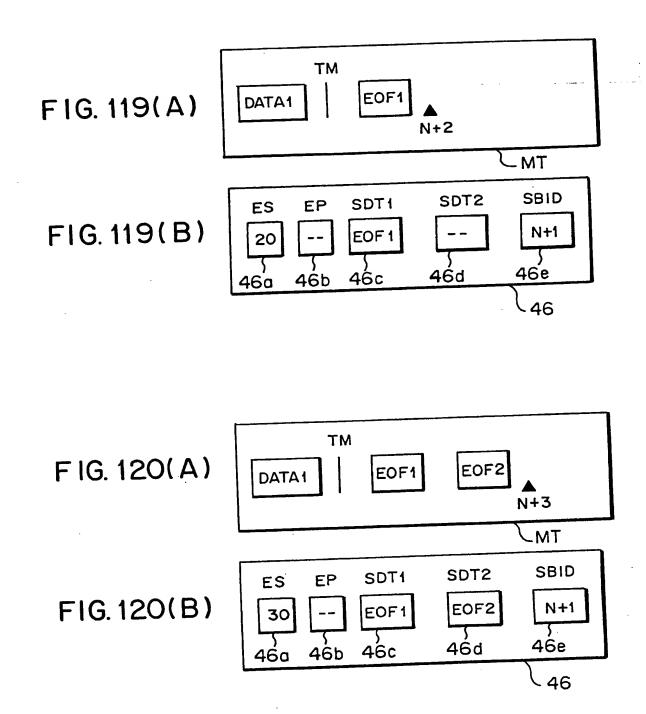
			.00	T			POST - PROCESS			PROCESS
		- PROCE	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
ES	EP	POSITION		REAL			SBID-REAL BID WHEN (REAL BID)-(SBID)=3	41	0	8. 31. 00
31		CDIDIEB		REAL			SBID-REAL BID	10		8. 41. 00
	-4	SBID+EP		WTM REAL			SBID-REAL BID	10		8. 41. 10
	-3	SBID+EP		WTM REAL_			SBID-REAL BID	10		8. 41. 20
41	-2	SBID+EP		WTM		<u> </u>		41	0	8. 41. 30
	-1			EMULATE	ļ		SBID-REAL BID	51		8. 41. 40
	0		ļ <u></u>	WTM	ļ- <u>-</u> -	-	WHEN (REAL BID)-(SBID)=1 SBID-REAL BID	10		8. 51. 00
51				WTM		ļ <u> </u>		10		8. 61. 00
	-4	SB1D+EP		REAL WTM			SBID-REAL BID	10		8, 61, 10
	-3	SB1D+EF		REAL WTM			SBID+ REAL BID			
	-2	SBID+E		REAL WTM			SBID-REAL BID	11		
6	-	SBID+E	P	REAL WTM			SBID-REAL BID	1		
	•••	0		EMULATI				6		1 8.61.40
	+	1 SBID+E	P	REAL WITH			SBID-REAL BID	1	0 -	- 8. 61. 50

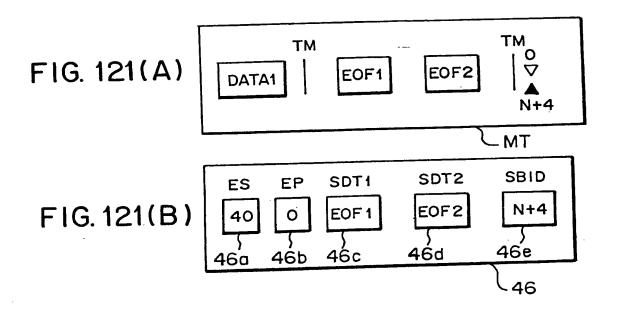
							POST - PROCESS			PROCESS
	PRE	- PROCE		OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
ES	EP	POSITION	RESTORE		BLUCK	SATE		10		8, 22, 00
22				REAL WTM			SBID-REAL BID	-		
32				REAL. WTM			SBID←REAL BID WHEN (REAL BID)-(SBID)=3	42	0	8. 32. 00
<u>JZ</u>	-	en in its		REAL			SBID+ REAL BID	10		8. 42. 00
	-4	SBIDHEP		WTM REAL			SBID-REAL BID	10		8. 42. 10
	-3	SB1D+EP		WTM				10		8. 42. 20
42	-2	SBIDHEP		REAL WITH			SBID←REAL BID		·	8. 42. 30
	-1			EMULATE				42	0	8. 42. 30
	0			REAL			SBID←REAL BID WHEN (REAL BID)-(SBID)=1	52		8. 42. 40
-	+		 	REAL		1	SBID-REAL BID	10	-	8. 52. 00
52	 -		┼	WTM	-	+	SBID-REAL BID	10		8. 62. 00
	-4	SBID+EF		WTM				10	;†	- 8. 62. 10
	-3	\$BID+E	·\	REAL WTM			SBID-REAL BID			
	-2	SBID+E	Р	REAL WIM			SBID-REAL BID	11	0 -	- 8. 62. 20
62	2	SBID+E		REAL			SBID-REAL BID	1	0 -	- 8. 62. 30
				ENLILATE		·		6	2 +	1 8. 62. 40
	()		REAL			SBID-REAL BID	1	0 .	- 8. 62. 50
	+	1 SBID+E	Р	WTM			SOID-HEAT DID			

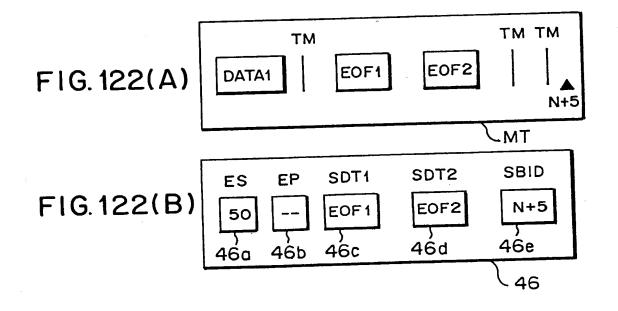
			200				POST - PROCESS			PROCESS
<u> </u>	PRE	- PROCE	:55	OPERATION			BID PROCESS	ES	ΕP	ID
ES	EP	RE- POSITION	RESTORE		BLOCK	SAVE				
33				REAL WITH			SBID←REAL BID WHEN (REAL BID)-(SBID)=3	42	0	8. 33. 00
-	-4	SB1D+EP		REAL			SBID+-REAL BID	10		8. 43. 00
	-3	SBID+EP		REAL WTM			SBID-REAL BID	10		8. 43. 10
43	-2	SBID+EP		REAL WTM			SBID←REAL BID	10		8. 43. 20
13	-1			EMULATE				43	0	8. 43. 30
	0			REAL			SBID+-REAL BID WHEN (REAL BID)-(SBID)=1	53		8. 43. 40
53	 			REAL WIM			SBID-REAL BID	10		8. 53. 00
-	-4	SB1D+EP		REAL WIN			SBID-REAL BID	10		8. 63. 00
	-3	SB1D+EP		REAL			SBID←REAL BID	10		8. 63. 10
	-2			REAL			SBID←REAL BID	10		8. 63. 20
63	1 -	SB1D+EF		REAL WTM			SBID←REAL BID	10		8. 63. 30
				EMULATE				63	+1	8. 63. 40
	+1			REAL WTM			SBID-REAL BID	10) -	8. 63. 50

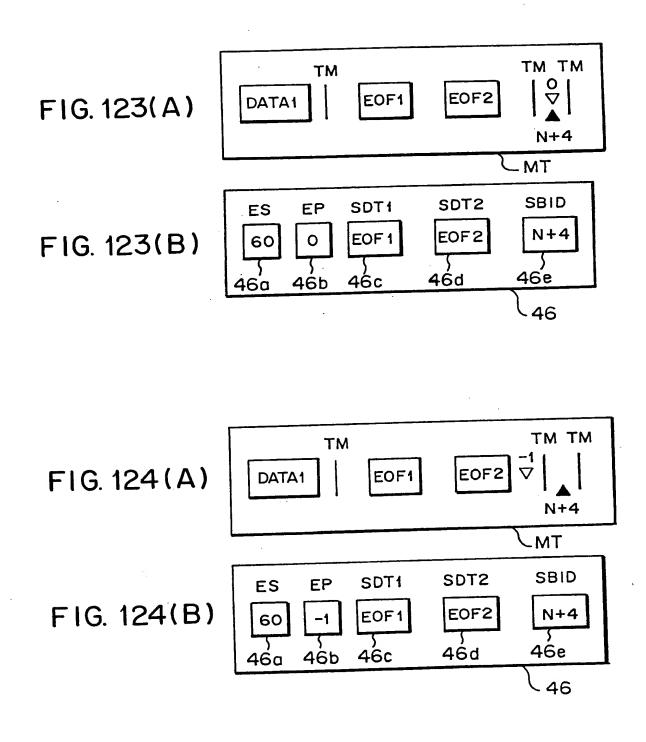


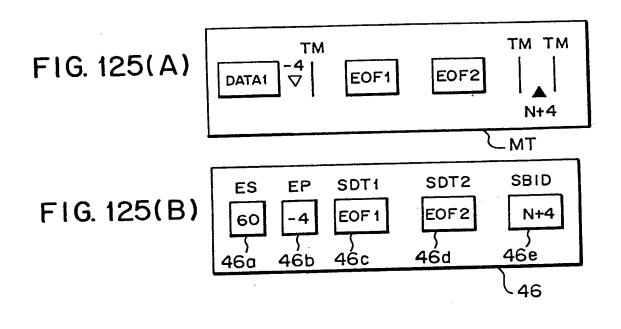


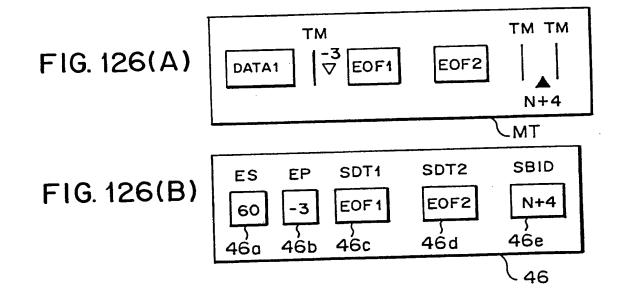


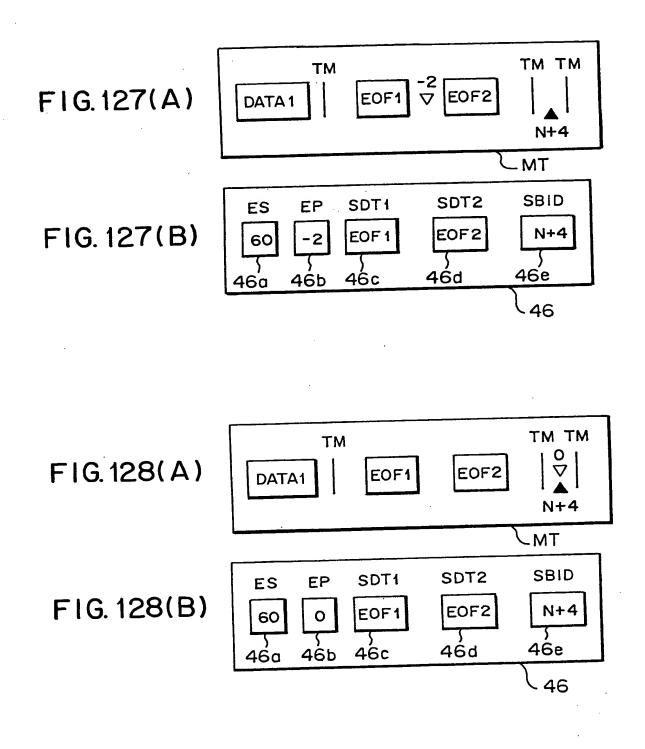


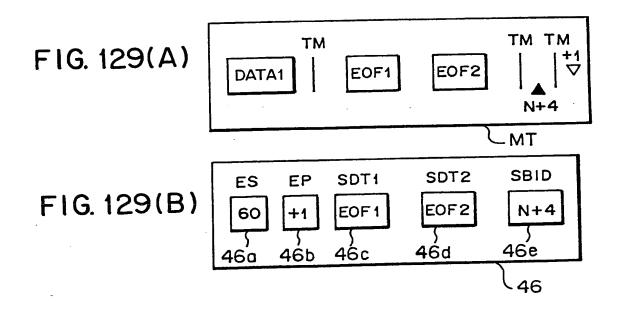


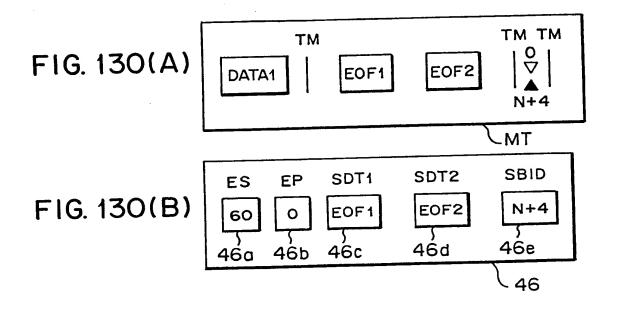


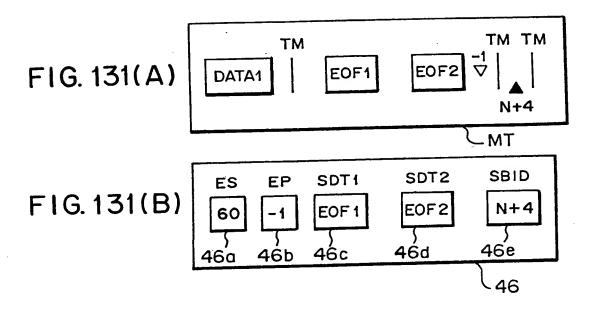


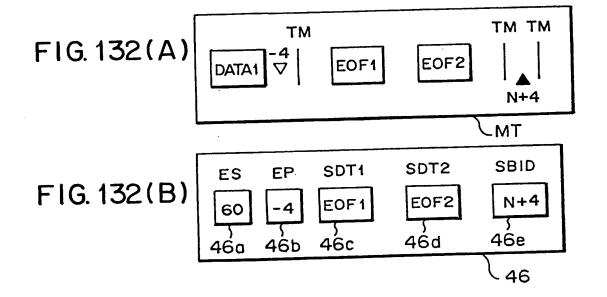


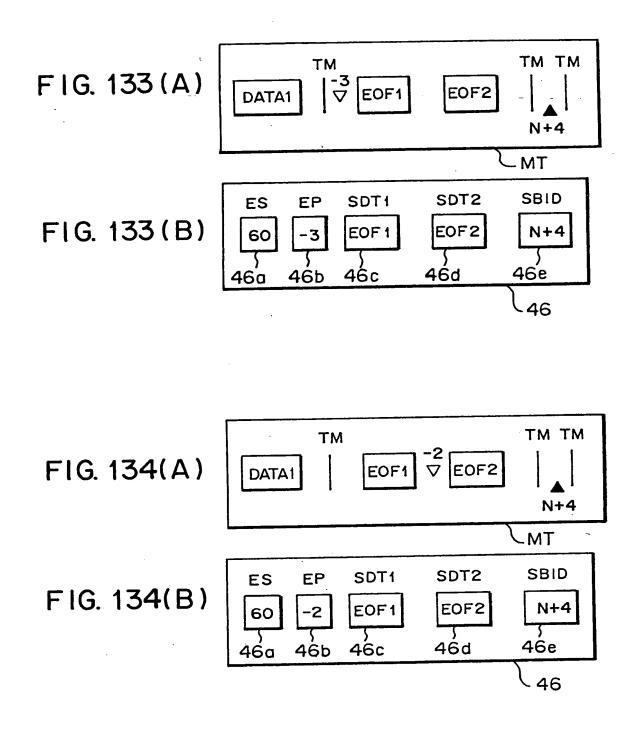












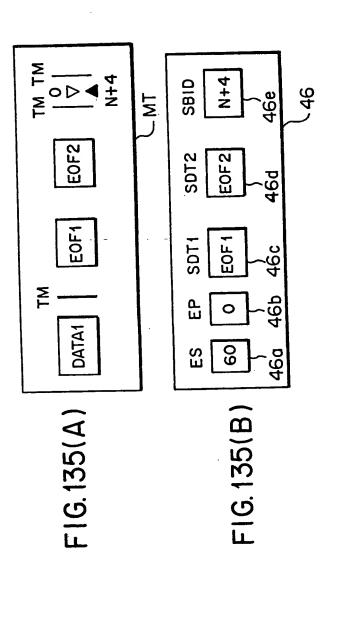


FIG. 136(A)

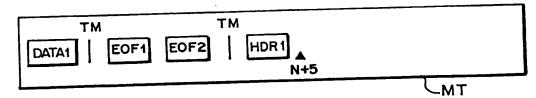


FIG. 136(B)

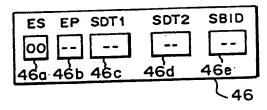


FIG. 137(A)

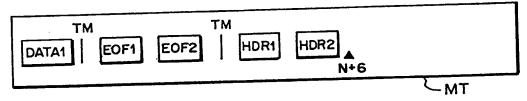


FIG. 137(B)

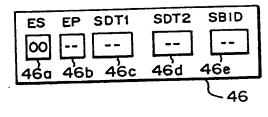


FIG. 138(A)

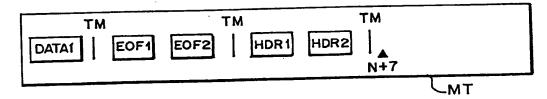


FIG. 138(B)

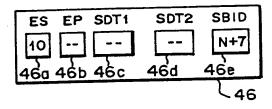


FIG. 139(A)

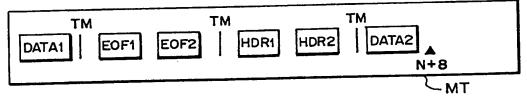


FIG. 139(B)

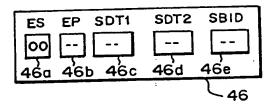


FIG. 140(A)

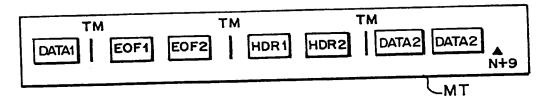
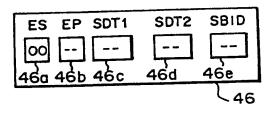


FIG. 140(B)



F1G. 141

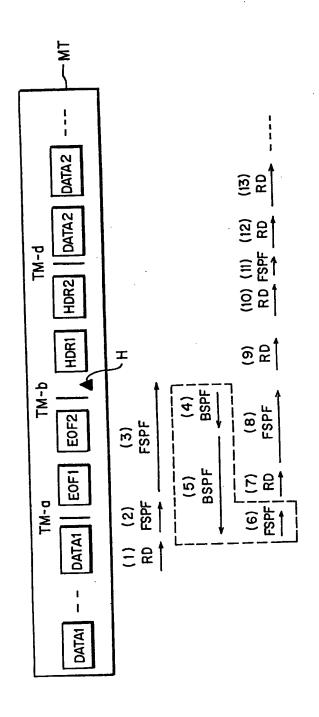


FIG. 142(A)

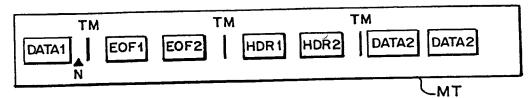


FIG. 142(B)

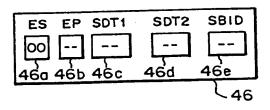


FIG. 143(A)

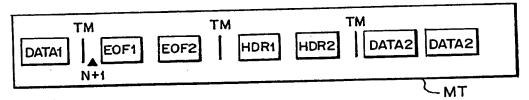


FIG. 143(B)

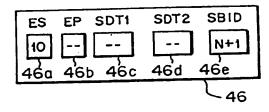


FIG. 144(A)

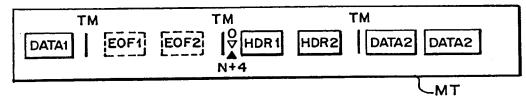


FIG. 144(B)

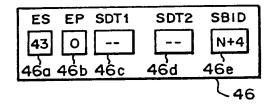


FIG. 145(A)

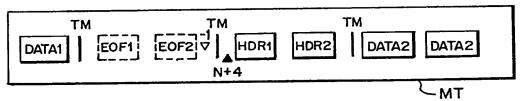


FIG. 145(B)

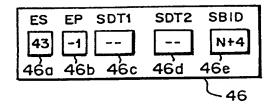


FIG. 146(A)

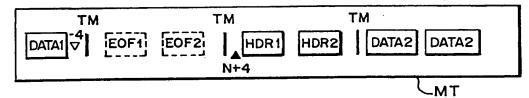


FIG. 146(B)

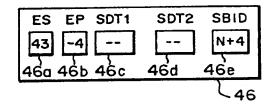


FIG. 147(A)

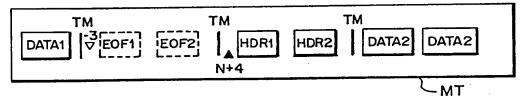


FIG. 147(B)

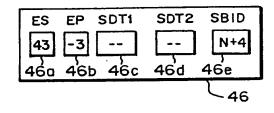


FIG. 148(A)

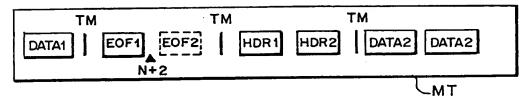


FIG. 148(B)

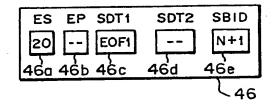


FIG. 149(A)

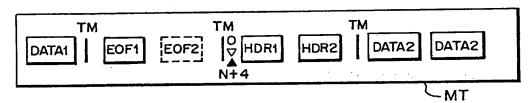


FIG. 149(B)

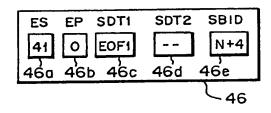


FIG. 150(A)

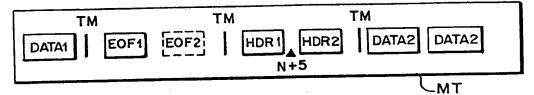


FIG. 150(B)

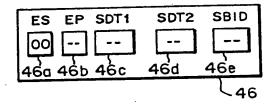


FIG. 151(A)

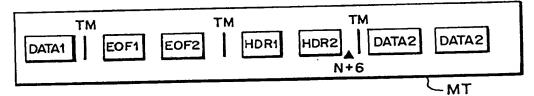


FIG. 151(B)

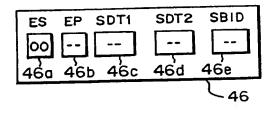


FIG. 152(A)

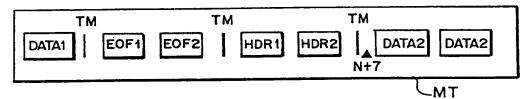


FIG. 152(B)

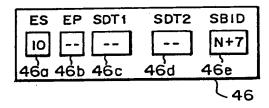


FIG. 153(A)

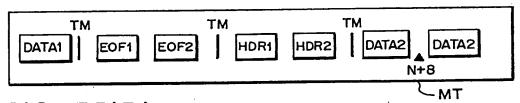


FIG. 153(B)

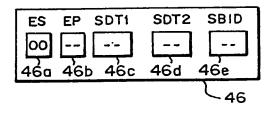
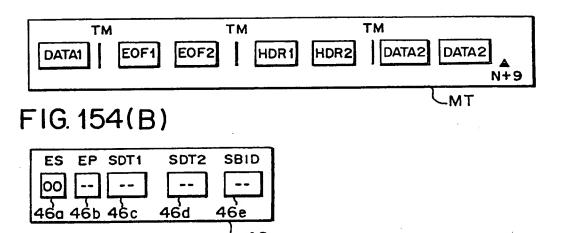
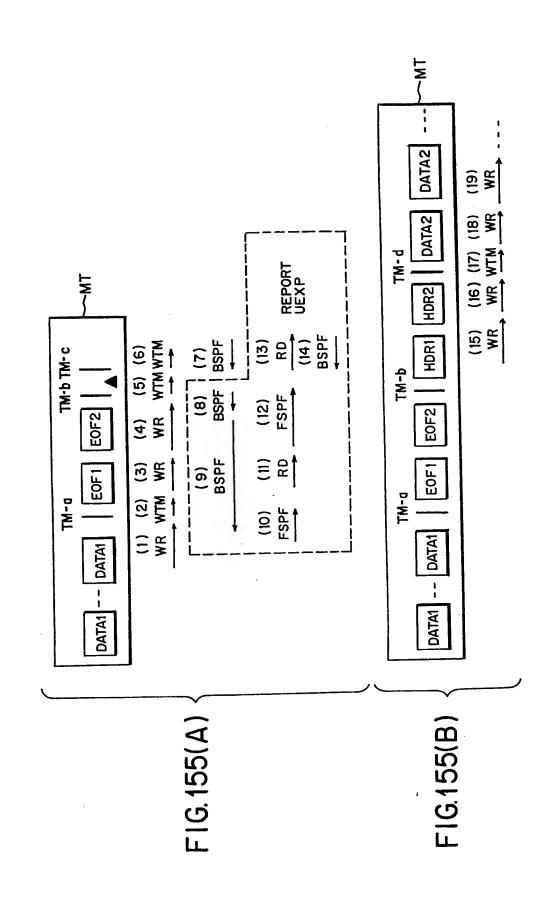


FIG. 154(A)





F1G.156

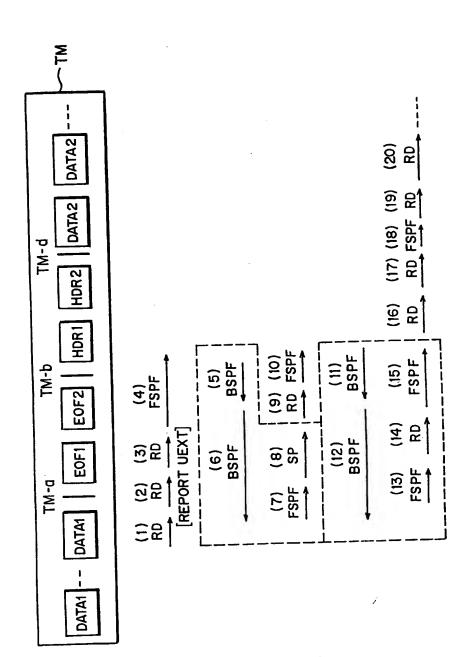


FIG. 157(A)

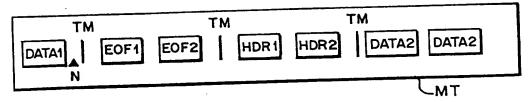


FIG. 157(B)

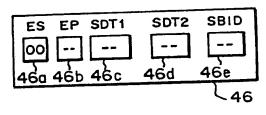


FIG. 158(A)

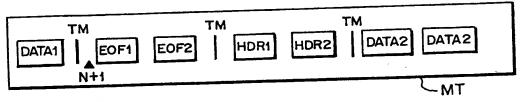


FIG. 158(B)

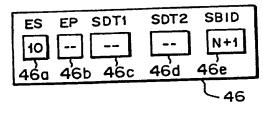


FIG. 159(A)

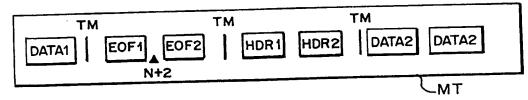


FIG. 159(B)

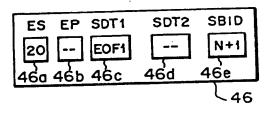


FIG. 160(A)

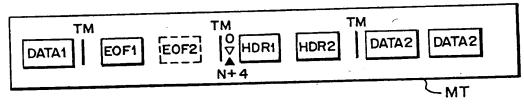


FIG. 160(B)

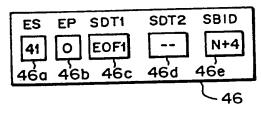


FIG. 161(A)

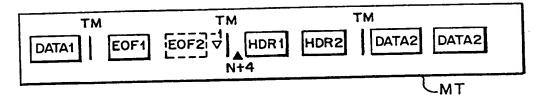


FIG. 161(B)

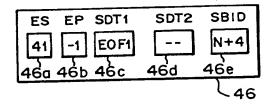


FIG. 162(A)

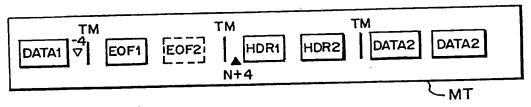


FIG. 162(B)

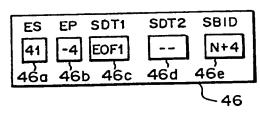
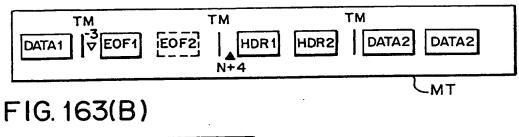


FIG. 163(A)



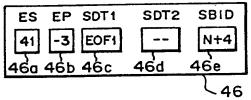


FIG. 164(A)

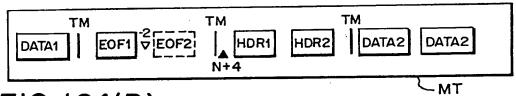


FIG. 164(B)

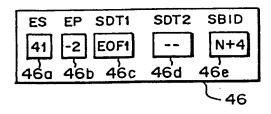


FIG. 165(A)

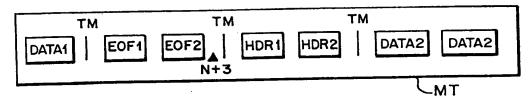


FIG. 165(B)

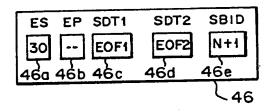


FIG. 166(A)

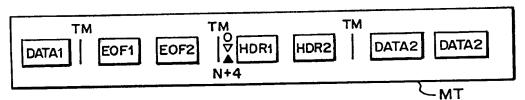


FIG. 166(B)

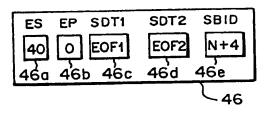


FIG. 167(A)

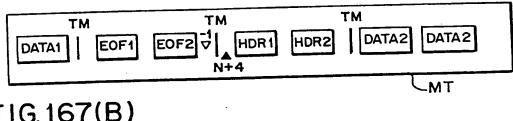


FIG.167(B)

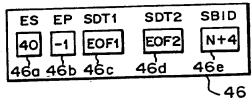


FIG. 168(A)

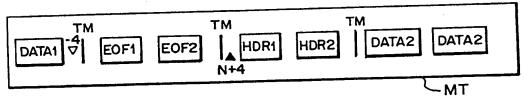


FIG. 168(B)

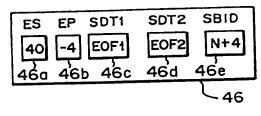


FIG. 169(A)

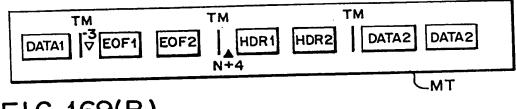


FIG. 169(B)

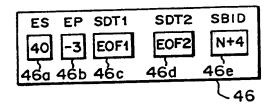


FIG. 170(A)

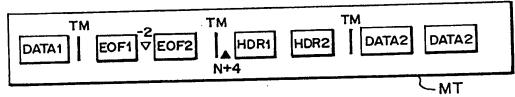


FIG. 170(B)

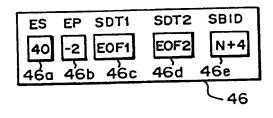


FIG. 171(A)

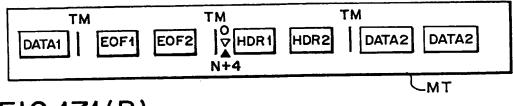


FIG. 171(B)

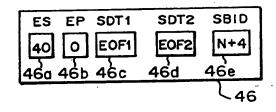


FIG. 172(A)

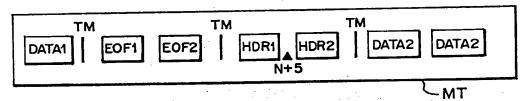


FIG. 172(B)

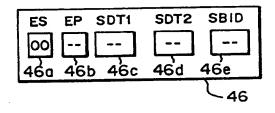


FIG.173

COMMAND	WITHOUT EMULATION	WITH EMULATION	REMARKS
RD	0. 001561	0. 001561	(1)
FSPF	0. 453726	0. 453726	(2)
FSPF	0. 453545	0. 453545	(3)
NOP	0. 293952	0. 293952	į
LDSP	0. 000525	0. 000525	
LDSP	0. 000456	0. 000456	
LDSP	0. 000492	0. 000492	
NOP	0. 000498	0. 000498	
SNIO	0. 002933	0. 002933	
RDVC	0. 000591	0. 000591	1
NOP	0. 000495	0. 000495	
SNS	0. 002706	0. 002706	
LDSP	0. 000425	0. 000425	
BSPF	0. 000581	0. 000500	(4)
BSPF	0. 457145	0. 000500	(5)
FSPF	0. 290540	0. 000500	(6)
RD	0. 531764	0. 531764	(7)
FSPF	1. 409729	1. 409729	(8)
RD	0. 369065	0. 369065	(9)
RD	0. 003179	0. 003179	(10)
FSPF	0. 657888	0. 657888	(11)
NOP	0. 454188	0. 454188	
RD	0. 127053	0. 127053	(12)
RD	0. 001793	0. 001793	(13)
TOTAL	5. 514830	4. 768064	SECOND

FIG.174

COMMAND	WITHOUT EMULATION	WITH EMULATION	REMARKS
RD	0. 001500	0. 001500	(1)
RD	0. 581100	0. 581100	(2)
RD	0. 531700	0. 531700	(3)
FSPF	1. 409700	1. 409700	(4)
BSPF	0. 000500	0. 000500	(5)
BSPF	0. 457100	0. 000500	(6)
FSPF	0. 290900	0. 000500	(7)
SP	0. 531400	0. 000500	(8)
RD	0. 531400	0. 531400	(9)
FSPF	0. 141400	0. 141400	(10)
LDSP	0. 000500	0. 000500	
LDSP	0. 000500	0. 000500	
NOP	0. 000500	0. 000500	
LDSP	0. 000500	0. 000500	
BSPF	0. 000500	0. 000500	(11)
BSPF	0. 457100	0. 000500	(12)
FSPF	0. 290500	0. 000500	(13)
RD	0. 531700	0. 000500	(14)
FSPF	1. 409700	0. 000500	(15)
RD	0. 369000	0. 369000	(16)
RD	0. 003100	0. 003100	(17)
LDSP	0. 000500	0. 000500	
FSPF	0. 657800	0. 657800	(18)
NOP	0. 454100	0. 454100	
LDSP	0. 000500	0. 000500	
RD	0. 127000	0. 127000	(19)
RD	0. 001700	0. 001700	(20)
TOTAL	8. 781900	4. 817000	SECOND

FIG. 175 RELATED ART

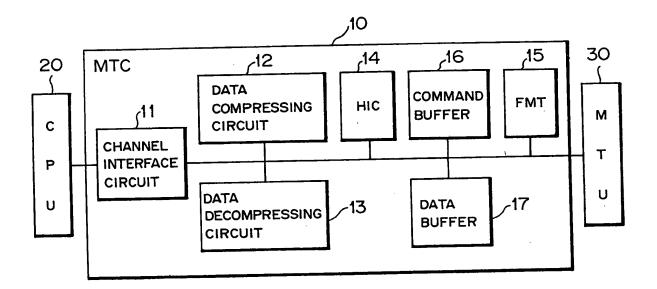


FIG. 176RELATED ART

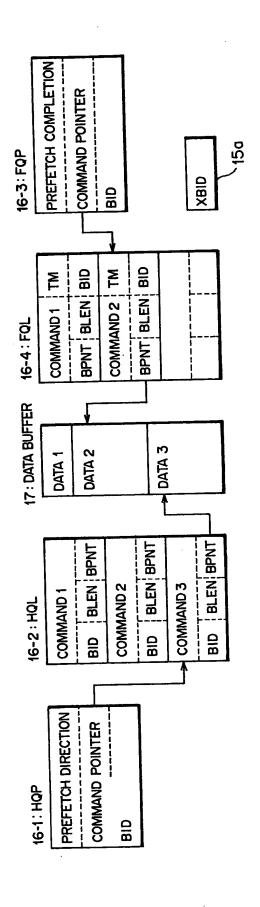


FIG. 177 RELATED ART

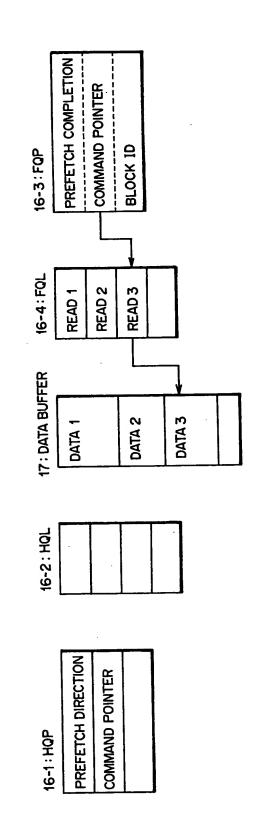


FIG. 178 RELATED ART

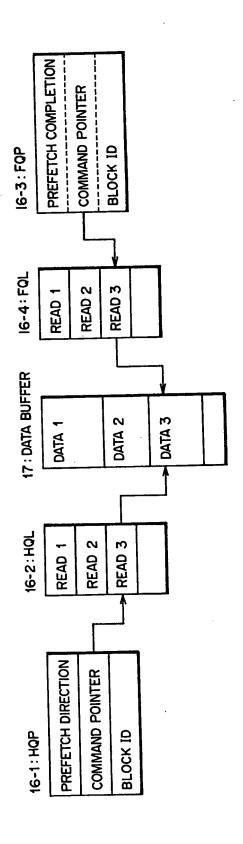


FIG. 179RELATED ART

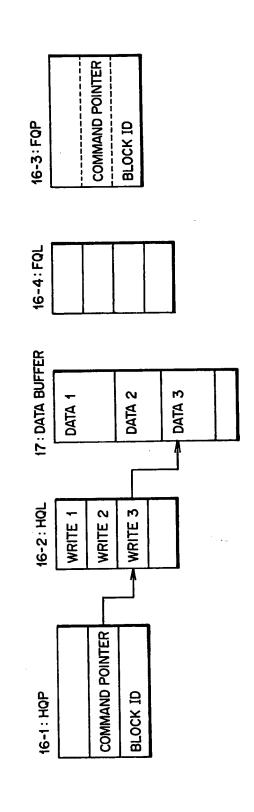


FIG. 180 RELATED ART

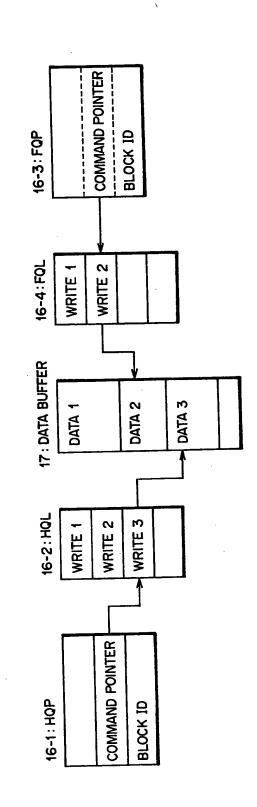


FIG. 181 RELATED ART

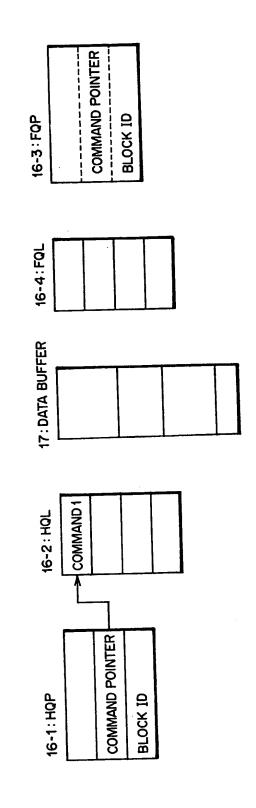


FIG. 182 RELATED ART

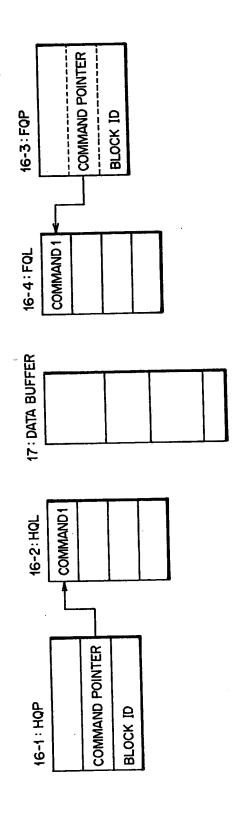


FIG. 183 RELATED ART

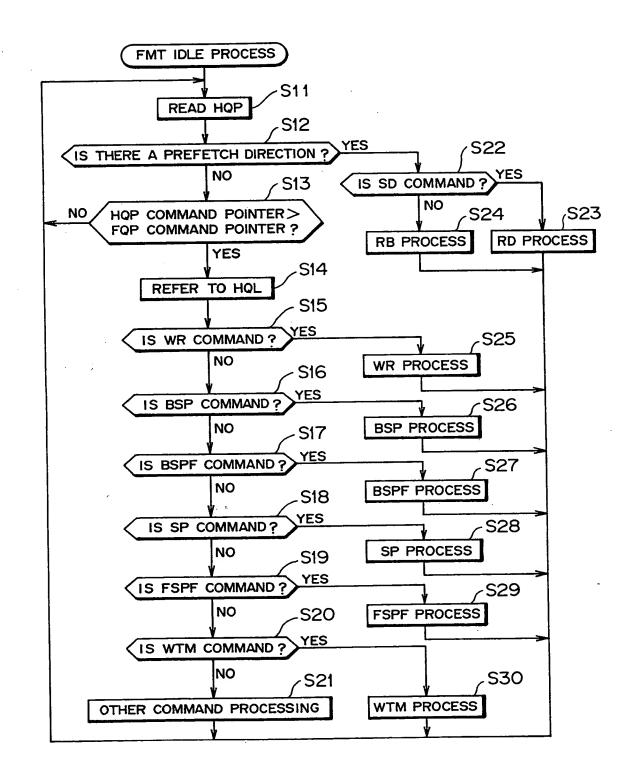


FIG. 184 RELATED ART

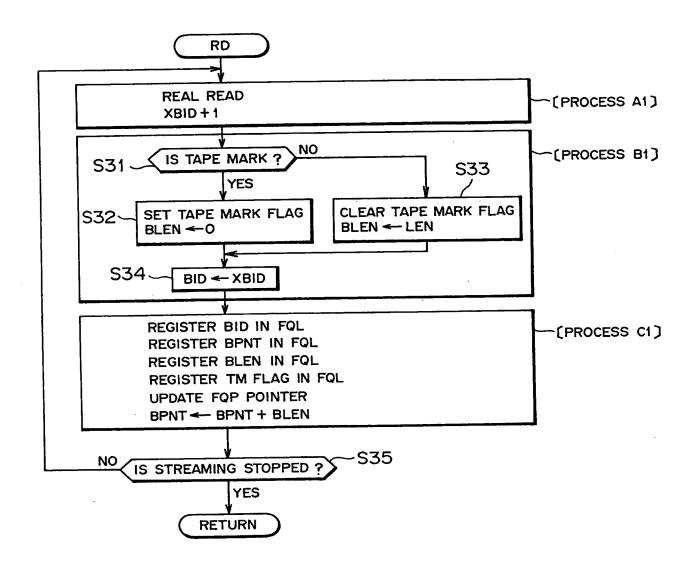


FIG. 185 RELATED ART

PROCESS A1		PROCESS B1				PROCESS C1
OPERATION	UPDATE XBID	CONDITIONS	TM FLAG	BLEN	BID	
		ТМ	1	0		REGISTER BID IN FQL REGISTER BPNT IN FQL REGISTER BLEN IN FQL
REAL READ XBID + 1	OTHERS	0	LEN	XBID	REGISTER TM FLAG IN FQL UPDATE FQP POINTER BPNT - BPNT + BLEN	

FIG. 186 RELATED ART

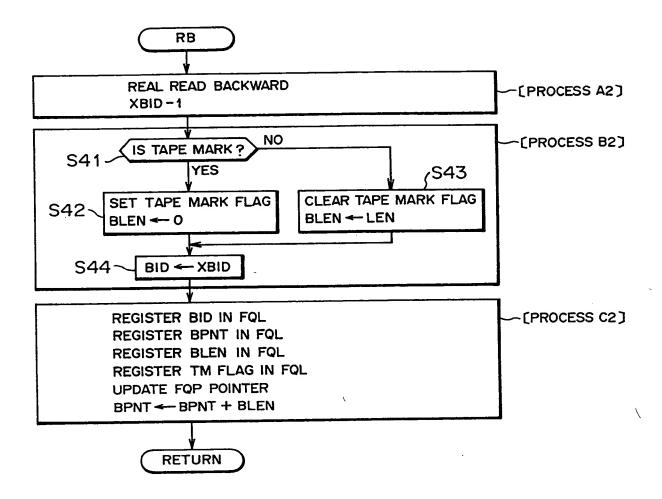


FIG. 187 RELATED ART

PROCESS A2		PROCESS B2				PROCESS C2	
OPERATION	UPDATE XBID	CONDITIONS	TM FLAG	BLEN	BID		
REAL READ BACKWARD XBID - 1	ТМ	1	0	VDID	REGISTER BID IN FQL REGISTER BPNT IN FQL REGISTER BLEN IN FQL		
	XBID - 1	OTHERS	0	LEN	XBID	REGISTER TM FLAG IN FQL UPDATE FQP POINTER BPNT - BPNT + BLEN	

FIG. 188 RELATED ART

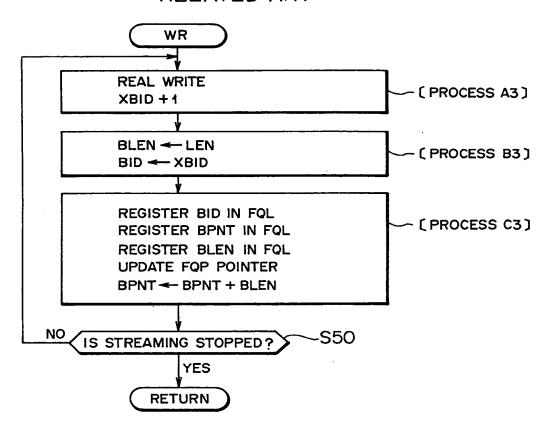


FIG. 189 RELATED ART

PROCESS A3		PROCESS B3				PROCESS C3	
OPERATION	UPDATE XBID	CONDITIONS	TM FLAG	BLEN	BID		
REAL WRITE	XBID + 1			LEN	XBID	REGISTER BID IN FQL REGISTER BPNT IN FQL REGISTER BLEN IN FQL UPDATE FQP POINTER BPNT - BPNT + BLEN	

FIG. 190 RELATED ART

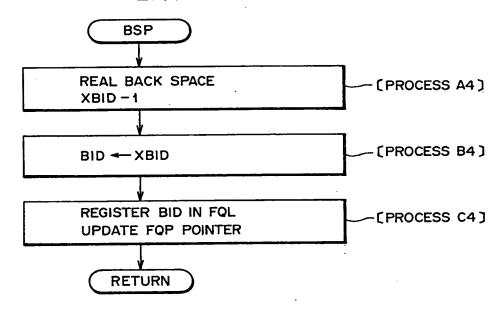


FIG. 191 RELATED ART

PROCESS A4		PROCESS B4				PROCESS C4
OPERATION	UPDATE XBID	CONDITIONS	TM FLAG	BLEN	BID	11(0,0200 0 1
REAL BACK SPACE	XBID-1					REGISTER BID IN FQL UPDATE FQP POINTER

FIG. 192 RELATED ART

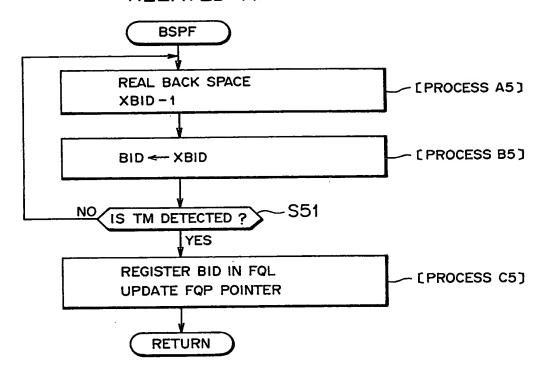


FIG. 193 RELATED ART

PROCESS A5		PF	OCESS B5	PROCESS C5		
OPERATION	UPDATE XBID	CONDITIONS	TM FLAG	BLEN	BID	FROCESS CS
REAL BACK SPACE	XBID-1				XBID	REGISTER BID IN FQL UPDATE FQP POINTER

FIG. 194 RELATED ART

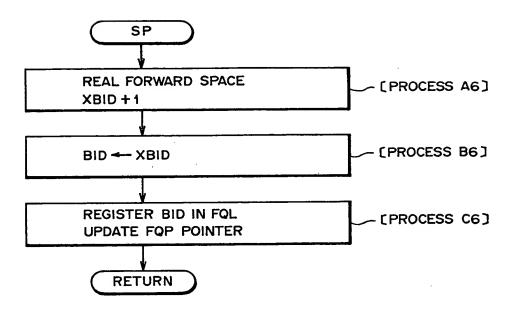


FIG. 195 RELATED ART

PROC	ESS A6	PR	OCESS BE	3		PROCESS C6
OPERATION	UPDATE XBID	CONDITIONS	TM FLAG	BLEN	BID	1 KOOLOO GO
REAL FORWARD SPACE	XBID+1				XBID	REGISTER BID IN FQL UPDATE FQP POINTER

FIG. 196 RELATED ART

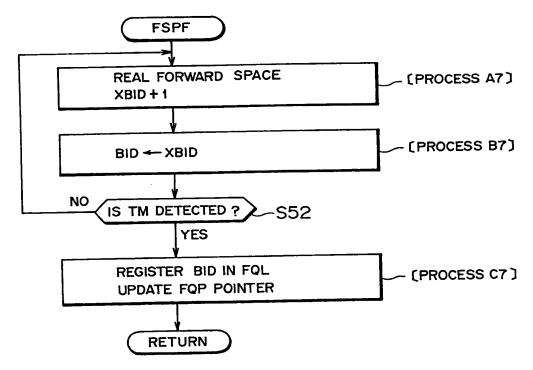


FIG. 197 RELATED ART

PROCI	ESS A7	PF	ROCESS B	7		PROCESS C7
OPERATION	UPDATE XBID	CONDITIONS	TM FLAG	BLEN BID		
REAL FORWARD SPACE	XBID+1				XBID	REGISTER BID IN FQL UPDATE FQP POINTER

FIG. 198 RELATED ART

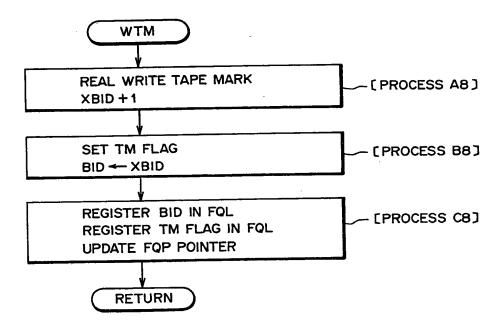
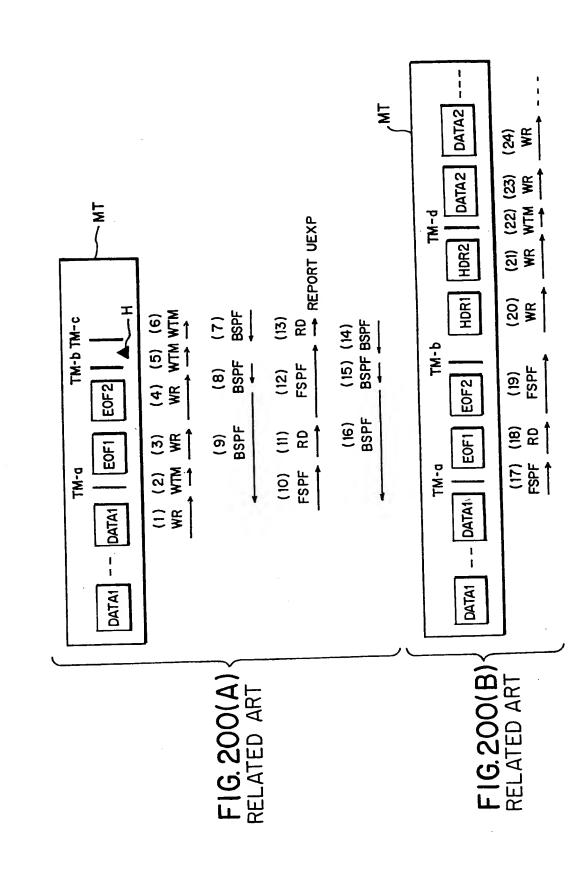
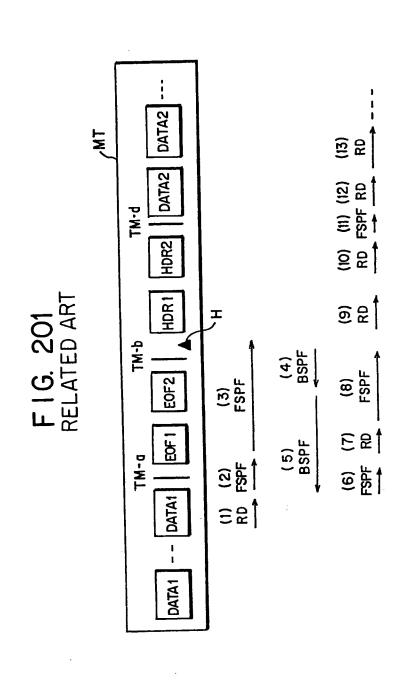


FIG. 199 RELATED ART

PROCI	ESS A8	PR	OCESS BE	3		PROCESS C8
OPERATION	UPDATE XBID	CONDITIONS	TM FLAG	BLEN	BID	
REAL WRITE TAPE MARK	XBID+1		1		XBID	REGISTER BID IN FQL REGISTER TM FLAG IN FQL UPDATE FQP POINTER





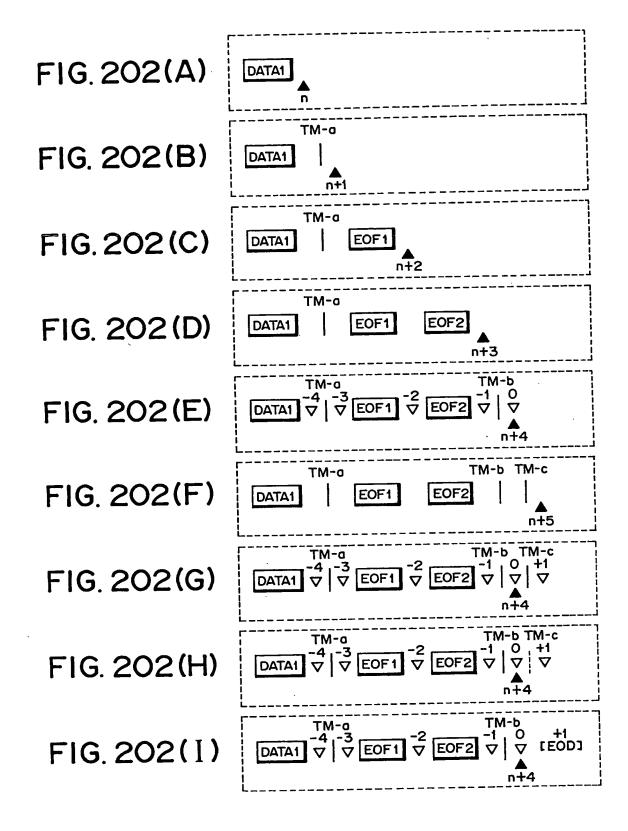
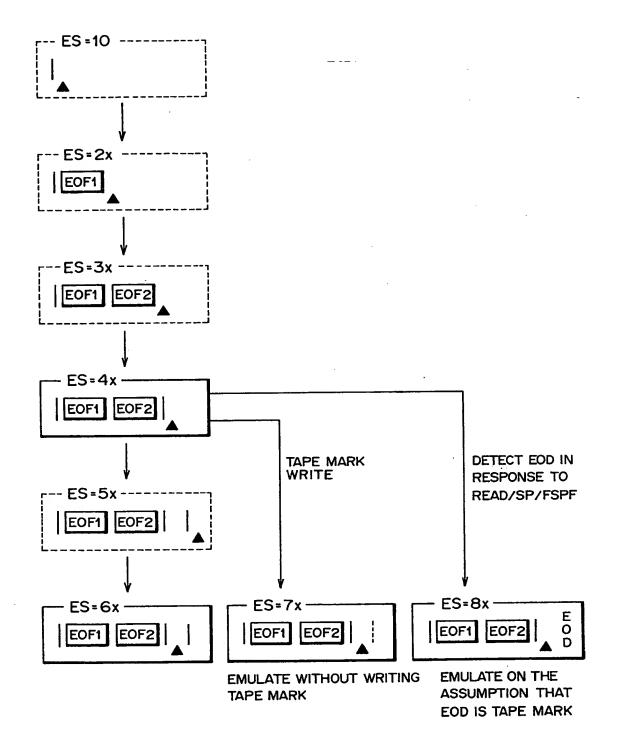


FIG. 203



	PRE	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
	-	70311101	===	REAL	TM		SBID-REAL BID	10		1.00.00
00					OTHERS			00		1. 00. 01
					EOF	SDT1	••	20		1.10.00
10				REAL	TM		SBID+-REAL BID	10		1. 10. 01
10				READ	OTHERS		æ=	00		1. 10. 02
			 		EOF	SDT2	••	30		1. 20. 00
20				REAL	TM		SBID←REAL BID	10		1. 20. 01
20				READ	OTHERS			00		1. 20. 02
	-			REAL	TM		SBIDREAL BID WHEN (REAL BID)-(SBID)=3	40	0	1. 30. 00
30				READ	OTHERS			00	-	1. 30. 01
├	-4			EMULATE				40	-3	1. 40. 00
1	-3		SDT1	EMULATE			•••••••••••••••••••••••••••••••••••••••	40	-2	1. 40. 10
1	-2		SDT2	EMULATE			=-	40	-1	1. 40. 20
	-1			EMULATE				40	0	1. 40. 30
40	1 1				EOF	SDT1		20		1. 40. 40
				REAL	TM		SBID-REAL BID WHEN (REAL BID)-(SBID)=1	50		1. 40. 41
1	0	·		READ	OTHER	S		00		1. 40. 42
					EOD			80	+1	1. 40. 43
\vdash	+		_		EOF	SDT1		20)	1. 50. 00
50	. ا			REAL	TM	T	SBID-REAL BID	10)	1. 50. 01
"				READ	OTHER	s		0) <u>-</u> -	1. 50. 02
-	+-		 	EMULATE				6	0 -	3 1.60.00
	-		SDT1	EMULATE				6	0 -:	2 1. 60. 10
	-		SDT		}			6	0 -	1 1.60.20
				EMULATE				6	0	0 1. 60. 30
6	0	0		EMULATE				6	0 +	1 1. 60. 40
		·			EOF	SDT1	SBID+1	2	0 -	- 1.60.50
		1 SBID+E	:р	REAL	TM		SBID-REAL BID	1	0 -	- 1. 60. 51
		1 301011		READ	OTHE	RS		0	0 -	- 1. 60. 52

<u> </u>	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP		RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
F	-4			EMULATE				70	-3	1. 70. 00
	-3		SDT1	EMULATE			###	70	-2	1. 70. 10
	-2		SDT2	ENULATE			~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	70	-1	1. 70. 20
	-1			ENULATE				70	0	1. 70. 30
70	0			ENULATE	······································			70	+1	1. 70. 40
			ļ		EOF	SDT1	\$BID+1	20		1. 70. 50
	+1	WTM		REAL	TH		SBID←REAL BID	10		1. 70. 51
	*'	* ***		READ	OTHERS			00		1. 70. 52
-	-4	 	 	EMULATE				80	-3	1. 80. 00
	-3		SDT1	ENULATE				80	-2	1. 80. 10
	ļ		SDT2	EMULATE		ļ <u></u>	**************************************	80	-1	1. 80. 20
80	-2 -1		3012	EMULATE		 		80	0	1. 80. 30
		ļ <u></u>		EMULATE		 	##	80	+1	1. 80. 40
	+1			REAL READ	EOD			00		1. 80. 50

ſ ·	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	욘	ID
31				REAL	TM		SBID⊶REAL BID WHEN (REAL BID)-(SBID)=3	41	0	1. 31. 00
31				READ	OTHERS		••	00		1. 31. 01
	-4			EMULATE				41	-3	1. 41. 00
	-3		SDT1	EMULATE				41	-2	1. 41. 10
		SB1D+EP		REAL	EOF	SDT2	SBID-3	30		1. 41. 20
	-2	SBIDHER		READ	OTHERS		***************************************	00		1. 41. 21
41	-1			EMULATE				41	0	1. 41. 30
					EOF	SDT1	••	20		1. 41. 40
!	0			REAL	TM		SBID→REAL BID WHEN (REAL BID)-(SBID)=1	51		1. 41. 41
	١			READ	OTHERS			00		1. 41. 42
					E0D		~~	81	+1	1. 41. 43
				·	EOF	SDT1		20		1. 51. 00
51				REAL READ	TM		SBID-REAL BID	10		1. 51. 01
					OTHERS	. 		00		1. 51. 02
	-4			EMULATE				61	-3	1. 61. 00
	-3		SDT1	EMULATE			**	61	-2	1. 61. 10
		CD D CD		REAL	EOF	SDT2	SBID-3	30		1. 61. 20
	-2	SBIDHEP	<u> </u>	READ	OTHERS		••	00		1. 61. 21
61	-1			ÉMULATE				61	0	1. 61. 30
	0			EMULATE				61	+1	1. 61. 40
			İ		EOF	SDT1	SBID+1	20		1. 61. 50
	+1	SBID+EP		REAL READ	TM		SBID-REAL BID	10		1. 61. 51
					OTHERS			00		1. 61. 52

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	€P	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
	-4			EMULATE				71	-3	1. 71. 00
	-3		SDT1	EMULATE			an an	71	-2	1.71.10
		WTM		REAL	EOF	SDT2	SB1D-3	30		1. 71. 20
	-2	SBIDHEP		READ	OTHERS		==	00		1. 71. 21
71	-1			EMULATE			======================================	71	0	1. 71. 30
	0			EMULATE				71	+1	1. 71, 40
					EOF	SDT1		20		1. 71. 50
	+1	WTM		REAL READ	TM		SBID-REAL BID	10		1. 71. 51
					OTHERS			00		1. 71. 52
	-4			EMULATE			••	81	-3	1. 81. 00
1	-3		SDT1	EMULATE				81	-2	1. 81. 10
			**********	REAL.	EOF	SDT2	S81D-3	30		1. 81. 20
81	-2	S81D+EP		READ	OTHERS			00		1. 81. 21
	-1			EMULATE				81	0	1. 81. 30
	0			EMULATE				81	+1	1. 81. 40
	+1			REAL READ	EOD			00		1. 81. 50

	PRF	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE-	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
	_	PUSTITUN			EOF	SDT2		32		1. 22. 00
22				REAL	TM		SBID-REAL BID	10		1. 22. 01
11				READ	OTHERS			00		1. 22. 02
				REAL	TM		SBID-REAL BID WHEN (REAL BID)-(SBID)=3	42	0	1. 32. 00
32				READ	OTHERS			00		1. 32. 01
	-4			EMULATE			W.P.	42	-3	1. 42. 00
				REAL	EOF	SDT1	SBID-3	20		1. 42 10
	-3	SBID+EP		READ	OTHERS		**************************************	00		1. 42. 11
	-2		SDT2	EMULATE				42	-1	1. 42. 20
١	-1	ļ		EMULATE				42	0	1. 42. 30
42					EOF	SDT1	B. T.	20		1. 42. 40
				REAL	TM		SBID-REAL BID WHEN (REAL BID)-(SBID)=1	52	-	1. 42. 41
	0	-		READ	OTHERS	S	mp	00) -	1. 42. 42
					EOD			82	2 +	1 1.42.43
-	-	 	+		EOF	SDT1		20	0 -	- 1.52.00
52	,			REAL	TM		SBID←REAL BID	11	0 -	- 1. 52. 01
34			Ì	READ	OTHER	s		0	0 -	- 1. 52. 02
-	-		 	EMULATE		 		6	2 -	3 1. 62. 00
					EOF	SDT1	SB1D-3	2	0	- 1.62.10
	-:	SBID+E	Р	REALREA	OTHER	s		0	0	- 1.62.11
			SDT	2 EMULATI				<u> </u>	2	-1 1. 62. 20
6				EMULATI	<u> </u>		#B	6	2	0 1. 62. 30
"	-	<u> </u>		EMULATI	E		= #		32	+1 1.62.40
					EOF	SDT	1 \$810+1		20	1. 62. 50
	1	1 SBID+F	e	REALREA	MT Q		SBID-REAL BID		10	1. 62. 51
		' "		ļ	OTHE	RS			00	1. 62. 57

				— т			POST - PROCESS			PROCESS
	PRE	- PROCE	SS	OPERATION				ES	EP	ID
ES	EP	RE- POSITION	RESTORE	G Dallia	BLOCK	SAVE	BID PROCESS			
	-4			EMULATE			### ##################################	72	-3	1. 72. 00
					E0F	SDT1	SB1D-3	20		1. 72. 10
	-3	WTM SBID+EP		REAL READ	OTHERS		###	00		1.72.11
			SDT2	EMULATE			······································	72	-1	1. 72. 20
	-2		3012	EMULATE			**************************************	72	0	1.72.30
72	-1			EMULATE				72	+1	1. 72. 40
	0			CHOCKIC	EOF	SDT1	SBID+1	20		1. 72. 50
ļ		,,,,,,		REAL.	TM		SBID-REAL BID	10		1. 72. 51
	+1	MTM		READ	OTHERS		~=	00		1. 72. 52
-	╀.	 		EMULATE			••	82	-3	1. 82. 00
1	-4				EOF	SDT1	\$81D-3	20		1. 82. 10
	-3	SB1D+EP		REAL READ	OTHERS			00		1. 82. 11
	ļ					<u></u>	— comos rocessos acessos 82	2 -1	1. 82. 20	
82	2 -2		SDT2	EMULATE		+		82	2 0	1. 82. 30
	-1			EMULATE	ļ 				2 +1	1. 82. 40
-				EMULATE			-			- 1. 82. 50
	+1			REAL READ	EOD		••	0	0	1. 62 30

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<u> </u>	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE-	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
=		100111011		REAL	TN		SBID+-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	1. 33. 00
33				READ	OTHERS		***	00		1. 33. 01
-	-4			EMULATE				43	-3	1. 43. 00
				REAL	E0F	SDT1	SBID-3	20		1. 43. 10
	-3	SB I D+EP		READ	OTHERS			00		1. 43. 11
				REAL	EOF	SDT2	SB1D-3	32		1. 43. 20
	-2	SB1D+EP		READ	OTHERS		#=	00		1. 43. 21
43	-1			EMULATE				43	0	1. 43. 30
					EOF	SDT1		20		1. 43. 40
				REAL	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)=1	53		1. 43. 41
	0			READ	OTHERS			00		1. 43. 42
	1	Ì			EOD			83	+1	1. 43. 43
\vdash			 	<u> </u>	EOF	SDT1		20		1. 53. 00
53				REAL READ	TM		SBID-REAL BID	10	ļ	1. 53. 01
				1.2.5	OTHERS			00	<u> </u>	1. 53. 02
	-4			EMULATE				63	-3	1. 63. 00
1		†····		REAL	EOF	SDT1	SB1D-3	20		1. 63. 10
-	-3	SBID+EP		READ	OTHERS		# G	00		1. 63. 11
1				REAL	EOF	SDT2	SB1D-3	32		1. 63. 20
	-2	SB1D+EP	'	READ	OTHERS		### ##################################	00		1. 63. 21
63	-1			EMULATE				63	0	1. 63. 30
	0			EMULATE			**	63	+1	1. 63. 40
			· ·····		EOF	SDT1	SBID+1	20)	1. 63. 50
	+1	SBID+EF	·	REAL	TM		SBID-REAL BID	10)	1. 63. 51
					OTHERS		••	00)	1. 63. 52

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
	-4			ENULATE				73	-3	1. 73. 00
		MTM		REAL.	EOF	SDT1	SBID-3	20		1. 73. 10
	-3	SB I D+EP		READ	OTHERS			00		1. 73. 11
1	_	WTM	***************************************	REAL	EOF	SDT2	SB1D-3	32		1. 73. 20
	-2	SB1D+EP		READ	OTHERS			00		1. 73. 21
73	-1			EMULATE				73	0	1. 73. 30
	0			EMULATE				73	+1	1. 73. 40
	·····				EOF	SDT1	SB1D+1	20		1. 73. 50
	+1	WTM		REAL READ	TM		SBID-REAL BID	10		1. 73. 51
				I ILAD	OTHERS			00		1. 73. 52
	-4			EMULATE				83	-3	1. 83. 00
				REAL	EOF	SDT1	\$81D-3	20		1. 83. 10
	-3	SB1D+EP		READ	OTHERS			00		1. 83. 11
				REAL	EOF	SDT2	SBID-3	32		1. 83. 20
83	-2	SBIDHEP		READ	OTHERS			00		1. 83. 21
	-1			EMULATE				83	0	1. 83. 30
	0			EMULATE				83	+1	1. 83. 40
	+1	+-		REAL READ	EOD			00		1. 83. 50

F	PRE - PROCESS		SS				POST PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	巴	ID
00				REAL RB				00		2. 00. 00
10				REAL RB			••	00		2. 10. 00
20				REAL RB				00		2. 20. 00
30	• • •			REAL RB				00		2. 30. 00
	-4	SB1D+EP		REAL RB				00		2. 40. 00
	-3			EMULATE				40	-4	2. 40. 10
40	-2			ENULATE				40	-3	2. 40. 20
	-1			EMULATE			**************************************	40	-2	2. 40. 30
	0			EMULATE			**************************************	40	-1	2. 40. 40
50				REAL RB	TM		SBID←REAL BID WHEN (SBID)-(REAL BID)=1	60	0	2. 50. 00
	-4	SB1D+EP		REAL RB				00		2. 60. 00
	-3			EMULATE			* *	60	-4	2. 60. 10
	-2			EMULATE			wa	60	-3	2. 60. 20
60	-1			EMULATE			•••	60	-2	2. 60. 30
	0			EMULATE				60	-1	2. 60. 40
	+1			EXULATE			==	60	0	2. 60. 50
	-4	WTM SBID+EP		REAL RB			••	00		2. 70. 00
	-3			EMULATE				70	-4	2. 70. 10
70	-2			EMULATE				70	-3	2. 70. 20
	-1			EMULATE				70	-2	2. 70. 30
	0			EMULATE		 	••••••••••••••••••••••••••••••••••••••	70	-1	2. 70. 40
	+1			EMULATE				70	0	2. 70. 50
	-4	SB1D+EP		REAL RB				00		2. 80. 00
	-3			EMULATE				80	-4	2.80.10
	-2			EMULATE			••	80	-3	2. 80. 20
80	-1			EMULATE				80	-2	2. 80. 30
	0			EMULATE				80	-1	2. 80. 40
	+1			EMULATE				80	0	2. 80. 50

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	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	1D
31				REAL RB				00		2. 31. 00
	-4	SBID+EP		REAL RB				00		2. 41. 00
	-3			EMULATE				41	-4	2.41.10
41	-2			EMULATE				41	-3	2.41.20
	-1			EMULATE			***	41	-2	2. 41. 30
	0			EMULATE			**************************************	41	-1	2. 41. 40
51		·		REAL RB	TM		SBIDREAL BID WHEN (SBID)-(REAL BID)=1	61	0	-2. 51. 00
	-4	SB1D+EP		REAL R8				00		2. 61. 00
	-3			EMULATE			» a	61	-4	2. 61. 10
	-2			EMULATE			± 4	61	-3	2. 61. 20
61	-1			EMULATE				61	-2	2. 61. 30
	0			EMULATE			~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	61	-1	2. 61. 40
	+1			EMULATE		 		61	0	2. 61. 50
	-4	WTM SB1D+EP		REAL RB				00		2. 71. 00
	-3			EMULATE				71	-4	2. 71. 10
71	-2			EMULATE			**	71	-3	2. 71. 20
	-1			EMULATE		T		71	-2	2. 71. 30
	0			EMULATE			**	71	-1	2. 71. 40
	+1			EMULATE			**	71	0	2. 71. 50
	-4	SB1D+EP		REAL RB				00		2. 81. 00
	-3	 		EMULATE			••	81	-4	2. 81. 10
	-2			EMULATE			••	81	-3	2. 81. 20
81	-1			EMULATE				81	-2	2. 81. 30
	0	†·····		EMULATE				81	-1	2. 81. 40
	+1			ENULATE				81	0	2. 81. 50

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
32				REAL RB	•	•		00		2 32 00
	-4	SB1D+EP		REAL RB				00		2.42.00
	-3			EMULATE			······································	42	-4	2 42 10
42	-2			EMULATE			**************************************	42	-3	2.42.20
	-1			EMULATE				42	-2	2. 42. 30
	0			EMULATE			**************************************	42	-1	2.42.40
52				REAL RB	TM		SBID←REAL BID WHEN (SBID)-(REAL BID)=1	62	0	2. 52. 00
	-4	SB1D+EP		REAL RB			••	00		2 62 00
	-3			ENULATE			**************************************	62	-4	2 62 10
1	-2			EMULATE				62	-3	2. 62. 20
62	-1			EMULATE				62	-2	2. 62. 30
	0			EMULATE				62	-1	2.62.40
	+1			EMULATE			••	62	0	2. 62. 50
	-4	WTM SBID+EP		REAL RB				00		2. 72. 00
	-3			EMULATE				72	-4	2.72.10
72	-2			EMULATE			•••	72	-3	2. 72. 20
1	-1			EMULATE				72	-2	2 72 30
	0			EMULATE		 		72	-1	2 72 40
	+1			EMULATE				72	0	2. 72. 50
	-4	SBID+EP		REAL RB				00		2. 82. 00
	-3			EMULATE				82	-4	2 82 10
	-2			EMULATE				82	-3	2. 82. 20
82	-1			EMULATE				82	-2	2. 82. 30
	0			EMULATE				82	-1	2 82 40
	+1			EMULATE				82	0	2. 82. 50

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	B	ID
33		••		REAL RB				00	-	2. 33. 00
	-4	SB I D+EP		REAL RB				00		2. 43. 00
	-3			EMULATE			##	43	-4	2. 43. 10
43	-2			ENULATE				43	-3	2. 43. 20
	-1			EMULATE			**************************************	43	-2	2. 43. 30
	0			EMULATE				43	-1	2. 43. 40
53				REAL RB	TM		SBID←REAL BID WHEN (SBID)-(REAL BID)=1	63	0	2. 53. 00
	-4	SBIDHEP		REAL RB				00		2. 63. 00
	-3			ENULATE				63	-4	2. 63. 10
	-2			EMULATE			***	63	-3	2. 63. 20
63	-1			EMULATE				63	-2	2. 63. 30
	0			EMULATE				63	-1	2. 63. 40
	+1			EMULATE				63	0	2. 63. 50
	-4	WTM SBID+EP		REAL RB				00		2. 73. 00
	-3			EMULATE				73	-4	2. 73. 10
73	-2			EMULATE				73	-3	2. 73. 20
	-1			EMULATE				73	-2	2. 73. 30
	0			EMULATE				73	-1	2. 73. 40
	+1			EMULATE				73	· 0	2. 73. 50
	-4	SB1D+EP		REAL RB				00		2. 83. 00
	-3			EMULATE				83	-4	2. 83. 10
83	-2			EMULATE				83	-3	2. 83. 20
	-1			EMULATE				83	-2	2. 83. 30
	0			EMULATE				83	-1	2. 83. 40
	+1	. ••		EMULATE				83	0	2. 83. 50

	PRF	- PROCE	SS				POST - PROCESS			PR	OCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	_	ID
00				REAL WR			••	00		+-	00.00
				REAL	EOF	SDT1		20			10. 00
10		,		WR	OTHERS		as	00		3	. 10. 01
	-		 	754	EOF	SDT2		30		3	. 20. 00
20				REAL WR	OTHERS			00	Ľ	3	. 20. 01
30	-			REAL WR				00		. 3	. 30. 00
30	-		 	REAL			·	00		- :	3. 40. 00
	-4			WR	EOF	SDT1	\$81D-3	20	-	-	3. 40. 10
	-3	1		REAL WR	OTHERS			00	-	-	3. 40. 11
	ļ	SB I D+EF			EOF	SDT2	SBID-3	30	-	-	3. 40. 20
40	-2			REAL WR	OTHERS			00		-	3. 40. 21
	-	1		REAL				00) .		3. 40. 30
	-1			WR	EOF	SDT		20)		3. 40. 40
1	1)		REAL.	OTHER	s		0			3. 40. 41
-			 	REAL	EOF	SDT		2	0		3. 50. 00
5	0 -	-		WR	OTHER	s		0	0		3. 50. 01
-	+		- 	REAL	1			0	0		3. 60. 00
		4		WR	EOF	SDT	1 SBID-3	2	0		3. 60. 10
	-	3		REAL WR	OTHE			0	0.		3. 60. 11
	-	SB1D+	₽		EOF		SBID-3	3	30		3. 60. 20
	-	-2		REAL WR	OTHE				00		3. 60. 21
	60 			REAL		-			00		3. 60. 30
1		-1		WR	EOF	SD	T1		20		3. 60. 40
1		0		REAL WR	OTHE				00		3. 60. 41
	.								20		3. 60. 50
		+1 SBID		KEAL	-		-		00		3. 60. 51
L					UIN	-~					

_	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
	-4			REAL WR				00		3. 70. 00
				REAL	EOF	SDT1	SBID-3	20		3. 70. 10
	-3			WR	OTHERS		. ==	00		3. 70. 11
		SB1D+EP		REAL	EOF	SDT2	SBID-3	30		3. 70. 20
	-2			WR	OTHERS			00		3. 70. 21
70	-1			REAL. WR			~~	00		3. 70. 30
-				REAL	EOF	SDT1		20		3. 70. 40
•	0			WR	OTHERS		**************************************	00		3. 70. 41
				REAL	EOF	SDT1	SBID+1	20		3. 70. 50
	+1	WTM		WR	OTHERS		**************************************	00		3. 70. 51
	-4			REAL WR				00		3. 80. 00
				REAL	EOF	SDT1	SBID-3	20		3. 80. 10
	-3			WR	OTHERS			00		3. 80. 11
		SBID+EP		REAL	EOF	SDT2	SBID-3	30		3. 80. 20
İ	-2			WR	OTHERS			00		3. 80. 21
80	-1			REAL WR				00		3. 80. 30
	}		†	REAL	EOF	SDT1		20		3. 80. 40
	0			WR	OTHERS			00		3. 80. 41
				REAL	EOF	SDT1	SBID+1	20		3. 80. 50
	+1	WTM		WR	OTHERS		***************************************	00		3. 80. 51

		- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
31				REAL WR				00		3. 31. 00
	-4			REAL WR			***************************************	00		3. 41. 00
				REAL	EOF	SDT1	SBID-3	20		3. 41. 10
	-3			WR	OTHERS			00	ļ	3. 41. 11
	ļ	SB1D+EP		REAL	EOF	SDT2	SBID-3	30	ļ .	3. 41. 20
41	-2			WR	OTHERS		w #b	00	ļ. <u></u>	3. 41. 21
	-1	1		REAL WR				00		3. 41. 30
		-		REAL	EOF	SDT1		20		3. 41. 40
	0			WR	OTHERS			00	<u> </u>	3. 41. 41
-	+	 	+	REAL	EOF	SDT1		20		3. 51. 00
51				WR	OTHERS			00)	3. 51. 01
-	-4		 	REAL	†			00) -	
				REAL	EOF	SDT1	SBID-3	20) -	- 3.61.10
	-3	3		WR	OTHERS	S		0	0 -	- 3.61.11
	}	SBID+E	·	REAL	EOF	SDT2	SBID-3	3	0 -	- 3. 61. 20
		2		WR	OTHER	s	###	0	0 -	- 3. 61. 21
6	1	1		REAL WR				0	0	- 3. 61. 30
	}			REAL	EOF	SDT		2	0	3. 61. 40
		0		WR	OTHER	s		0	0	3. 61. 41
				REAL	EOF	SDT	SBID+1	2	20	3. 61. 50
	+	1 SBIDH	₽		OTHER	rs		(00	3. 61. 51

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
	-4			REAL. WR				00		3. 71. 00
				REAL	EOF	SDT1	SBID-3	20		3. 71. 10
	-3			WR	OTHERS			00		3.71.11
		SB1D+EP		REAL	EOF	SDT2	SBID-3	30		3. 71. 20
	-2			WR	OTHERS	-	==	00		3. 71. 21
71	-1			REAL WR				00		3761. 30
	ļ		<u> </u>	REAL	EOF	SDT1		20		3. 71. 40
	0			WR	OTHERS			00		3. 71. 41
	·····			REAL	EOF	SDT1	SBID+1	20		3. 71. 50
	+1	WTM		WR	OTHERS		***************************************	00		3. 71. 51
	-4			REAL WR				00		3. 81. 00
	·····	1	***************************************	REAL	EOF	SDT1	SB1D-3	20		3. 81. 10
	-3			WR	OTHERS			00		3. 81. 11
	ļ	SB1D+EP		REAL	EOF	SDT2	SBID-3	30		3. 81. 20
	-2			WR	OTHERS			00		3. 81. 21
81	-1	1		REAL WR				00		3. 81. 30
	 	†		REAL	EOF	SDT1		20		3. 81. 40
	0			WR	OTHERS			00	<u> </u>	3. 81. 41
			· · · · · · · · · · · · · · · · · · ·	REAL	EOF	SDT1	SBID+1	20	<u> </u>	3. 81. 50
	+1	MTW		WR	OTHERS		e==	00		3. 81. 51

	PRE	- PROCE	SS		_		POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
				REAL	EOF	SDT2	##	32		3. 22. 00
22				WR	OTHERS			00		3. 22. 01
32				REAL WR				00		3. 32. 00
	-4			REAL. WR				00		3. 42. 00
				REAL	EOF	SDT1	SBID-3	20		3. 42. 10
ļ	-3	-	···	WR	OTHERS		***	00		3. 42. 11
		SB I D+EP		REAL	EOF	SDT2	SBID-3	32		3. 42. 20
42	-2			WR	OTHERS			00		3. 42. 21
	-1			REAL WR				00		3. 42. 30
				REAL	EOF	SDT1		20		3. 42. 40
	0			WR	OTHERS			00		3. 42. 41
	-			REAL	EOF	SDT1	••	20		3. 52. 00
52				WR	OTHERS		ma	00		3. 52. 01
	-4			REAL				00		3. 62. 00
	ļ	1		REAL	EOF	SDT1	SB1D-3	20		3. 62. 10
	-3			₩R	OTHERS			00		3. 62. 11
		SB1D+EP		REAL	EOF	SDT2	S81D-3	32		3. 62. 20
	-2			WR	OTHERS		***************************************	00		3. 62. 21
62	-1	1		REAL WR				00		3. 62. 30
		.		REAL	EOF	SDT1		20		3. 62. 40
	0			- WR	OTHERS	;		00		3. 62. 41
				REAL	EOF	SDT1	SBID+1	20		3. 62. 50
	+1	SBID+EF	·	WR	OTHER	S		00	-	3. 62. 51

Г	PRE	- PROCE	SS				POST PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
	-4			REAL WR			••	00		3. 72. 00
				REAL	EOF	SDT1	SB1D-3	20		3. 72. 10
İ	-3			WR	OTHERS			00		3. 72. 11
		SB1D+EP		REAL	EOF	SDT2	\$81D-3	32		3. 72. 20
	-2			WR	OTHERS		##	00		3. 72. 21
72	-1			REAL WR				00		3. 72. 30
				REAL	EOF	SDT1	±=	20		3. 72. 40
	0			WR	OTHERS			00		3. 72. 41
				REAL	EOF	SDT1	SBID+1	20		3. 72. 50
	+1	MIM		WR	OTHERS			00		3. 72. 51
	-4			REAL.				00		3. 82. 00
		1		REAL	EOF	SDT1	SBID-3	20		3. 82. 10
	-3			WR	OTHERS			00		3. 82. 11
	ļ	SBID+EP		REAL	EOF	SDT2	SBID-3	32		3. 82. 20
	-2	i ·		WR	OTHERS			00		3. 82. 21
82	-1			REAL WR			-	00	<u> </u>	3. 82. 30
1		†		REAL	EOF	SDT1		20		3. 82. 40
}	0			WR	OTHERS			00		3. 82. 41
	-			REAL	EOF	SDT1	SBID+1	20		3. 82. 50
	+1	WTM		WR	OTHERS			00		3. 82. 51

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
33			•	REAL WR		•		00	1	3. 33. 00
	-4			REAL ₩R	•			00		3. 43. 00
	_			REAL	EOF	SDT1	SBID-3	20		3. 43. 10
	-3			WR	OTHERS			00		3. 43. 11
	_	SB1D+EP	•••••••	REAL	EOF	SDT2	SBID-3	32		3. 43. 20
43	-2	i		WR	OTHERS			00		3. 43. 21
	-1			REAL WR				00		3. 43. 30
				REAL	EOF	SDT1		20		3. 43. 40
	0			WR	OTHERS			00		3. 43. 41
				REAL	EOF	SDT1		20		3. 53. 00
53				WR	OTHERS			00		3. 53. 01
	-4			REAL WR				00		3. 63. 00
				REAL	EOF	SDT1	SBID-3	20		3. 63. 10
	-3			WR	OTHERS			00		3. 63. 11
		SB1D+EP		REAL	EOF	SDT2	SBID-3	32		3. 63. 20
	-2			WR	OTHERS			00		3. 63. 21
63	-1			REAL WR				00		3. 63. 30
			********	REAL.	EOF	SDT1		20		3. 63. 40
	0			WR	OTHERS		••	00		3. 63. 41
				REAL	EOF	SDT1	SBID+1	20		3. 63. 50
	+1	SB1D+EP		WR	OTHERS			00		3. 63. 51

	DDE	- PROCE	22				POST - PROCESS			PROCESS
ES	EP	RE-	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
	-4	POSITION		REAL			••	00		3. 73. 00
				WR	EOF	SDT1	SB1D-3	20		3, 73, 10
	-3			REAL WR	OTHERS			00		3. 73. 11
		SBID+EP		REAL	EOF	SDT2	SB1D-3	32		3. 73. 20
	-2			WR	OTHERS			00		3. 73. 21
73	-1			REAL WR			me	00		3. 73. 30
	\			REAL	EOF	SDT1	**************************************	20		3. 73. 40
	0			WR	OTHERS		a	00		3. 73. 41
	 			REAL	EOF	SDT1	SBID+1	20		3. 73. 50
	+1	MTM		WR	OTHERS			00	<u> </u>	3. 73. 51
-	-4		 	REAL				00		3. 83. 00
	}	-{		REAL	EOF	SDT1	SBID-3	20		3. 83. 10
	-3			WR	OTHERS			00		3. 83. 11
		- SBID+EF	·	REAL	EOF	SDT2	SBID-3	32		3. 83. 20
	-2	:		WR	OTHERS			00	 	
83	3	L		REAL. WR				00		3. 83. 30
	-	·		REAL	EOF	SDT1		20	-	3. 83. 40
	()		WR	OTHER	s	***************************************	00) -	
	ļ			REAL	EOF	SDT1	SBID+1	20) -	
	+:	MTM		WR	OTHER	s		0	0 -	- 3. 83. 51

FIG. 224

ES EP 00 10 20 30 40 -2 -1 0 50 -4	- PROCE RE- POSITION SBID+EP	RESTORE	REAL BSP REAL BSP REAL BSP REAL BSP REAL BSP REAL BSP	BLOCK		BID PROCESS	00 00 10 20	 	4. 00. 00 4. 10. 00 4. 20. 00 4. 30. 00
10 20 30 -4 -3 40 -2 -1 0 50	 SBID+EP 	 	BSP REAL BSP REAL BSP REAL BSP REAL BSP REAL BSP REAL BSP REAL BSP				00		4. 10. 00 4. 20. 00
10 20 30 -4 -3 40 -2 -1 0 50	 SBID+EP 	 	REAL BSP REAL BSP REAL BSP REAL BSP EMULATE				10		4. 20. 00
30 -4 -3 40 -2 -1 0 50	SBID+EP	 	REAL BSP REAL BSP REAL BSP EMULATE				-		
-4 -3 40 -2 -1 0 50	SBID+EP		REAL BSP REAL BSP EMULATE			<u></u>	20		4 30 00
-4 -3 40 -2 -1 0 50			REAL BSP EMULATE						7. 30. 00
40 -2 -1 0 50		 	EMULATE				00		4. 40. 00
40 -2 -1 0 50	†	 	EMULATE				40	-4	4. 40. 10
-1 0 50	†						40	-3	4. 40. 20
50			EMULATE				40	-2	4. 40. 30
50	 		EMULATE				40	-1	4. 40. 40
			REAL BSP	MT		SBID-REAL BID WHEN (SBID)-(REAL BID)=1	60	0	4. 50. 00
	SB1D+EP		REAL BSP			•-	00		4. 60. 00
-3			EMULATE				60	-4	4. 60. 10
-2	·		EMULATE				60	-3	4. 60. 20
60	ļ		EMULATE		†		60	-2	4. 60. 30
0			EMULATE				60	-1	4. 60. 40
+1			EMULATE				60	0	4. 60. 50
-4	WTM SBID+EP		REAL BSP				00		4. 70. 00
-3			EMULATE				70	-4	4. 70. 10
70 -2			EMULATE				70	-3	4. 70. 20
-1	· † ······		EMULATE			•	70	-2	4. 70. 30
			EMULATE			~	70	-1	4. 70. 40
+1			EMULATE		'		70	0	4. 70. 50
-4		·	REAL BSP				00		4. 80. 00
-3			EMULATE				80) -4	4. 80. 10
-2			EMULATE			**	80) -3	4. 80. 20
80 -1			EMULATE				80) -2	4. 80. 30
			EMULATE				80) -1	4. 80. 40
+1			EMULATI				80) (4. 80. 50

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
31				REAL BSP		:	· ••	20		4. 31. 00
	-4	SBIDHEP		REAL BSP				00		4. 41. 00
	-3			EMULATE				41	-4	4. 41. 10
41	-2			EMULATE				41	-3	4. 41. 20
	-1	- 		EMULATE			***	41	-2	4. 41. 30
	0			EMULATE				41	-1	4. 41. 40
51				REAL BSP	TM		SBID-REAL BID WHEN (SBID)-(REAL BID)=1	61	0	4. 51. 00
	-4	SB1D+EP		REAL BSP				00		4. 61. 00
	-3			EMULATE				61	-4	4. 61- 10
	-2			EMULATE			##	61	-3	4. 61. 20
61	-1			EMULATE				61	-2	4. 61. 30
	0			EMULATE		†		61	-1	4. 61. 40
1	+1			EMULATE				61	0	4. 61. 50
	-4	WTM SBID+EP		REAL BSP				00		4. 71. 00
	-3			EMULATE				71	-4	4. 71. 10
71	····			EMULATE		T		71	-:	4. 71. 20
1	-1			EMULATE				71	-:	4. 71. 30
				EMULATE				7	-	4. 71. 40
	+1			EMULATI				7	Ц_	4. 71. 50
十	-/	SBID+E	P	REAL BSP				0	0 -	- 4. 81. 00
	-:	3		EMULATI	=		**	8	1 -	4 4. 81. 10
				EMULATI	E			8	1 -	3 4. 81. 20
8	1			EMULAT	E			8	1 -	2 4. 81. 30
		· 0		EMULAT	E			8	1 -	1 4. 81. 40
	+			EMULAT	E			8	1	0 4.81.50

ES E - 32 -		PROCE RE- POSITION	RESTORE	OPERATION		POST - PROCESS					
32 -					BLOCK	SAVE	BID PROCESS	ES	EP	ID	
	十			REAL BSP			••	10		4. 22. 00	
				REAL BSP				22		4. 32. 00	
	-4	SB1D+EP		REAL BSP				00		4. 42. 00	
-	-3			EMULATE				42	-4	4. 42. 10	
42	-2			EMULATE			***************************************	42	-3	4. 42. 20	
	- <u>-</u>			EMULATE				42	-2	4. 42. 30	
-	0			EMULATE			**************************************	42	-1	4. 42. 40	
52		••		REAL BSP	TM		SBID←REAL BID WHEN (SBID)-(REAL BID)=1	62	0	4. 52. 00	
	-4	SB1D+EP		REAL BSP				00		4. 62. 00	
"	-3			EMULATE				62	-4	4. 62. 10	
	-2			EMULATE		†		62	-3	4. 62. 20	
62	<u>-</u> 1			EMULATE				62	-2	4. 62. 30	
-	0			EMULATE				62	-1	4. 62. 40	
-	+1			EMULATE				62	0	4. 62. 50	
	-4	WTM SB1D+EP		REAL BSP				00		4. 72. 00	
\	-3			EMULATE			######################################	72	-4	4. 72. 10	
	-2			EMULATE				72	-3	4. 72. 20	
'-	-1			EMULATE				72	-2	4.72.30	
	0			EMULATE				72	-1	4. 72. 40	
1 1	+1			EMULATE			**	72	0	4. 72. 50	
	-4	SB1D+EF	,	REAL BSP				00		4. 82. 00	
	-3			EMULATE				82	-4	4. 82. 10	
	-2	 		EMULATE		·		82	-3	4. 82. 20	
82	-1			EMULATE				82	-2	4. 82. 30	
	0			EMULATE				82	2 -1	4. 82. 40	
	+1	· 		EMULATE				82	2 (4. 82. 50	

	PRE	- PROCE	SS			POST - PROCESS						
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID		
33				REAL BSP				22		4. 33. 00		
	-4	SB1D+EP		REAL BSP				00		4. 43. 00		
	-3			EMULATE			~~····	43	-4	4. 43. 10		
43	-2			EMULATE				43	-3	4. 43. 20		
	-1			EMULATE				43	-2	4. 43. 30		
	0			EMILATE				43	-1	4. 43. 40		
53				REAL BSP	TM		SBID→REAL BID WHEN (SBID)-(REAL BID)=1	63	0	4. 53. 00		
	-4	SBID+EP		REAL BSP				00		4. 63. 00		
	-3			EMULATE			***************************************	63	-4	4. 63. 10		
	-2			EMULATE				63	-3	4. 63. 20		
63	-1			EMULATE		†		63	-2	4. 63. 30		
	0	ļ		EMULATE			***************************************	63	-1	4. 63. 40		
	+1	ļ		EMULATE				63	0	4. 63. 50		
	-4	WTM SBID+EP		REAL BSP						4. 73. 00		
	-3			EMULATE				73	-4	4. 73. 10		
73		±		EMULATE				73	-3	4. 73. 20		
	-1	†····-		EMULATE				73	-2	4. 73. 30		
ĺ	0			EMULATE				73	-1	4. 73. 40		
	+1			EMULATE				73	0	4. 73. 50		
-	-4	SB I D+EF	·	REAL BSP				00		4. 83. 00		
	-3	·		EMULATE				83	-4	4. 83. 10		
	-2	+		EMULATE		T		83	-3	4. 83. 20		
83	} - -1	.+		EMULATE				83	3 -2	4. 83. 30		
	0			EMULATE		· 		83	3 -1	4. 83. 40		
	+1			EMULATE				8	3 0	4. 83. 50		

00 10 20 30	ROCESS RE- ITION RESTORE IDHEP	OPERATION REAL BSPF REAL BSPF REAL BSPF REAL BSPF REAL BSPF REAL BSPF EMULATE EMULATE	BLOCK	SAVE	POST — PROCESS BID PROCESS	00 00 00 00 00	EP	5. 00. 00 5. 10. 00 5. 20. 00 5. 30. 00
00	ITION RESIDEE	REAL BSPF REAL BSPF REAL BSPF REAL BSPF REAL BSPF EMULATE				00 00 00 00		5. 10. 00 5. 20. 00
10 20 304 SBI -3 402 -1	ID+EP	BSPF REAL BSPF REAL BSPF REAL BSPF REAL BSPF REAL BSPF REAL BSPF EMULATE	 			00		5. 10. 00 5. 20. 00
20 30 -4 SBI -3 40 -2 -1	ID+EP	BSPF REAL BSPF REAL BSPF REAL BSPF REAL BSPF EMULATE				00		5. 20. 00
30 -4 SBI -3 40 -2 -1		REAL BSPF REAL BSPF REAL BSPF EMULATE				00		
-4 SBI -3 40 -2 -1	ID+EP	REAL BSPF REAL BSPF EMULATE EMULATE						5. 30. 00
-4 SBI -3 40 -2 -1	 	REAL BSPF ENULATE ENULATE		ļ	·	00		
-3 40 -2 -1	 	EMULATE EMULATE					l	5. 40. 00
40 -2 -1		EMULATE		1	**************************************	40	-4	5. 40. 10
-1		EMULATE				40	-4	5. 40. 20
		1				40	-4	5. 40. 30
		EMULATE				40	-1	5. 40. 40
50		REAL BSPF	TM		SBID←REAL BID WHEN (SBID)-(REAL BID)=1	60	0	5. 50. 00
-4 SB	ID+EP	REAL BSPF				00		5. 60. 00
-3		EMULATE				60	-4	5. 60. 10
-2		EMULATE				60	-4	5. 60. 20
60 -1		EMULATE			***	60	-4	5. 60. 30
0		EMULATE				60	-1	5. 60. 40
+1		EMULATE				60	0	5. 60. 50
	WTM	REAL BSPF				00		5. 70. 00
-3		EMULATE				70	-4	5. 70. 10
70 -2		EMULATE				70	-4	5. 70. 20
-1		EMULATE				70	-4	5. 70. 30
0		EMULATE				70	-1	5. 70. 40
+1		EMULATE			••	70	0	5. 70. 50
-4 S8	3ID+EP	REAL BSPF				00		5. 80. 00
-3		EMULATE		Ī		80	-4	5. 80. 10
-2		EMULATE				80	-4	5. 80. 20
80 -1		EMULATE				80	-4	5. 80. 30
0		EMULATE				80	-1	
+1		EMULATE		T		80) (5. 80. 50

	PRF	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
31				REAL BSPF	••		· •••	00		5. 31. 00
 	-4	SBIDHEP		REAL BSPF				00		5. 41. 00
	-3			EMULATE			### AAAAAAA	41	-4	5. 41. 10
41	-2			EMULATE				41	-4	5. 41. 20
"	-1			EMULATE				41	-4	5. 41. 30
	0			EMULATE			•••	41	-1	5. 41. 40
51				REAL BSPF	TM		SBID←REAL BID WHEN (SBID)-(REAL BID)=1	61		5. 51. 00
-	-4	SB1D+EP		REAL BSPF				00		5. 61. 00
	-3			EMULATE				61	-4	5. 61. 10
	-2			EMULATE		†		61	-4	5. 61. 20
61	-1			EMULATE				61	-4	5. 61. 30
1	0			EMULATE				61	-1	5. 61. 40
	+1			EMULATE				61	10	5. 61. 50
-	-4	WTM SBID+EF	,	REAL BSPF				00		5. 71. 00
	-3			EMULATE			**	71		
7				EMULATE			***	71	-4	
	-1			EMULATI				71		
				ENULATI	E		##	7		
	+	; 		ENULATI	E			7	1	5. 71. 50
-	1.	4 SBID+E	Р	REAL BSPF				0	0 -	
1	-:	3		EMULAT	E			8	<u></u>	4 5. 81. 10
	_	2		EMULAT	E			8		4 5. 81. 20
8	1 -			EMULAT	E			8	<u></u> -	4 5. 81. 30
		0		EMULAT	E	Ī	••	8		1 5. 81. 40
	}	1		EMULAT	E			8	1	0 5. 81. 50

Γ-	PRF	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK.	SAVE	BID PROCESS	ES	EP	1D
22		POSITION		REAL BSPF				00		5. 22. 00
32				REAL BSPF				00		5. 32. 00
-	-4	SB I D+EP		REAL BSPF				00		5. 42. 00
	-3			EMULATE			### ##################################	42	-4	5. 42. 10
42	-2			EMULATE			**************************************	42	-4	5. 42. 20
-	-1			EMULATE				42	-4	5. 42. 30
	0			EMULATE			4-	42	-1	5. 42. 40
52				REAL BSPF	TM		SBID←REAL BID WHEN (SBID)-(REAL BID)=1	62		5. 52. 00
	-4	SB1D+EP		REAL BSPF				00		5. 62. 00
	-3			EMULATE		 		62	-4	5. 62. 10
	-2			EMULATE		<u> </u>	a#	62	-4	
62	-1			EMULATE]		62	· 	
	0			EMULATE				62	-+	
	+1			EMULATE				62	- 0	5. 62. 50
	-4	WTM SB1D+EF		REAL BSPF				00		
	-3			EMULATE		T		72	·- -	
7:	2 -2			EMULATE		<u> </u>	***	72		
	-1			EMULATE			- -	72		
				EMULATE				72		
	+1			EMULATE				77	+	0 5. 72. 50
		4 SBID+E	Р	REAL BSPF				0		
		3		EMULATI				8	···+···	4 5. 82. 10
٥	2 -	2		EMULATI	Ξ			8		4 5. 82. 20
l°	_	1		EMULAT	E					4 5. 82. 30 1 5. 82. 40
		0		EMULAT	E			8	∤	1 5. 82. 40 0 5. 82. 50
	+	1		EMULAT	E				2	3. 02. 30

	PRE	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
33				REAL BSPF			••	00		5. 33. 00
	-4	SBID+EP		REAL BSPF				00		5. 43. 00
	-3			EMULATE			,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	43	-4	5. 43. 10
43	-2			EMULATE			***************************************	43	-4	5. 43. 20
	-1			EMULATE			**************************************	43	-4	5. 43. 30
	0			EMULATE			***	43	-1	5. 43. 40
53				REAL BSPF	TM		SBID←REAL BID WHEN (SBID)-(REAL BID)=1	63		5. 53. 00
-	-4	SB1D+EP		REAL BSPF				00		5. 63. 00
	-3			EMULATE	*-		••	63	-4	5. 63. 10
	-2			EMULATE			***************************************	63	-4	5. 63. 20
63	-1			EMULATE		<u> </u>		63	-4	5. 63. 30
	0			EMULATE		 	**	63	-1	5. 63. 40
	+1			EMULATE		T		63	0	5. 63. 50
	-4	WTM SBID+EP		REAL BSPF				00		5. 73. 00
	-3			EMULATE				73	-4	5. 73. 10
73	-2			EMULATE				73	-4	5. 73. 20
	-1			EMULATE				73	-4	5. 73. 30
1	0			EMULATE				73	-1	5. 73. 40
	+1			EMULATE				73	0	5. 73. 50
	-4	SB1D+EP		REAL BSPF				00		5. 83. 00
	-3			EMULATE		<u> </u>		83	-4	5. 83. 10
02	-2			EMULATE]	***************************************	83	-4	5. 83. 20
83	-1			EMULATE		<u> </u>		83	-4	5. 83. 30
	0			EMULATE		<u> </u>		83	-1	5. 83. 40
	+1			EMULATE			••	83	0	5. 83. 50

	PRF	- PROCE	ss				POST	T - PROCESS			PF	OCESS
ES	EΡ	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE		BID PROCESS	ES	EP		ID
=		rosition		DEAL	TM		SBID+	-REAL BID	10		6.	00.00
00				REAL SP	OTHERS			± 0	00		6	00. 01
					EOF				22		6	10.00
10				REAL	TM		SBID-	-REAL BID	10		6	. 10. 01
10		1		SP	OTHERS			••	00	<u> </u>	6	. 10. 02
		 			EOF				31		<u> </u>	. 20. 00
20		<u> </u>		REAL	TM		SBID	-REAL BID	10		. 6	. 20. 01
20				. SP	OTHERS			•	00	<u> </u> -	1	5. 20. 02
	-			REAL	TM		SBID- WHEN	←REAL BID (REAL BID)-(SBID)=3	40			30. 00
30				SP	OTHERS			**	00	1	.] !	6. 30. 01
	+-		 	EMULATE			1		40	-	3	6. 40. 00
,	-4	.+		EMULATE				φ σ	40	-	2	6. 40. 10
	-3			EMULATE				± 7	40) -	1	6. 40. 20
	-2			ENULATE					41		0	6. 40. 30
40	-1				EOF			p. 4	2	2 -	-	6. 40. 40
				··· REAL	TM		SBIC)←REAL BID I (REAL BID)-(SBID)=1	5	0	-	6. 40. 41
	()		SP SP	OTHER	s		pp. 2000 100 110 100 100 100 100 100 100 10	0	0		6. 40. 42
					EOD				8	0 ·	F1	6. 40. 43
-	+				EOF	-			2	2		6. 50. 00
5	۔ اہ	_		REAL	TM		SBI	D-REAL BID	1	0		6. 50. 01
1	֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓֓			SP	OTHE	RS			(0		6. 50. 02
\vdash	+	4	 	EMULAT	E	<u> </u>			16	0	-3	6. 60. 00
	ļ	3		EMULAT	E	-				30	-2	6. 60. 10
		-2				-	-			50	-1	6. 60. 2
		-1		EMULA			-	##		50	0	6. 60. 3
16	50	0		EMULA.			-			60	+1	6. 60. 4
					EO	F -	- S8	ID+1		22	 	6. 60. 5
		+1 SBID+	FP	REAL	. Т	i † -	- SB	ID←REAL BID		10		6. 60. 5
1	- '	' 35,5.	_	SP	отн	RS ·	-	**************************************		00		6. 60. 5

	DDF	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE-	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
		POSITION		EMULATE			••	70	-3	6. 70. 00
	-4			EMULATE			######################################	70	-2	6. 70. 10
	-3	 		ENLATE			**************************************	70	-1	6. 70. 20
	-2			ENULATE			**************************************	70	0	6. 70. 30
70	-1			EMULATE			**************************************	70	+1	6. 70. 40
ļ	0			CHULAIC	EOF		SBID+1	22		6. 70. 50
				REAL	TM	ļ <u>.</u>	SBID←REAL BID	10		6. 70. 51
	+1	MTM		SP	OTHERS	 		00		6. 70. 52
-	—		 	CARN ATE	UINCIS	 		80	-3	6. 80. 00 .
1	-4			EMULATE				80	-2	6. 80. 10
	-3			EMULATE				80	-1	6. 80. 20
	-2			EMULATE					. 	
80	-1			EMULATE				80	0	
	0			EMULATE				80	+1	6. 80. 40
	+1			REAL SP	EOD			00		6. 80. 50

	PRE	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
				REAL	TM		SBID→REAL BID WHEN (REAL BID)-(SBID)=3	41	0	6. 31. 00
31				SP	OTHERS		- -	00		6. 31. 01
	-4			EMULATE	•			41	-3	6. 41. 00
	-3			EMULATE				41	-2	6. 41. 10
	-2			EMULATE			wa	41	-1	6. 41. 20
	-1			EMULATE				41	0	6. 41. 30
41					EOF		***	22		6. 41. 40
		· 		REAL	TM		SBID⊷REAL BID WHEN (REAL BID)-(SBID)=1	51		6. 41. 41
	0			SP	OTHERS			00		6. 41. 42
					EOD		**	81	+1	6. 41. 43
					EOF			22		6. 51. 00
51				REAL SP	TM		SBID-REAL BID	10		6. 51. 01
		į			OTHERS			00	<u> </u>	6. 51. 02
	-4			EMULATE				61	-3	6. 61. 00
	-3			EMULATE			<u> </u>	61	-2	6. 61. 10
	-2			EMULATE				61	-1	6. 61. 20
	-1			EMULATE				61	0	6. 61. 30
61	0			EMULATE		,		61	+1	6. 61. 40
			· • • • • • • • • • • • • • • • • • • •	-	EOF		SBID+1	22		6. 61. 50
	+1	SBID+EP		REAL SP	TM	†	SBID-REAL BID	10		6. 61. 51
				эг 	OTHERS			00		6. 61. 52

		777005	-00				POST - PROCESS			PROCESS
	PRE	- PROCE		OPERATION	2: 00%	CUT	BID PROCESS	ES	EP	ID
ES	타	RE- POSITION	RESTORE		BLOCK	SAVE		4-	-	6. 71. 00
	-4			EMULATE			##	71	-3	
	-3			EMULATE				71	-2	6. 71. 10
	-2			EMULATE				71	-1	6. 71. 20
				EMULATE			~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	71	0	6. 71. 30
71	-1		ļ <u></u>					71	+1	6. 71. 40
	0			EMULATE		 	\$BID+1	22		6. 71. 50
					EOF			10	<u> </u>	6. 71. 51
	+1	WTW		REAL SP	TM		SBID←REAL BID	-		
			1		OTHERS			00	<u> </u>	6. 71. 52
-	-4		 	EMULATE			••	81	-3	6. 81. 00
		ļ		EMULATE				81	-2	6. 81. 10
1	-3					ļ	. · · · · · · · · · · · · · · · · · · ·	81	-1	6. 81. 20
81	-2			EMULATE		.}	###	81	1 0	6. 81. 30
81	-1			EMULATE		<u></u>		81		••
	0			EMULATE						
	+1			REAL SP	EOD			00)	6. 81. 50

	PRE	- PROCE	SS			_	POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
					EOF		••	33		6. 22. 00
22				REAL SP	TM		SBID+-REAL BID	10		6. 22. 01
			•	3	OTHERS		######################################	00		6. 22. 02
				REAL	TM		SBID←REAL BID · WHEN (REAL BID)-(SBID)=3	42	0	6. 32. 00
32				SP	OTHERS			00		6. 32. 01
	-4			EMULATE				42	-3	6. 42. 00
	-3			EMULATE				42	-2	6. 42. 10
1	-2			EMULATE				42	-1	6. 42. 20
	-1			EMULATE				42	0	6. 42. 30
42	·····				EOF			22		6. 42. 40
			ļ	REAL	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)=1	52		6. 42. 41
	0		-	SP	OTHERS			00		6. 42. 42
					EOD	†		82	+1	6. 42. 43
	1				EOF			22		6. 52. 00
52				REAL SP	TM		SBID←REAL BID	10		6. 52. 01
				"	OTHERS			00	<u> </u>	6. 52. 02
	-4			EMULATE				62	-3	6. 62. 00
	-3			EMULATE				62	-2	6. 62. 10
	-2			EMULATE				62	-1	6. 62. 20
	-1			EMULATE				62	0	6. 62. 30
62	0			EMULATE				62	+1	6. 62. 40
	····	†·····			EOF]	SB1D+1	22		6. 62. 50
Ì	+1	SBID+EP		REAL SP	TM		SBIDREAL BID	10		6. 62. 51
					OTHERS		***************************************	00		6. 62. 52

	PRE	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID .
 	-4			EMULATE			••	72	-3	6. 72. 00
	-3			EMULATE				72	-2	6. 72. 10
	-2			EMULATE				72	-1	6. 72. 20
	-1			ENULATE				72	0	6. 72. 30
72	0			EMULATE			**************************************	72	+1	6. 72. 40
		ļ			EOF		SB1D+1	22		6. 72. 50
		WITH		REAL	TM		SBID-REAL BID	10		6. 72. 51
	+1	W 100	-	SP	OTHERS			00		6. 72. 52
-	-4		 	EMULATE				82	-3	6. 82. 00
	-3		·	EMULATE			·	82	-2	6. 82. 10
	·····			EMULATE				82	-1	6. 82. 20
82	-2	ļ <u>-</u> -		EMULATE				82	0	6. 82. 30
	-1			EMULATE			_ =	82	+1	6. 82. 40
	+1			REAL SP	EOD			00		6. 82. 50

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
=				REAL	TM		SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	6. 33. 00
33				SP	OTHERS		***	00		6. 33. 01
一	-4	+-		EMULATE			(m.m)	43	-3	6. 43. 00
	-3			EMULATE			~~	43	-2	6. 43. 10
	-2			ENULATE				43	-1	6. 43. 20
	-1			EMULATE			##	43	0	6. 43. 30
43	······				EOF		m m	22		6. 43. 40
				REAL	TM		SBID⊶REAL BID WHEN (REAL BID)-(SBID)=1	53		6. 43. 41
	0			SP	OTHERS			00		6. 43. 42
				į	EOD			83	+1	6. 43. 43
	 		1		EOF			22		6. 53. 00
53				REAL SP	TM		SBID-REAL BID	10		6. 53. 01
		ļ		~	OTHERS			00	<u> </u>	6. 53. 02
	-4			ENULATE				63	-3	6. 63. 00
	-3			EMULATE		<u> </u>		63	-2	6. 63. 10
	-2			EMULATE		Ī	##	63	-1	6. 63. 20
	-1			EMULATE		I	##	63	0	6. 63. 30
63	0			EMULATE		I - -		63	+1	6. 63. 40
		†			EOF	<u> </u>	SBID+1	22		6. 63. 50
	+1	SBID+EP		REAL SP	TM		SBID+REAL BID	10		6. 63. 51
					OTHERS			00		6. 63. 52

	DDE	- PROCE	SS				POST - PROCESS			PROCESS
		RE-	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	1D
ES	EP	POSITION	RESTURE					73	-3	6. 73. 00
	-4			EMULATE				73	-2	6. 73. 10
	-3			ENULATE				73	-1	6. 73. 20
	-2			EMULATE						6. 73. 30
	-1			EMULATE				73	ļ	
73	0			EMULATE			**	73	+1	6. 73. 40
	}				EOF		SBID+1	22		6. 73. 50
		MTM		REAL	TM		SBID+ REAL BID	10		6. 73. 51
	+1	NIM		SP	OTHERS		—	00		6. 73. 52
_	↓_			CHR ATE		+		83	-3	6. 83. 00
1	-4			EMULATE		<u> </u>		83	-2	6. 83. 10
	-3			EMULATE				83	-1	6. 83. 20
	-2			EMULATE				83	.	6. 83. 30
83	-1			EMULATE				83		
				EMULATE			a-			
	+1	·		REAL SP	EOD			0	<u> </u>	6. 83. 50

	PRE	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
	-	rosman		REAL	TM		SBID-REAL BID	10		7. 00. 00
00				FSPF	OTHERS			00		7. 00. 01
						,	SBID←REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 10. 00
10				REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 10. 01
					OTHERS		***	00		7. 10. 02
-	-						SBID←REAL BID WHEN (REAL BID)-(SBID)=3	41	0	7. 20. 00
20				REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 20. 01
					OTHERS			00		7. 20. 02
-	-						SBID←REAL BID WHEN (REAL BID)-(SBID)=3	40	0	7. 30. 00
30				REAL FSPF	TM		SBID-REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 30. 01
				1	OTHERS			00		7. 30. 02
-	-4		 	ENULATE				40	-3	7. 40. 00
	-3			EMULATE				40	0	7. 40. 10
	-2	÷		EMULATE				40	0	7. 40. 20
	-1			EMULATE				40	0	7. 40. 30
40							SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 40. 40
40		-			TM.		SBID-REAL BID WHEN (REAL BID)-(SBID)=1	50		7. 40. 41
	0			REAL FSPF			SBID→REAL BID WHEN (REAL BID)-(SBID)≠1AND3	10		7. 40. 42
					OTHER	s		00		7. 40. 43
		1			EOD			80	+1	7. 40. 44
十	1	1					SBIDREAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 50. 00
50	,			REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 50. 01
				ŀ	OTHER	s		00)	7. 50. 02

	PRF	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	10
	-4			EMULATE				60	-3	7. 60. 00
	-3			EMULATE			# B	60	0	7. 60. 10
	-2			ENULATE			. en. + + e e e e e e e e e e e e e e e e e	60	0	7. 60. 20
	-1			FWILATE			. e 200 (60	0	7. 60. 30
60			<u></u>	ENULATE				60	+1	7. 60. 40
	0				************		SBID⊷REAL BID- WHEN (REAL BID)-(SBID)=3	43	0	7. 60. 50
	+1	SB1D+EP		REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 60. 51
					OTHERS		***	00		7. 60. 52
-	-4			EMULATE				70	-3	7. 70. 00
	-3			EMULATE				70	0	7. 70. 10
	-2			EMULATE				70	0	7. 70. 20
	-1	ļ		EMULATE				70	0	7. 70. 30
70	l	ļ		FMULATE				70	+1	7. 70. 40
							SBID+-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 70. 50
	+1	WTM		REAL FSPF	TM		SBID→REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 70. 51
	1				OTHERS			00		7. 70. 52
-	-4	 		EMULATE		·		80	-3	7. 80. 00
	-3			EMULATE		T		80	0	7. 80. 10
	-2	.+		EMULATE				80	0	7. 80. 20
80	1 -	· 		EMULATE				80	(7. 80. 30
	:			EMULATE				80	+1	7. 80. 40
	+1			REAL FSPF	EOD			00)	7. 80. 50

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
			·				SBID⊶REAL BID WHEN (REAL BID)-(SBID)=3	41	0	7. 31. 00
31				REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 31. 01
					OTHERS			00		7. 31. 02
 	-4			ENULATE			**	41	-3	7. 41. 00
	-3			EMULATE				41	0	7. 41. 10
	-2			EMULATE				41	0	7. 41. 20
	-1			EMULATE				41	0	7. 41. 30
41							SBID←REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 41. 40
"					TM		SBID⊶REAL BID WHEN (REAL BID)-(SBID)=1	51		7. 41. 41
	0			REAL FSPF			SBID←REAL BID WHEN (REAL BID)-(SBID)≠1AND3	10		7. 41. 42
		1			OTHERS	<u> </u>		00		7. 41. 43
					EOD			81	+1	7. 41. 44
-							SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 51. 00
51		ļ <u></u>		REAL FSPF	TM		SBIDREAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 51. 01
	į	•			OTHERS			00		7. 51. 02
-	-4			EMULATE				61	-3	7. 61. 00
	-3			EMULATE		†		61	. 0	7. 61. 10
	-2			EMULATE				61	0	7. 61. 20
	-1			EMULATE				61	0	7. 61. 30
61	0			EMULATE				61	+1	7. 61. 40
							SBID+-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 61. 50
	+1	SB1D+EP		REAL FSPF	TM		SBID+-REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 61. 51
					OTHERS		•	00		7. 61. 52

	PRE	- PROCE	ss				POST - PROCESS			PROCESS
ES	ΕP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
=	-4			EMULATE				71	-3	7. 71. 00
İ	-3			EMULATE			###	71	0	7. 71. 10
	-2			EMULATE				71	0	7. 71. 20
	-1			EMULATE				71	0	7. 71. 30
71	0			ENULATE			**************************************	71	+1	7. 71. 40
					.,		SBID⊷REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 71. 50
	+1	MTM		REAL. FSPF	TM		SBID⊷REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 71. 51
	1				OTHERS		***	00		7. 71. 52
-	-4			EMULATE				81	-3	7. 81. 00
1	-3			EMULATE				81	0	7. 81. 10
	-2			EMULATE				81	0	7. 81. 20
81	-1			ENULATE		†···		81	0	7. 81. 30
	0	ļ		EMULATE				81	+1	7. 81. 40
	+1			REAL FSPF	EOD			00		7. 81. 50

							PRO0500		$\neg \neg$	
	PRE	- PROCE	SS	0050171011			POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
							SBIDREAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 22. 00
22				REAL FSPF	TM		SBID-REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 22. 01
					OTHERS			00		7. 22. 02
							SBID←REAL BID WHEN (REAL BID)-(SBID)=3	42	0	7. 32. 00
32				REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 32. 01
				'	OTHERS			00	-	7. 32. 02
-	-4			ENULATE				42	-3	7. 42. 00
	-3			EMULATE			##	42	0	7. 42. 10
	-2			EMULATE				42	0	7. 42. 20
	-1			ENULATE				42	0	7. 42. 30
	-						SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 42. 40
42					TM		SBID-REAL BID WHEN (REAL BID)-(SBID)=1	52		7. 42. 41
	0			REAL FSPF			SBID←REAL BID WHEN (REAL BID)-(SBID)≠1AND3	10		7. 42. 42
		1			OTHERS		——————————————————————————————————————	00		7. 42. 43
					EOD			82	+1	7. 42. 44
	+	1	1	 			SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 52. 00
52				REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 52. 01
					OTHER	s		00		7. 52. 02
-	1-4			EMULATE				62	-3	7. 62. 00
	-3			EMULATE				62	0	7. 62. 10
	-2			EMULATE				62	2 0	7. 62. 20
	-1			EMULATE				62	2 0	7. 62. 30
62	,			EMULATE				62	2 +1	7. 62. 40
							SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	3 0	7. 62. 50
	1	1 SBID+E	P	REAL FSPF	MT		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10	0 -	7. 62. 51
				FOFF	OTHE	s	HI half (1 Co Co Co Co Co Co Co Co Co Co Co Co Co	0	0 -	7. 62. 52

	PRE	- PROCE	SS				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
	-4			EMULATE				72	-3	7. 72. 00
	-3			EMULATE			##	72	0	7. 72. 10
	-2			ENULATE				72	0	7. 72. 20
	-1			EMULATE				72	0	7. 72. 30
72	0			EMULATE				72	+1	7. 72. 40
	}						SBID←REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 72. 50
		WTM		REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 72. 51
					OTHERS			00		7. 72. 52
-	-4			EMULATE				82	-3	7. 82. 00
	-3			ENULATE				82	0	7. 82. 10
	-2			EMULATE			<u> </u>	82	0	7. 82. 20
82	-1			EMULATE			<u> </u>	82	0	7. 82. 30
	0			EMULATE				82	+1	7. 82. 40
	+1			REAL FSPF	EOD			00		7. 82. 50

	PRE	- PROCE	SS				POST - PROCESS			PROCESS					
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	1D					
						••	SBID←REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 33. 00					
33	33		REAL TNI FSPF		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 33. 01							
				·	OTHERS		**************************************	00		7. 33. 02					
	-4			EMULATE				43	-3	7. 43. 00					
	-3			EMULATE				43	0	7. 43. 10					
	-2	~		EMULATE				43	0	7. 43. 20					
	-1			ENULATE				43	0	7. 43. 30					
43	0				************		SBID→REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 43. 40					
					TM		SBID⊷REAL BID WHEN (REAL BID)-(SBID)=1	53		7. 43. 41					
				REAL FSPF			SBID←REAL BID WHEN (REAL BID)-(SBID)≠1AND3	10		7. 43. 42					
]			•		•						OTHERS			00	
					EOD			83	+1	7. 43. 44					
一							SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 53. 00					
53				REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 53. 01					
ļ					OTHERS			00		7. 53. 02					
	-4			EMULATE				63	-3	7- 63- 00					
	-3			EMULATE				63	. 0	7. 63. 10					
	-2			EMULATE				63	0	7. 63. 20					
	-1			EMULATE				63	0	7. 63. 30					
63	0			EMULATE				63	+1	7. 63. 40					
	······	***************************************					SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	7. 63. 50					
	+1	SB1D+EP		REAL FSPF	TM		SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 63. 51					
					OTHERS			00		7. 63. 52					

	PRF	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
-	-4			EMULATE			••	73	-3	7. 73. 00
l	-3			EMULATE				73	0	7. 73. 10
1	ļ			ENULATE			**************************************	73	0	7. 73. 20
l	-2			EMULATE			~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	73	0	7. 73. 30
73	-1					·		73	+1	7. 73. 40
13	0			EMULATE			SBID-REAL BID	43	0	7. 73. 50
	+1	WTM		1	TNA		WHEN (REAL BID) - (SBID) = 3	43	ļ <u>.</u>	
1				REAL FSPF			SBID←REAL BID WHEN (REAL BID)-(SBID)≠3	10		7. 73. 51
					OTHERS			00		7. 73. 52
-	-4		 	EMULATE				83	-3	7. 83. 00
	-3			EMULATE				83	0	7. 83. 10
			·	EMULATE		 		83	0	7. 83. 20
83	-2			ENULATE				83	0	7. 83. 30
	-1	ļ				 	##	83	+1	7. 83. 40
	0	<u></u>		REAL		- 		00	†==	7. 83. 50
	+1			FSPF	EOD			00	1	1. 03. 30

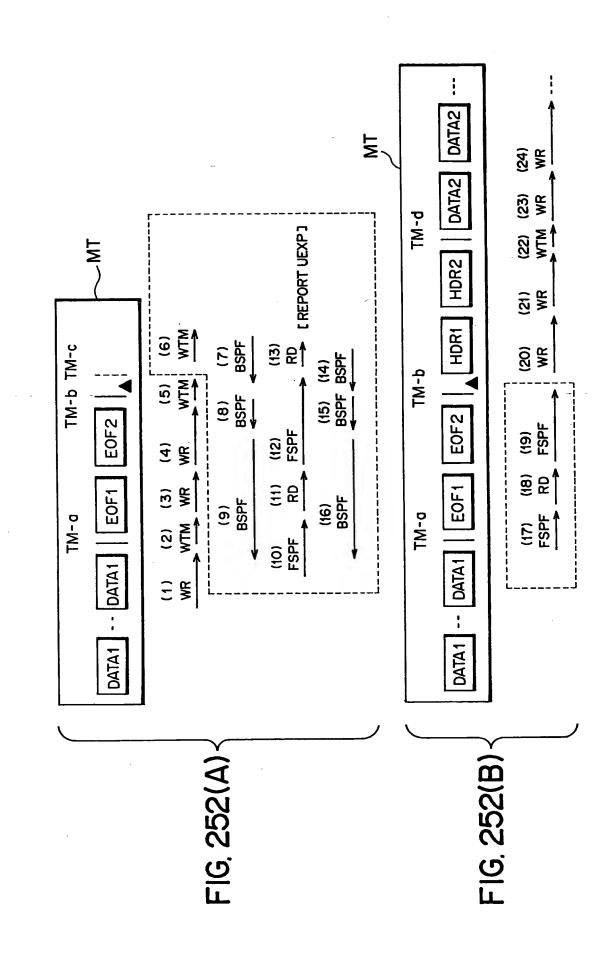
	PRE - PROCESS						POST - PROCESS			PROCESS	
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID	
00				REAL WTM			SBIDREAL BID	10		8. 00. 00	
10				REAL.			SBID-REAL BID	10	1	8. 10. 00	
20				REAL			SBID-REAL BID	10		8. 20. 00	
30	-			WTM REAL			SBID-REAL BID WHEN (REAL BID)-(SBID)=3	40	0	8. 30. 00	
	-4	SBID+EP		WIM REAL			SBID-REAL BID	10		8. 40. 00	
	-3	SB1D+EP		REAL			SBID+-REAL BID	10		8. 40. 10	
40	-2	SBID+EP		WTM REAL			SBID+REAL BID	10		8. 40. 20	
	-1			WTM ENULATE				40	0	8. 40. 30	
	0			EMULATE			**************************************	70	+1	8. 40. 40	
50				REAL WTM			SBID-REAL BID	10		8. 50. 00	
-	-4	SB1D+EP		REAL			SBID+-REAL BID	10		8. 60. 00	
	-3	SBID+EP		REAL WITM			SBID+REAL BID	10		8. 60. 10	
	-2	SBID+EP		REAL			SBID-REAL BID	10		8. 60. 20	
60	-1	SBID+EP		REAL. WTM			SBID-REAL BID	10		8. 60. 30	
	0			EMULATE				60	+1	8. 60. 40	
	+1	SB1D+EP		REAL WTM			SBID←REAL BID	10		8. 60. 50	
	-4	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 70. 00	
	-3	SBID+EP		REAL WTM			SBID←REAL BID	10		8. 70. 10	
	-2	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 70. 20	
70	-1	SB1D+EP		REAL WTM			SBID←REAL BID	10		8. 70. 30	
	0			EMULATE		Ī		70	+1	8. 70. 40	
	+1	WTM		REAL.			SBID←REAL BID	10		8. 70. 50	
	-4	SB1D+EP		REAL WTM			SBID←REAL BID	10		8. 80. 00	
	-3	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 80. 10	
80	-2	SB1D+EP		REAL WTM			SBID←REAL BID	10)	8. 80. 20	
00	-1	SBIDHEP		REAL WTM			SBIDREAL BID	10			
	0		<u></u>	EMULATE				80) +1	8. 80. 40	
	+1	WTM		REAL WTM			SBID+REAL BID	10)	8. 80. 50	

	PRE	- PROCE	ss				POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	타	ID
31				REAL. WITM			SBIDREAL BID WHEN (REAL BID)-(SBID)=3	41	0	8. 31. 00
	-4	SB I D+EP		REAL WTM			SBID-REAL BID	10		8- 41- 00
	-3	SB1D+EP		REAL WITM			SBID←REAL BID	10		8. 41. 10
41	-2	SB I D+EP		REAL WTM			SBID←REAL BID	10		8. 41. 20
	-1			EMULATE				41	0	8. 41. 30
	0			EMULATE			••	71	+1	8.41.40
51				REAL WTN			SBID-REAL BID	10		8. 51. 00
	-4	SB I D+EP		REAL WTM			SBID-REAL BID	10		8. 61. 00
	-3	SB1D+EP		REAL WTM			SBID←REAL BID	10		8. 61. 10
	-2	SB I D+EP		REAL WTM			SBID←REAL BID	10		8. 61. 20
61	-1	SB1D+EP		REAL WITH			SBID-REAL BID	10		8. 61. 30
	0			EMULATE				61	+1	8. 61. 40
	+1	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 61. 50
	-4	SB1D+EP		REAL WTM			SBID+-REAL BID	10		8. 71. 00
	-3	SB1D+EP		REAL. WTM			SBID+-REAL BID	10		8. 71. 10
7.	-2	SB1D+EP		REAL WTM			SBID-REAL BID	10	<u> </u>	8. 71. 20
71	-1	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 71. 30
	0			EMULATE				. 71	+1	8. 71. 40
	+1	WTM		REAL WTM			SBID←REAL BID	10		8. 71. 50
	-4	SB I D+EP		REAL WTM			SBID-REAL BID	10		8. 81. 00
	-3	SB1D+EP		REAL WTM			SBID←REAL BID	10		8. 81. 10
0.4	-2	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 81. 20
81	-1	SB1D+EP		REAL WTM		-	SBID←REAL BID	10		8. 81. 30
	0			EMULATE				81	+1	8. 81. 40
	+1	WTW		REAL WTM			SBID←REAL BID	10		8. 81. 50

	PRE - PROCESS						POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
22				REAL WTM			SBID-REAL BID	10		8. 22. 00
32				REAL WTM			SBID-REAL BID WHEN (REAL BID)-(SBID)=3	42	0	8. 32. 00
_	-4	SB I D+EP		REAL WITM			SBID-REAL BID	10		8. 42. 00
	-3	SB1D+EP		REAL WITM			SBID+-REAL BID	10		8. 42. 10
42	-2	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 42. 20
	-1			EMULATE			***	42	0	8. 42. 30
	0			EMULATE				72	+1	8. 42. 40
52				REAL WTM			SBID-REAL BID	10		8. 52. 00
	-4	SBIDHEP		REAL WTM			SBID-REAL BID	10		8. 62. 00
	-3	SB1D+EP		REAL WITM			SBID←REAL BID	10		8. 62. 10
	-2	SB I D+EP		REAL WTM			SBID+REAL BID	10		8. 62. 20
62	-1	SB1D+EP		REAL WTM			SBID+-REAL BID	10		8. 62. 30
1	0			EMULATE				62	+1	8. 62. 40
	+1	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 62. 50
	-4	SB1D+EP		REAL WTM			SBID+REAL BID	10		8. 72. 00
	-3	SB1D+EP		REAL.			SBID-REAL BID	10		8. 72. 10
70	-2	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 72. 20
72	-1	SB1D+EP		REAL. WTM			SBID←REAL BID	10		8. 72. 30
	0			EMULATE				72	+1	8. 72. 40
	+1	MTM		REAL. WTM			SBID-REAL BID	10		8. 72. 50
	-4	SBIDHEP		REAL. WTM			SBID-REAL BID	10		8. 82. 00
	-3	SB1D+EP		REAL WTM			SBID←REAL BID	10		8. 82. 10
	-2	SBIDHEP		REAL WTM			SBID-REAL BID	10		8. 82. 20
82	-1	SBID+EP		REAL WITM			SBID←REAL BID	10		8. 82. 30
3	0			EMULATE	T	1		82	+1	8. 82. 40
	+1	MIM		REAL WTM			SBID-REAL BID	10		8. 82. 50

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	PRE - PROCESS					·	POST - PROCESS			PROCESS
ES	EP	RE- POSITION	RESTORE	OPERATION	BLOCK	SAVE	BID PROCESS	ES	EP	ID
33				REAL WIN			SBID-REAL BID WHEN (REAL BID)-(SBID)=3	43	0	8. 33. 00
	-4	SBIDHEP		REAL			SBID-REAL BID	10		8. 43. 00
	-3	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 43. 10
43	-2	SB1D+EP		REAL WITM			SBID-REAL BID	10		8. 43. 20
	-1			ENULATE			######################################	43	0	8. 43. 30
	0			EMULATE			••	73	+1	8. 43. 40
53				REAL_ WTM			SBID+-REAL BID	10		8. 53. 00
	-4	SB1D+EP		REAL WTM			SBID←REAL BID	10		8. 63. 00
	-3	SB1D+EP		REAL WTM			SBID+REAL BID	10		8. 63. 10
	-2	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 63. 20
63	-1	SBID+EP		REAL WTM			SBID-REAL BID	10		8. 63. 30
	0			EMULATE				63	+1	8. 63. 40
	+1	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 63. 50
\vdash	-4	SB1D+EP		REAL WTM			SBID-REAL BID	10		8. 73. 00
	-3	SB I D+EP		REAL WIM			SBID-REAL BID	10		8. 73. 10
	-2	SB1D+EP		REAL.			SBID-REAL BID	10		8. 73. 20
73	-1	SB1D+EF		REAL. WTM			SBID←REAL BID	10) 	8. 73. 30
	0			EMULATE				73	+1	8. 73. 40
	+1	МТМ		REAL WTM			SBID←REAL BID	10)	8. 73. 50
	-4	SB1D+EF		REAL WTM			SBID←REAL BID	10)	8. 83. 00
	-3	SBID+E		REAL.			SBID-REAL BID	10)	8. 83. 10
	-2	SBID+E		REAL WTM			SBID-REAL BID	11	0 -	8. 83. 20
83	3 -1	SBID+E		REAL WTM			SBID+-REAL BID	11	0 -	8. 83. 30
	1-7			EMULATE				8	3 +	1 8. 83. 40
	+	MTM		REAL WTM			SBID←REAL BID	1	0 -	- 8. 83. 50



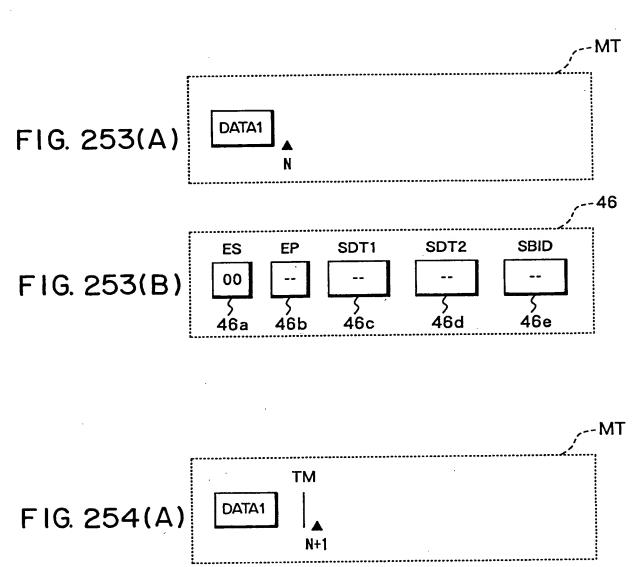
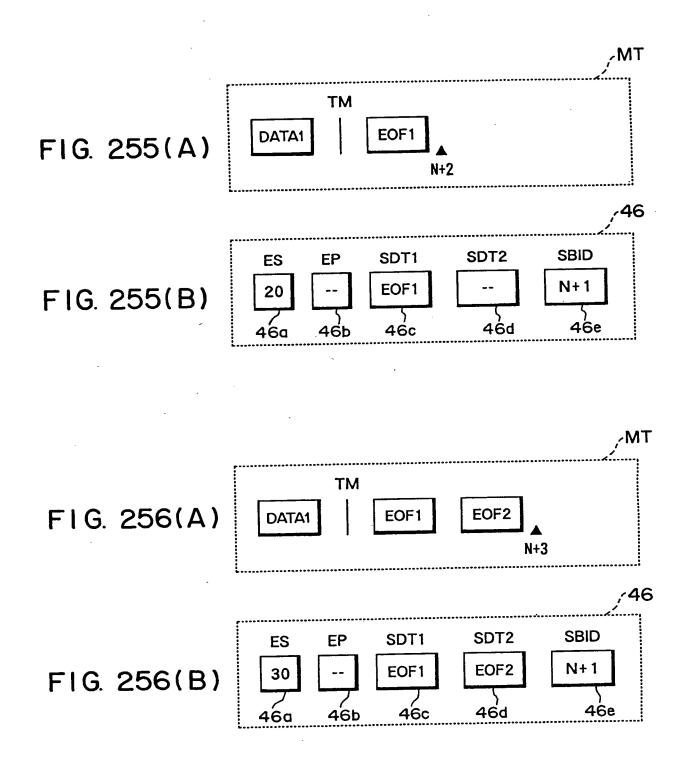


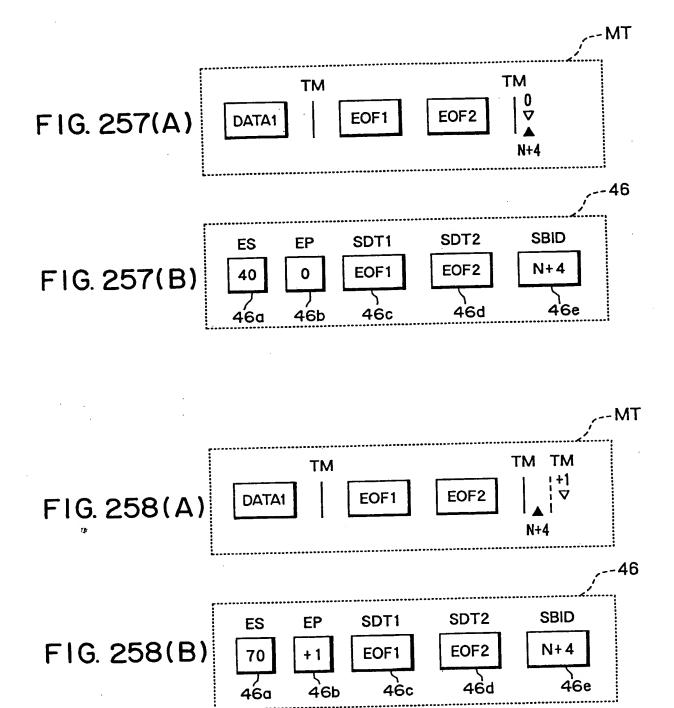
FIG. 254(B)

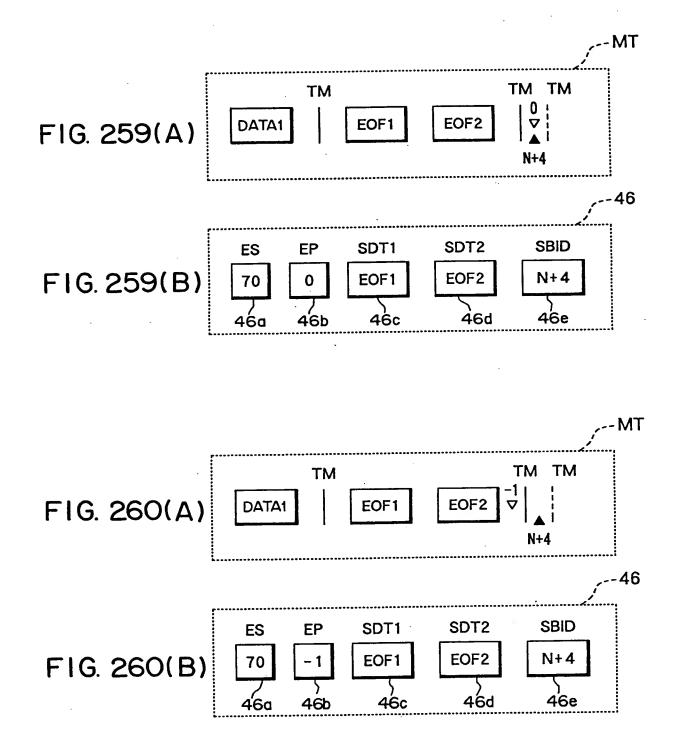
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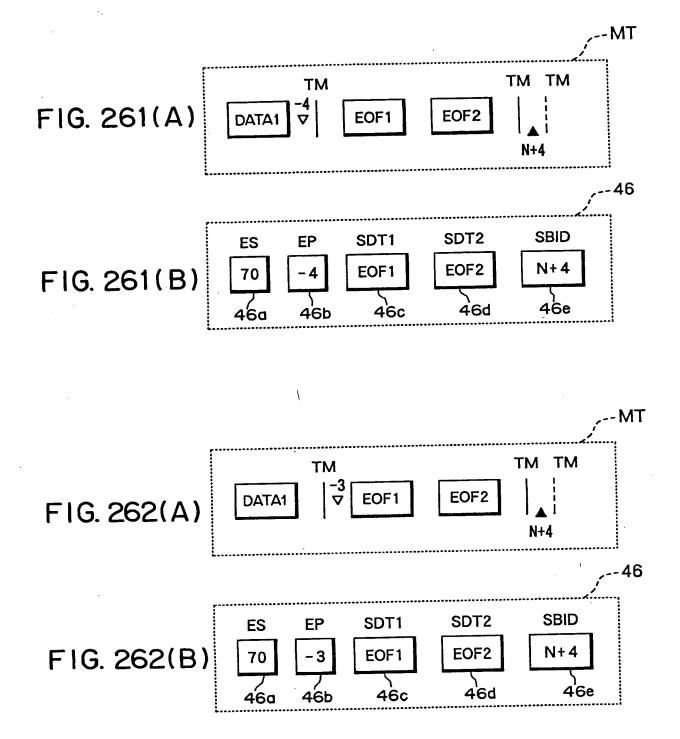
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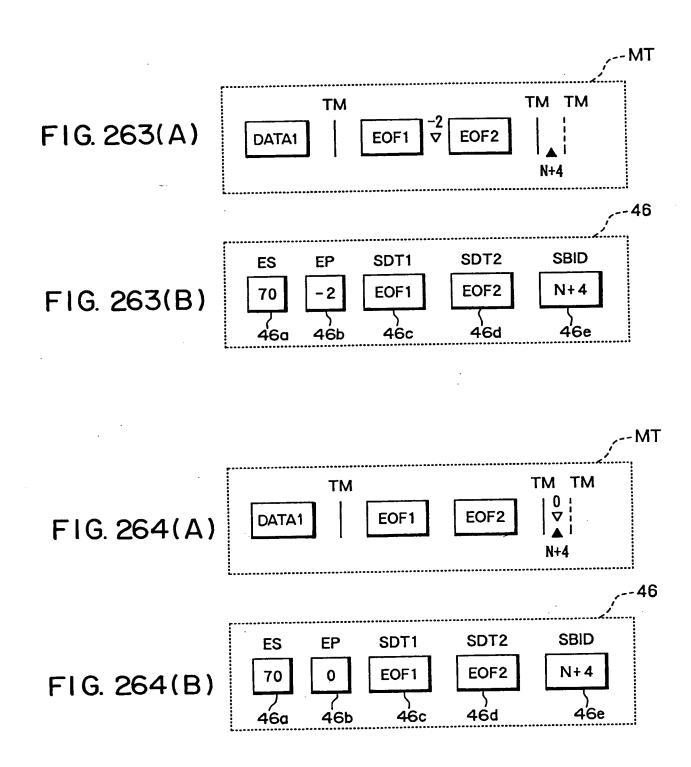
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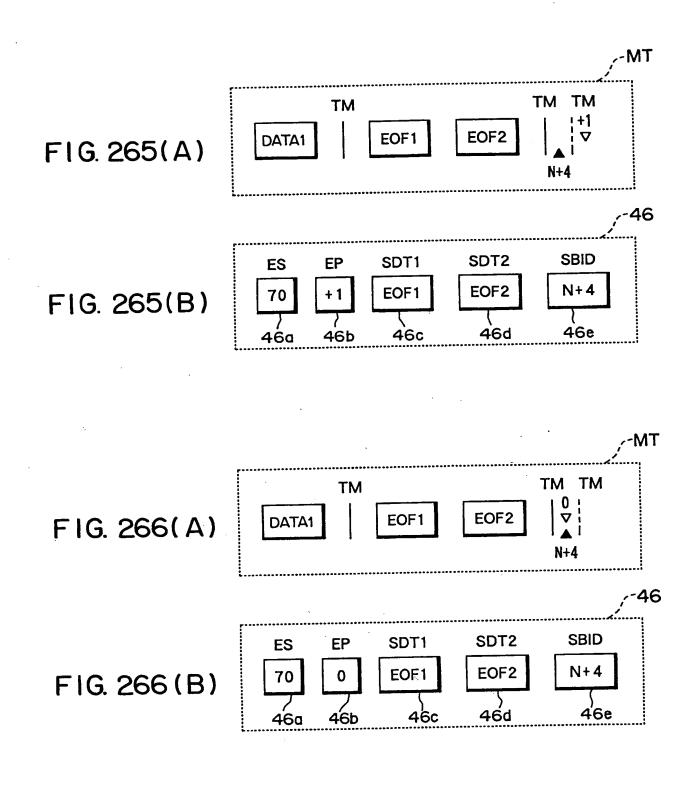


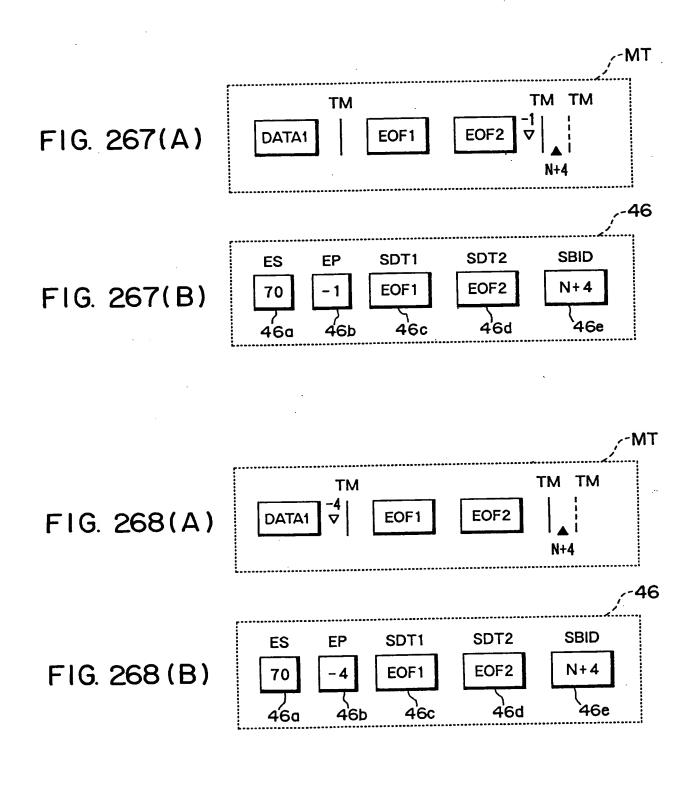


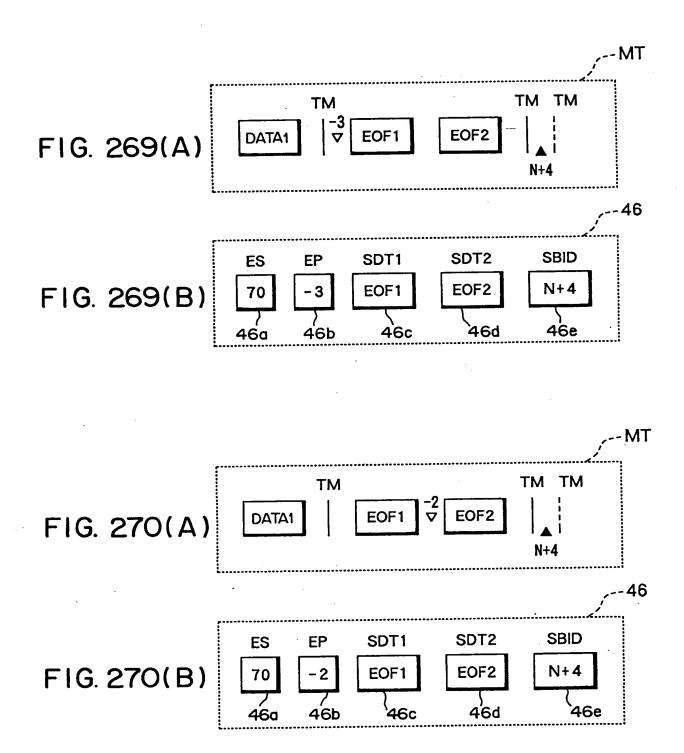


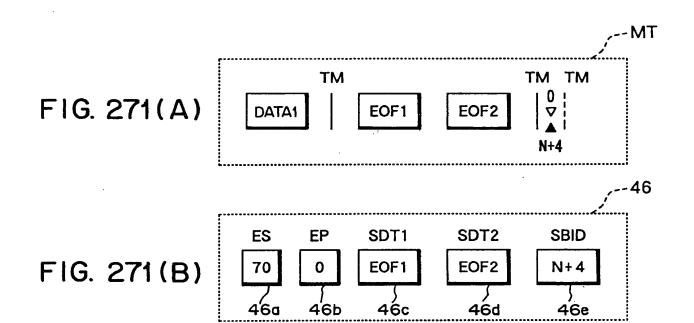


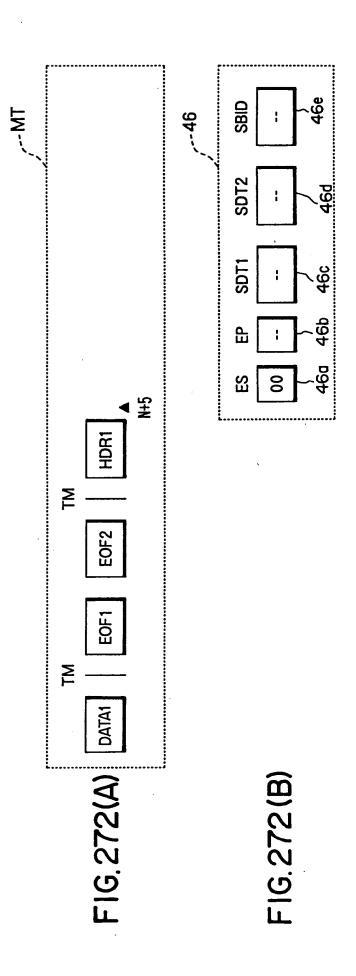












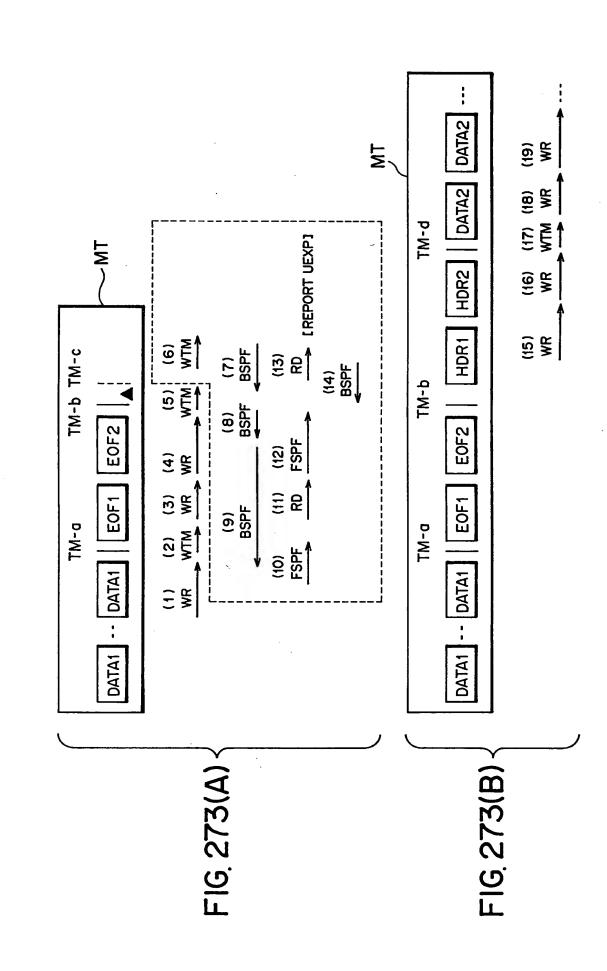


FIG.274

COMMAND	WITHOUT EMULATION	WITH EMULATION	REMARKS
WR	0. 000731	0. 000731	(1)
WTM	0. 713581	0. 713581	(2)
WR	0. 003502	0. 003502	(3)
WR	0. 000641	0. 000641	(4)
WTM	0. 596851	0. 596851	(5)
WTM	0. 298012	0. 000500	(6)
BSPF	0. 460880	0. 000500	(7)
NOP	0. 000548	0. 000500	
LDSP	0. 000521	0. 000521	
LDSP	0. 000469	0. 000469	
LDSP	0. 000493	0. 000493	
NOP	0. 000524	0. 000524	
SNIO	0. 002945	0. 002945	
RDVC	0. 000580	0. 000580	
NOP	0. 000529	0. 000529	
SNS	0. 002731	0. 002731	
LDSP	0. 000420	0. 000420	
BSPF	0. 000583	0. 000500	(8)
BSPF	0. 291417	0. 000500	(9)
FSPF	0. 291342	0. 000500	(10)
RD	0. 531535	0. 000500	(11)
FSPF	1. 410659	0. 000500	(12)
RD	0. 581191	0. 000500	(13)
BSPF	0. 000606	0. 000500	(14)
BSPF	0. 455999	0. 000500	(15)
BSPF	0. 293099	0. 000500	(16)
FSPF	0. 290969	0. 000500	(17)
RD	0. 531411	0. 000500	(18)
FSPF	1. 414089	0. 000500	(19)
NOP	0. 290595	0. 000500	(00)
WR	0. 000615	0. 000615	(20)
WR	0. 000615	0. 000615	(21)
WTM	0. 639278	0. 339278	(22)
WR	0. 003324	0. 003324	(23)
WR	0. 000641	0. 000641	(24)
TOTAL	9. 111926	1. 981191	SECOND

FIG.275

COMMAND	WITHOUT EMULATION	WITH EMULATION	REMARKS
WR	0. 000700	0. 000700	(1)
WTM	0. 843145	0. 541334	(2)
WR	0. 011607	0. 011771	(3)
WR	0. 005543	0. 005291	(4)
WTM	0. 615312	0. 310921	(5)
WTM	0. 308603	0. 005847	(6)
BSPF	0. 482427	0. 005927	(7)
NOP	0. 003681	0. 003799	
LDSP	0. 006880	0. 006852	
LDSP	0. 006785	0. 006929	
NOP	0. 003463	0. 003419	
NOP	0. 003500	0. 003514	·
SNS	0. 011085	0. 011727	
LDSP	0. 006662	0. 006966	
BSPF	0. 300570	0. 005836	(8)
BSPF	0. 305450	0. 005990	(9)
FSPF	0. 474757	0. 005934	(10)
RD	0. 097280	0. 008346	(11)
FSPF	0. 205869	0. 005892	(12)
RD	0. 306587	0. 004664	(13)
BSPF	0. 476655	0. 005798	(14)
LDSP	0. 006960	0. 006918	
WR	0. 004667	0. 004711	(15)
WR	0. 005425	0. 005041	(16)
WTM	1. 833579	0. 308303	(17)
WR	0. 003300	0. 003300	(18)
WR	0. 000600	0. 000600	(19)
TOTAL	6. 331092	1. 296330	SECOND

45:DATA BUFFER **→** RD EOF1 EOF2

FIG. 276(B) PHYSICAL IMAGE

FIG. 276(C)